

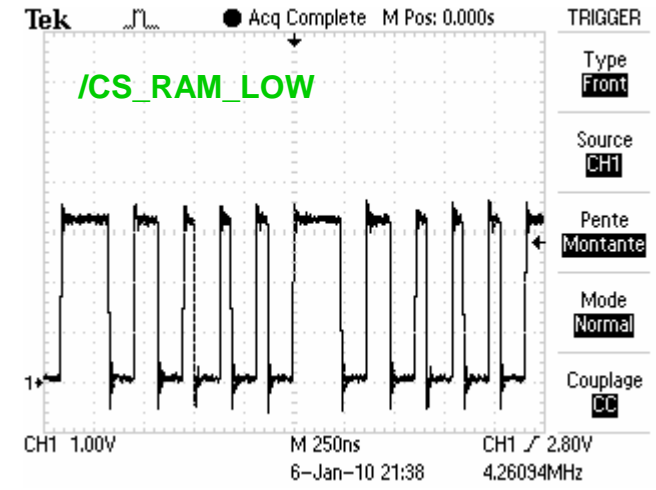
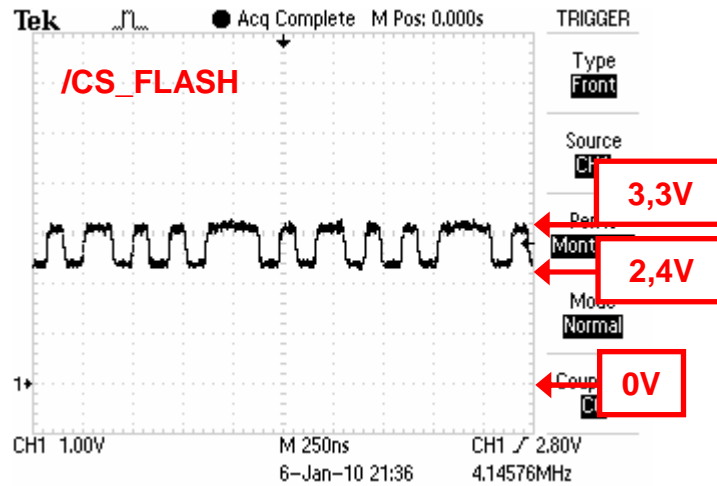
# Bilan des investigations

M7 & M8: mémoires FLASH AT29LV040A 20TC

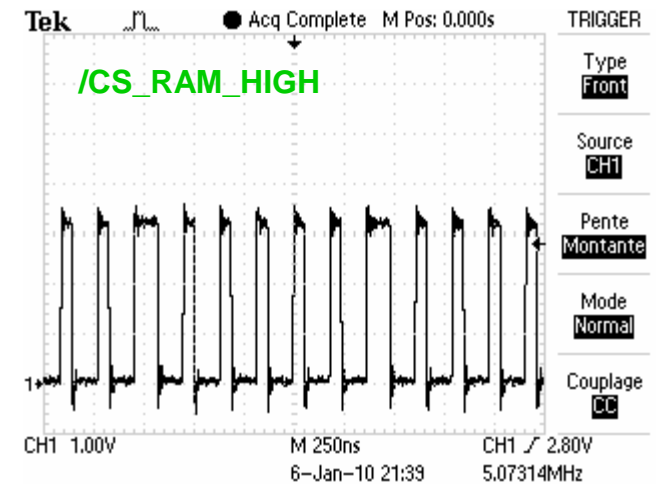
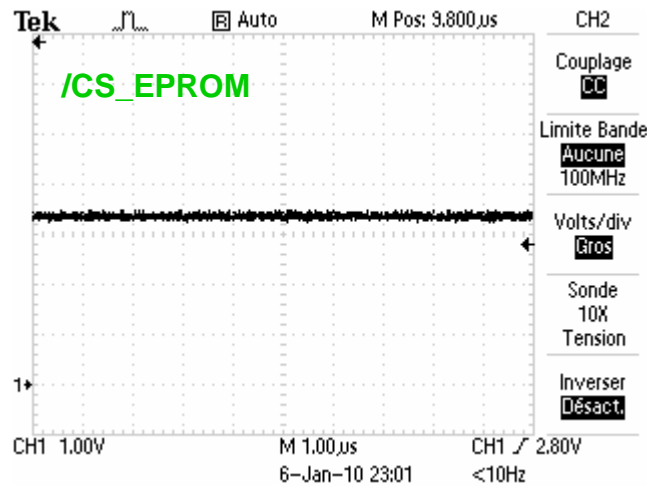
	<i>M7 &amp; M8 présentes</i>	<i>/CS_FLASH levés sur M7 &amp; M8</i>	<i>M7 présente &amp; M8 dessoudée</i>	<i>M7 &amp; M8 dessoudées</i>
<i>/CS_FLASH</i>	<b>KO</b> (slide 2)	<b>OK</b> (signal en sortie du $\mu$ p) CS de M7: OK CS de M8: KO (slide 6)	<b>OK</b> (slide 7)	<b>OK</b> (slide 8)
<i>R/W</i>	<b>OK</b> (slide 3)	<b>OK</b>	<b>OK</b>	<b>OK</b> (slide 8)
<i>/RD</i>	<b>OK</b> (slide 3)	<b>OK</b>	<b>OK</b> (slide 7)	<b>OK</b> (slide 8)
<i>/WR</i>	<b>KO</b> (slide 3)	<b>KO</b>	<b>KO</b> (slide 7)	<b>OK</b> (slide 8)
<i>A18</i>	<b>KO</b> (slide 4)	<b>KO</b>	<b>KO</b> (slide 7)	<b>OK</b> (slide 8)
<i>A19</i>	<b>KO</b> (slide 4)	<b>KO</b>	<b>KO</b> (slide 7)	<b>OK</b> (slide 8)
<i>D8</i>	<b>KO</b> (slide 5)	<b>KO</b>	<b>OK</b>	<b>OK</b>

# CS

⇒ Mémoires FLASH M7 & M8 connectées.

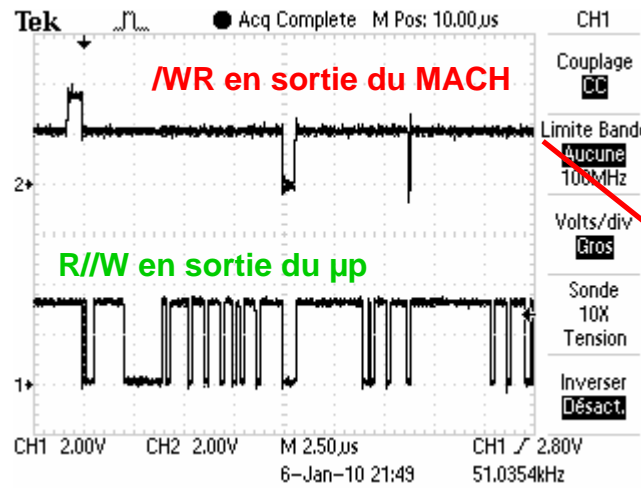
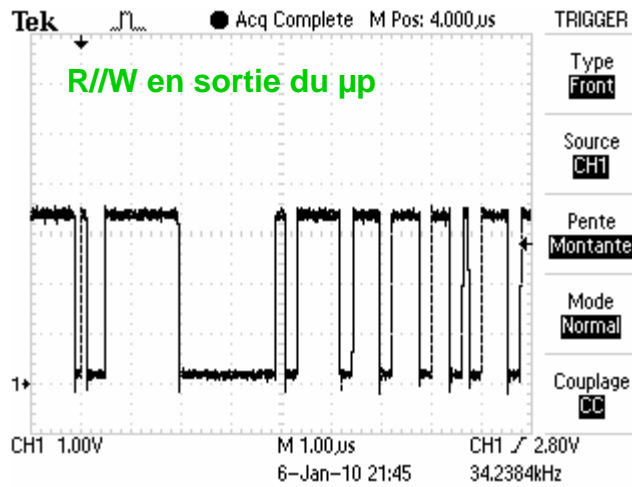


⇒ Le signal /CS des mémoires FLASH bagote entre 2,4 et 3,3V même après que le programme de secours se soit lancé.

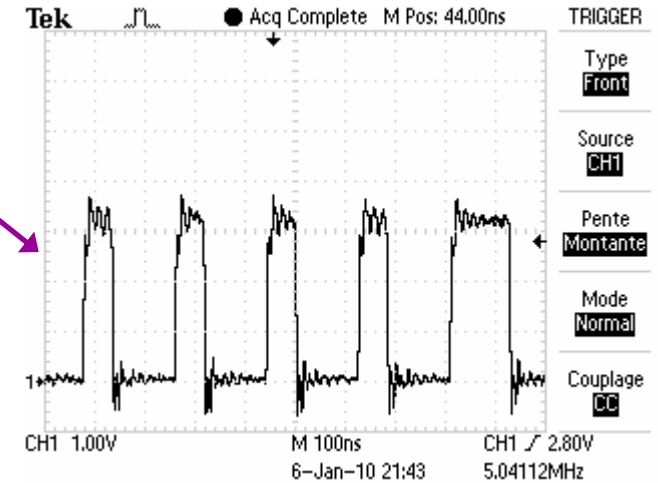
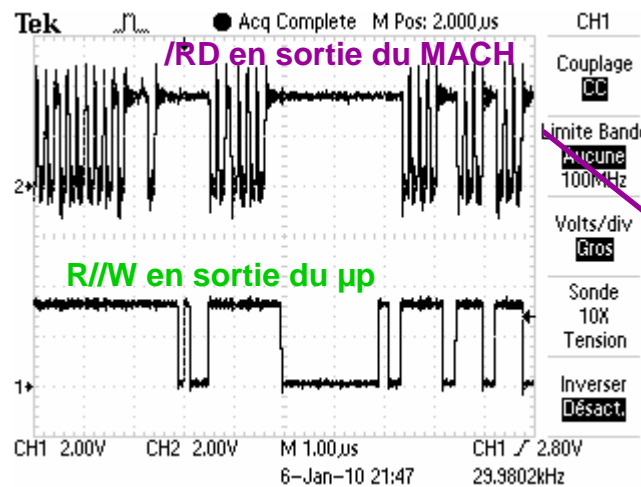
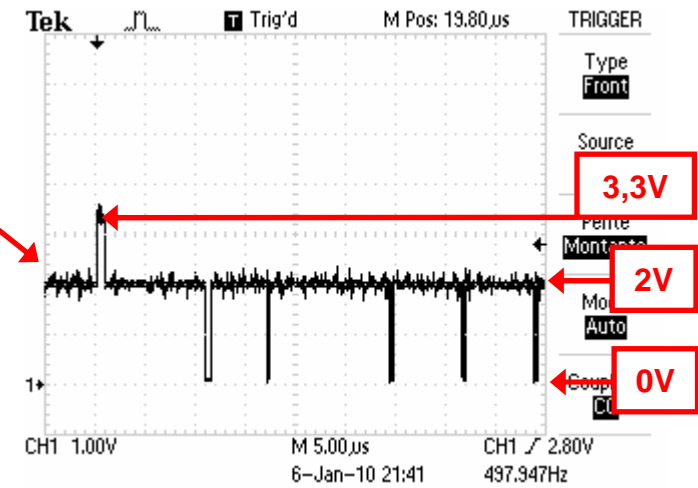


# Signaux READ & WRITE

→ Mémoires FLASH M7 & M8 connectées.

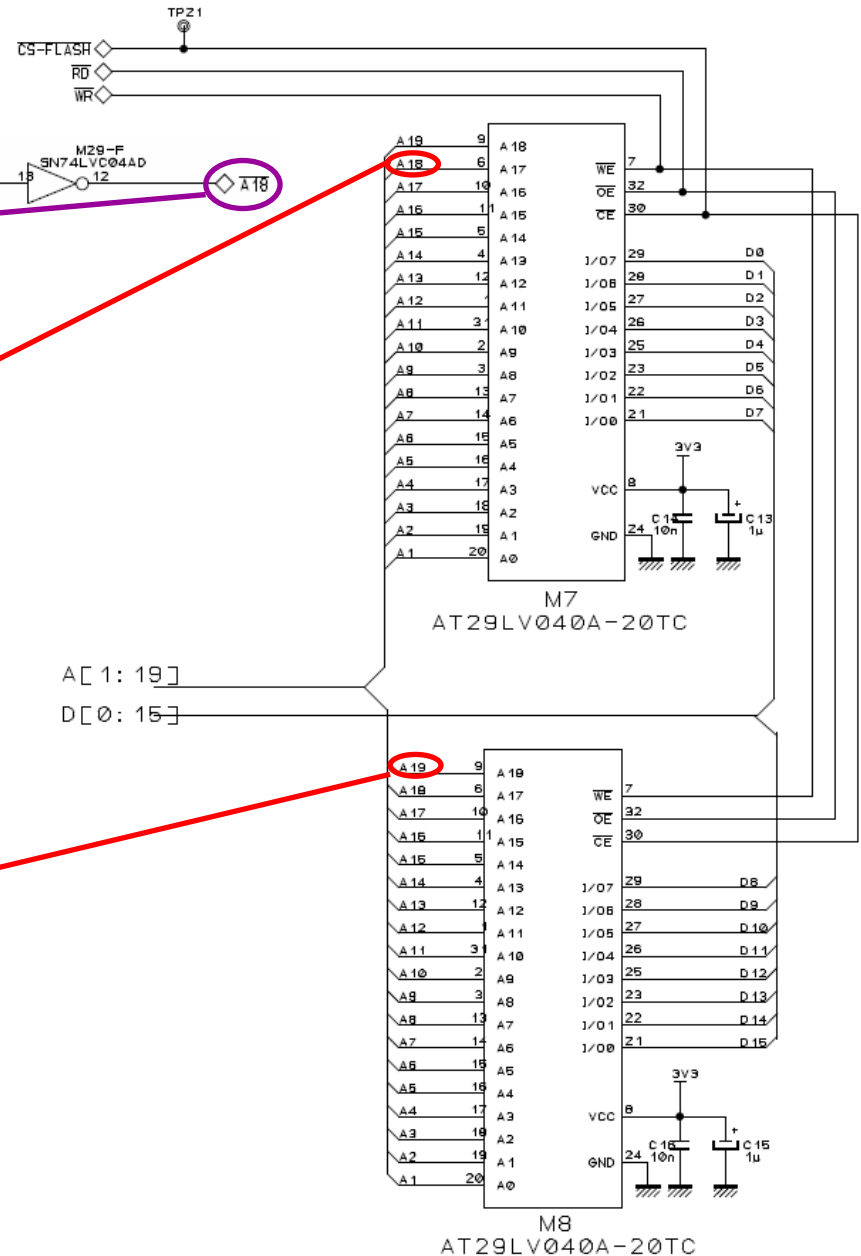
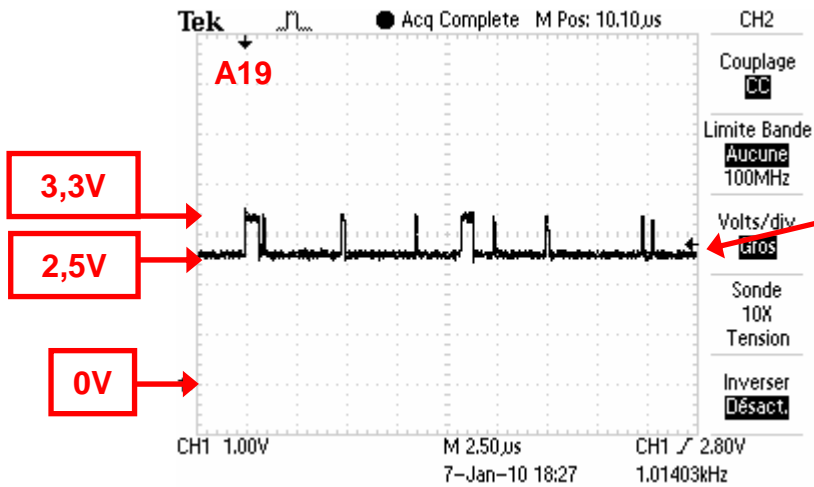
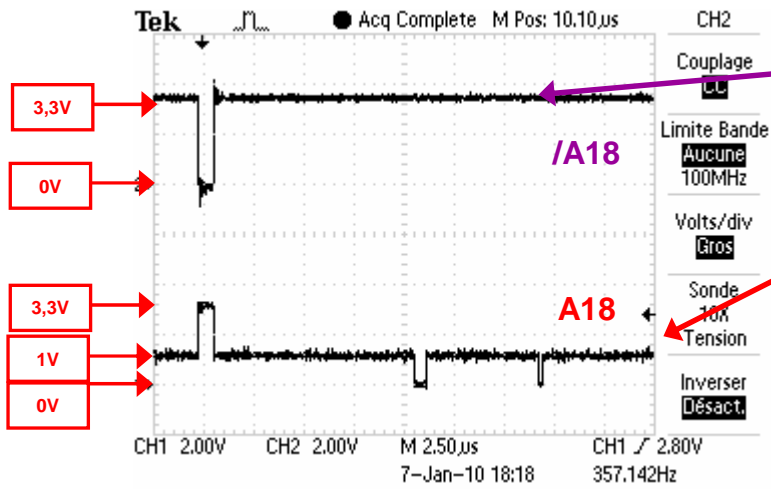


Le signal /WR évolue entre 2 et 3,3V.



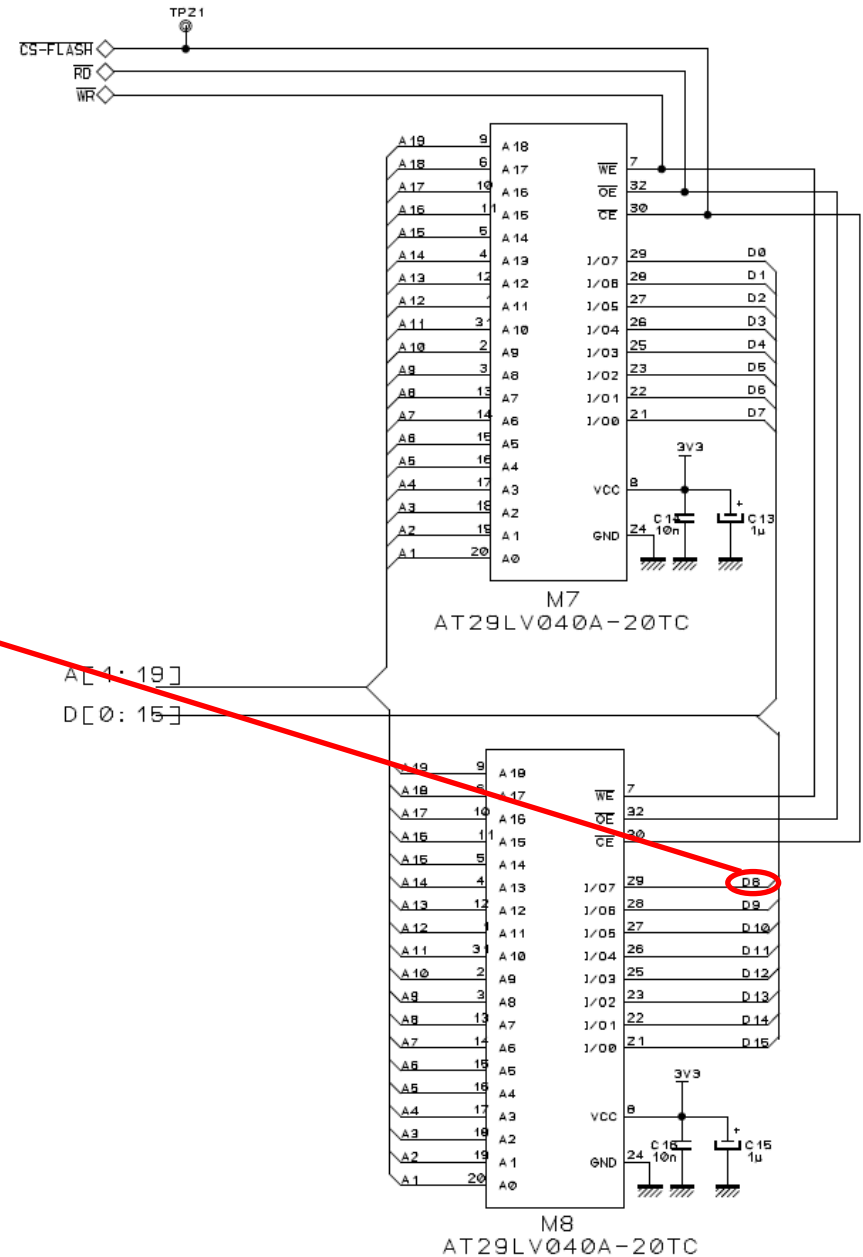
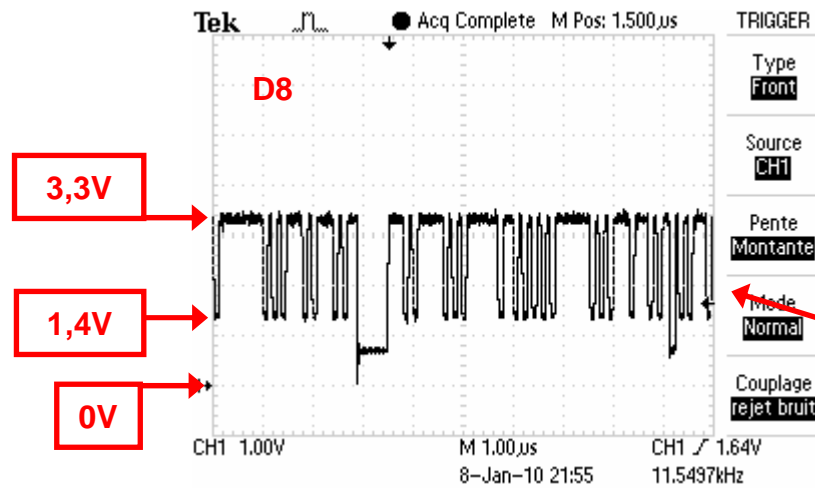
# Signaux d'adresses

→ Mémoires FLASH M7 & M8 connectées.



# Signaux de données

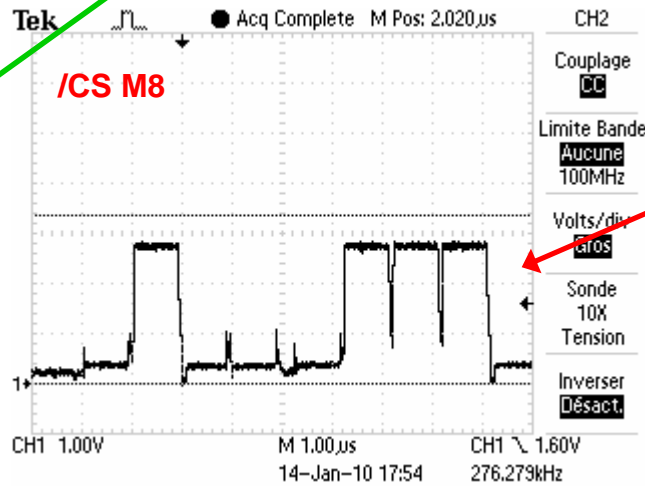
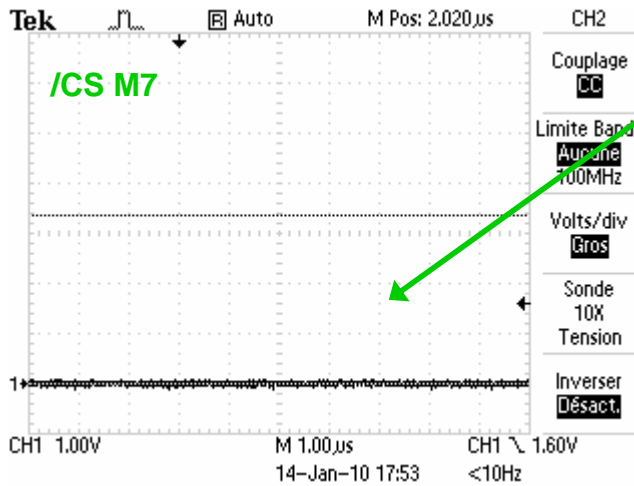
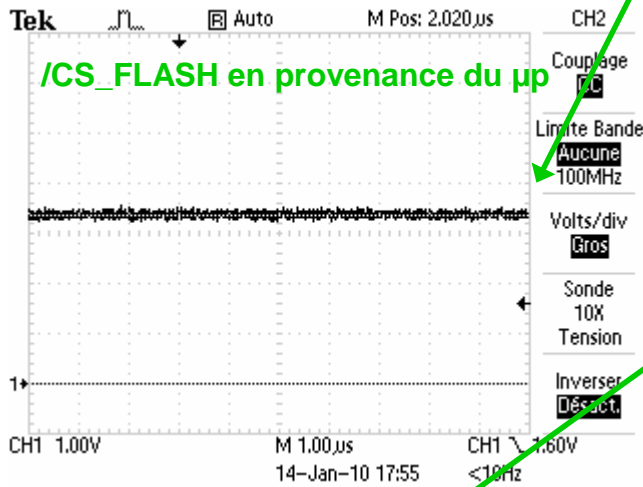
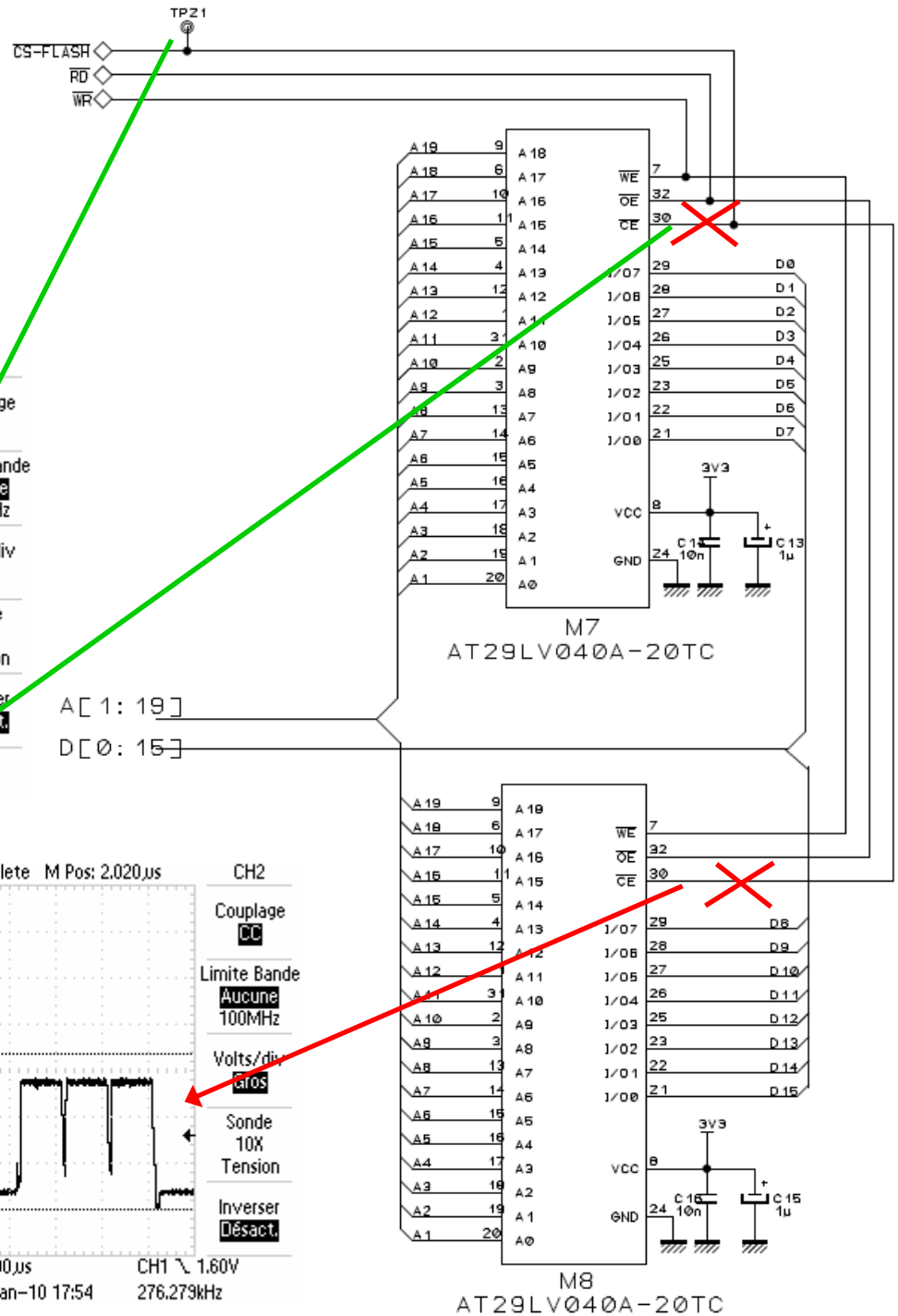
⇒ Mémoires FLASH M7 & M8 connectées.



# Déconnection du CS

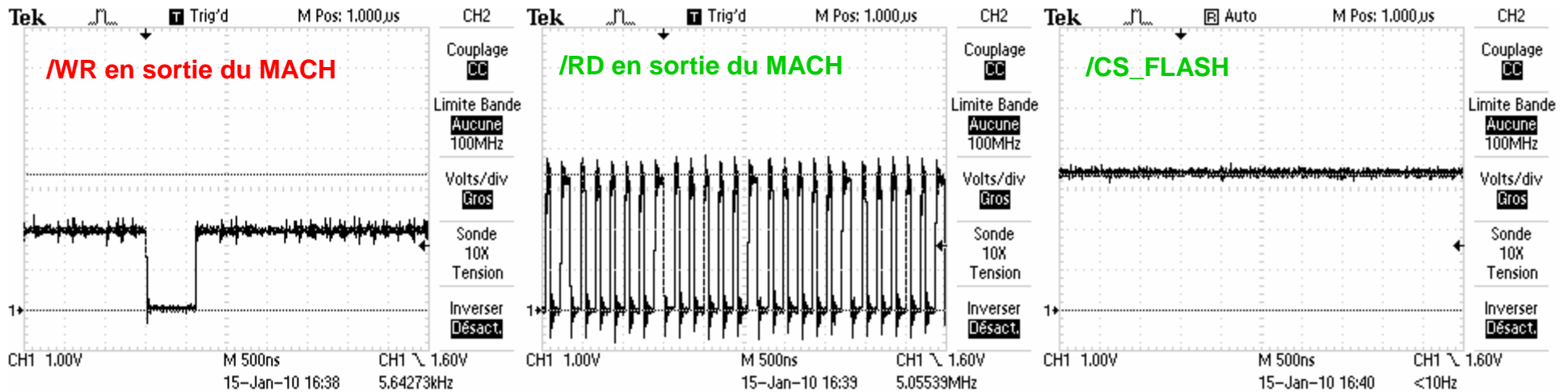
→ CS des Mémoires FLASH M7 & M8 déconnectées.

Les pins de CS des mémoires flash ont été levées.



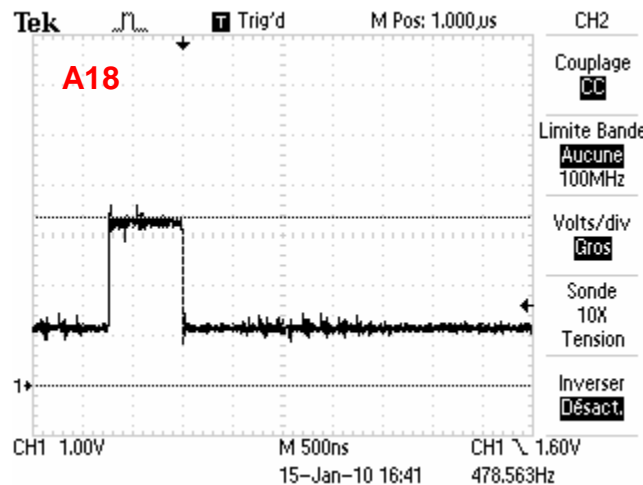
# M8 dessoudée

⇒ Mémoires FLASH M7 connectée & M8 dessoudée.

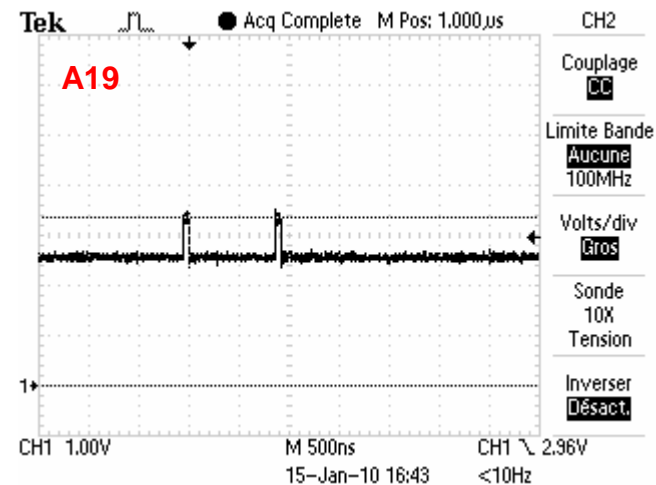


⇒ **/WR évolue toujours entre 0V et 2V.**

⇒ **Le signal /CS\_FLASH est à présent à un niveau correct.**



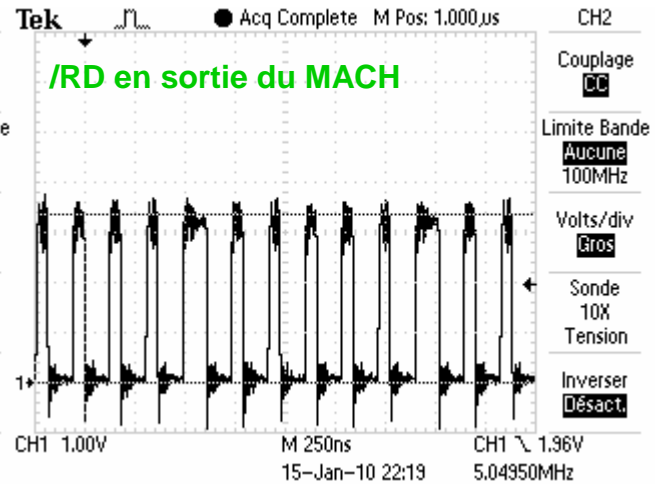
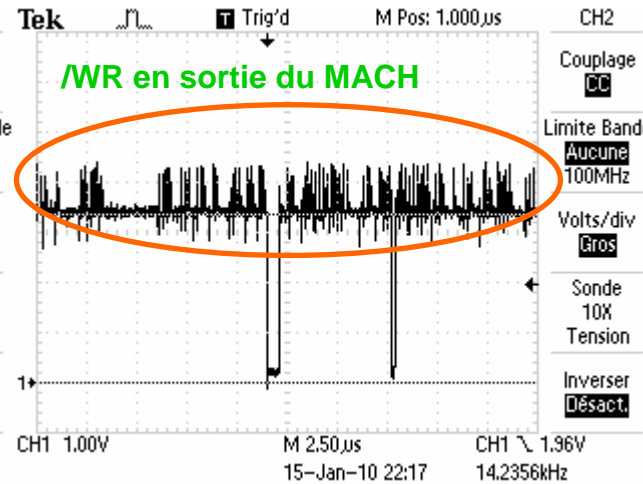
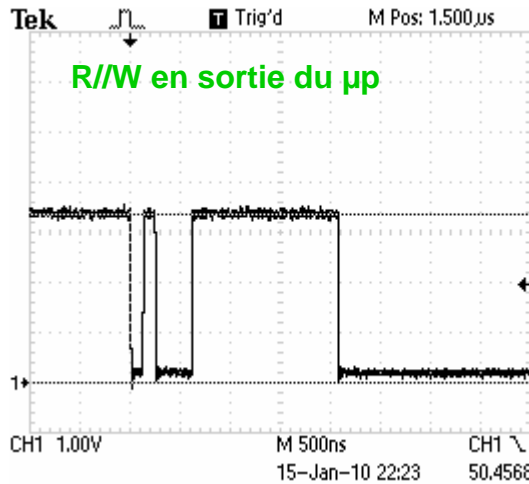
⇒ **A18 évolue toujours entre 1V et 3,3V.**



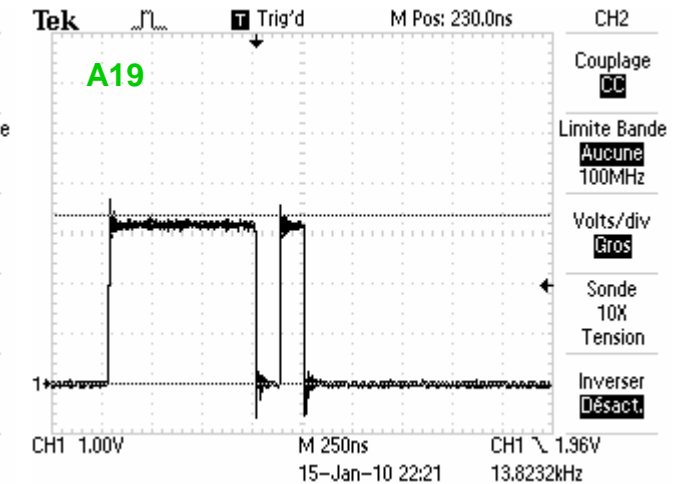
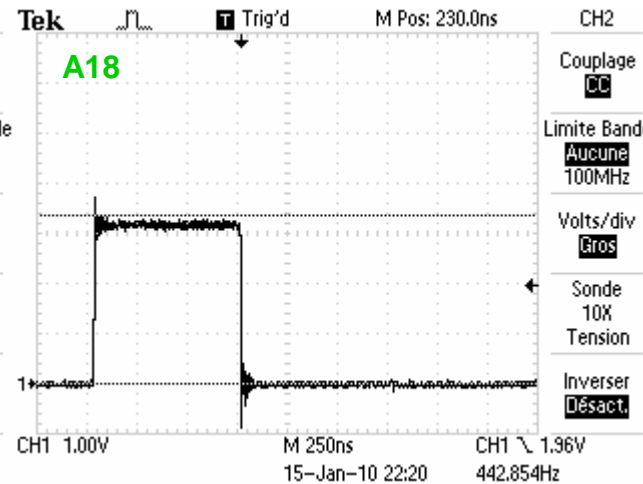
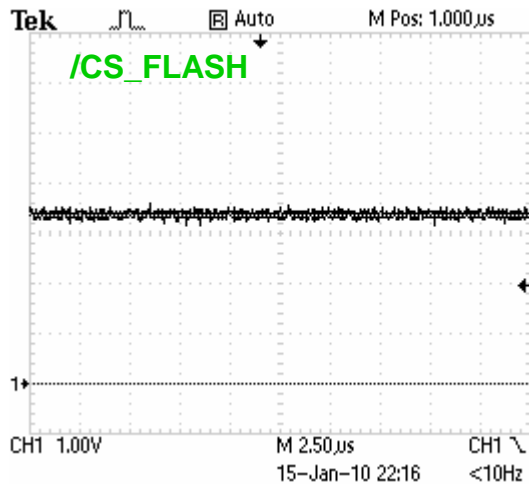
⇒ **A19 évolue toujours entre 2,5V et 3,3V.**

# M7 & M8 dessoudées

⇒ Mémoires FLASH M7 & M8 dessoudés.



⇒ Le signal /WR évolue à présent à des niveaux corrects (bien qu'il y ait des overshoots).



⇒ Le signal A18 évolue à présent à des niveaux corrects.

⇒ Le signal A19 évolue à présent à des niveaux corrects.



# Spec Mémoire FLASH ATMEL AT29LV040A 20TC

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## 4.4 Input Levels

While operating with a 3.3V  $\pm 10\%$  power supply, the address inputs and control inputs ( $\overline{OE}$ ,  $\overline{CE}$  and  $\overline{WE}$ ) may be driven from 0 to 5.5V without adversely affecting the operation of the device. The I/O lines can only be driven from 0 to 3.6V.