

CHASSIS : CP-520

MODEL : DTY-28A8/28A6/28A7  
DTY-2880/28W5  
DTY-21U7/21B4  
DTY-25G6/25G7

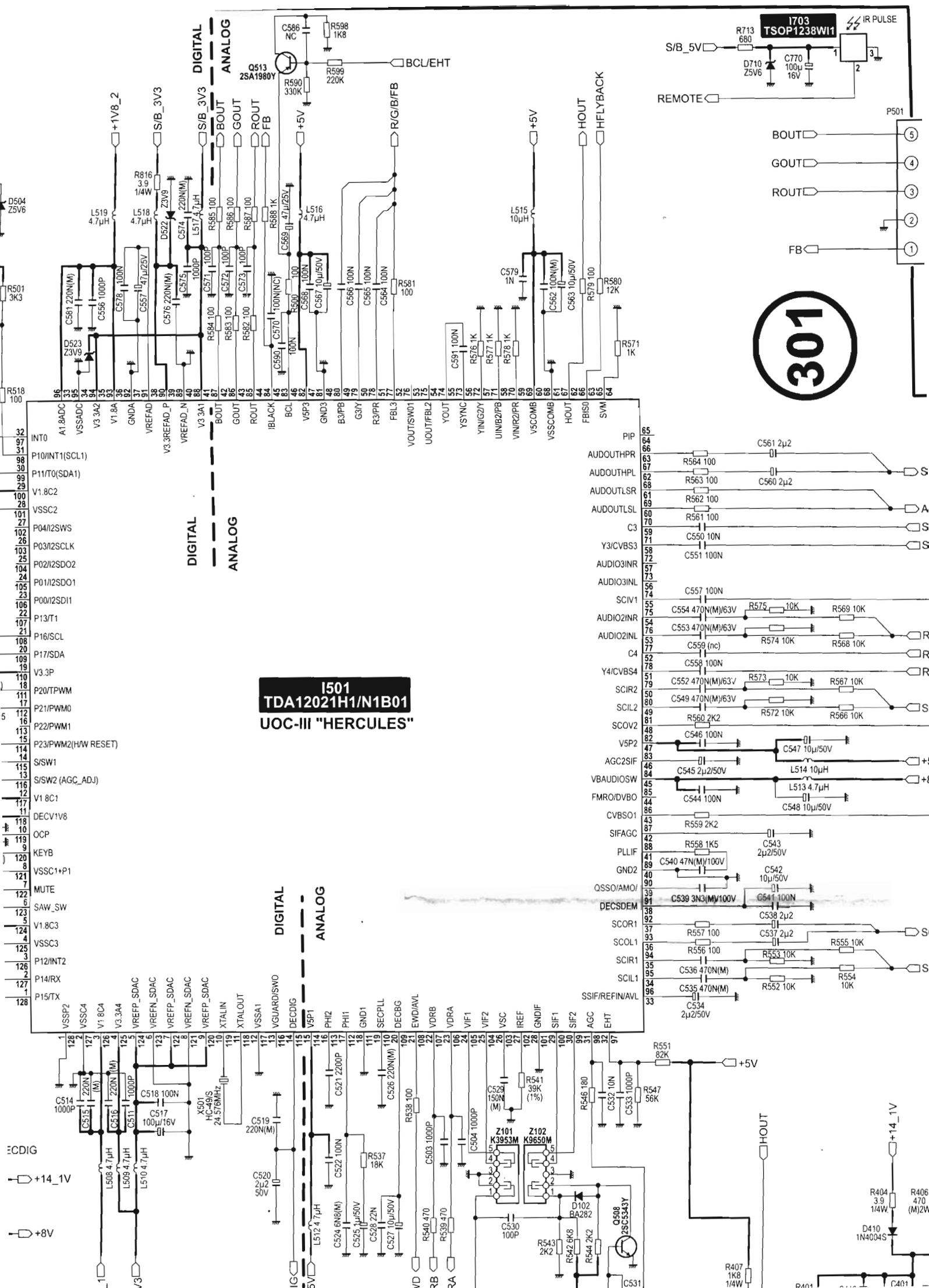
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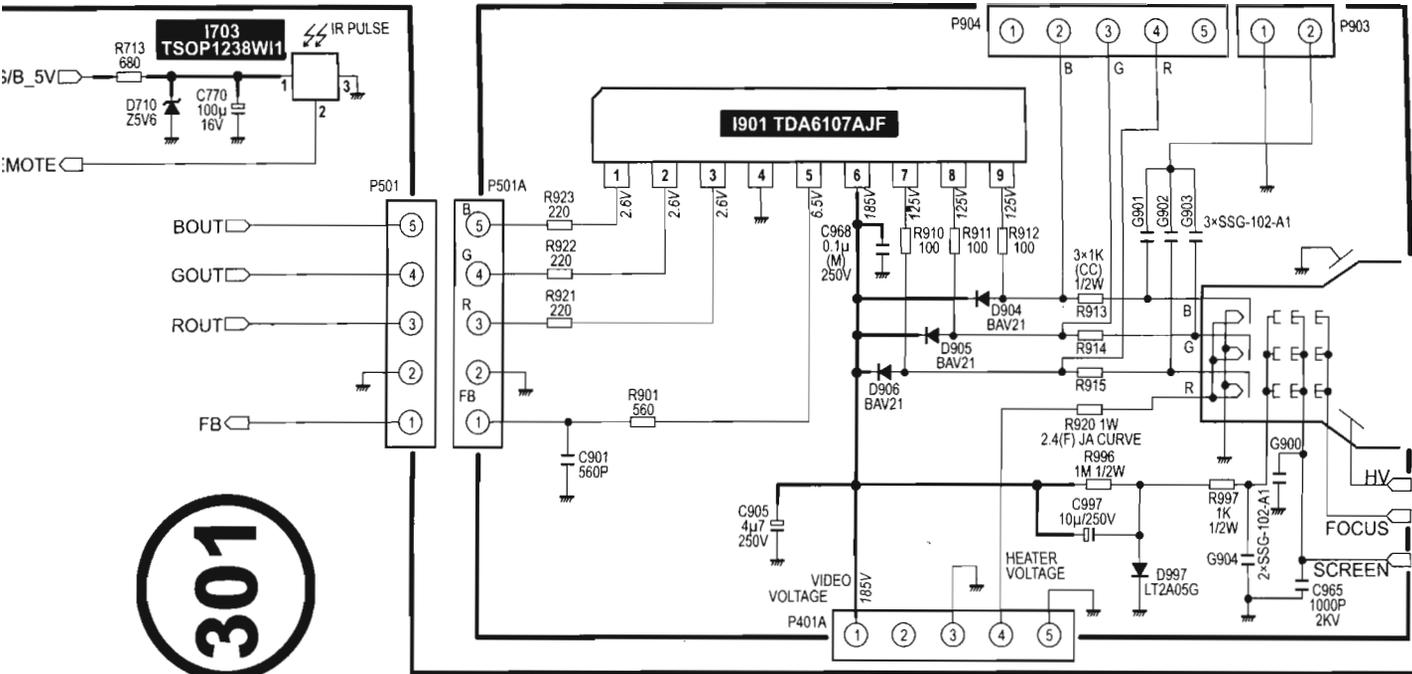
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DTC-21Y1/21Y2

CHASSIS : CP-520F

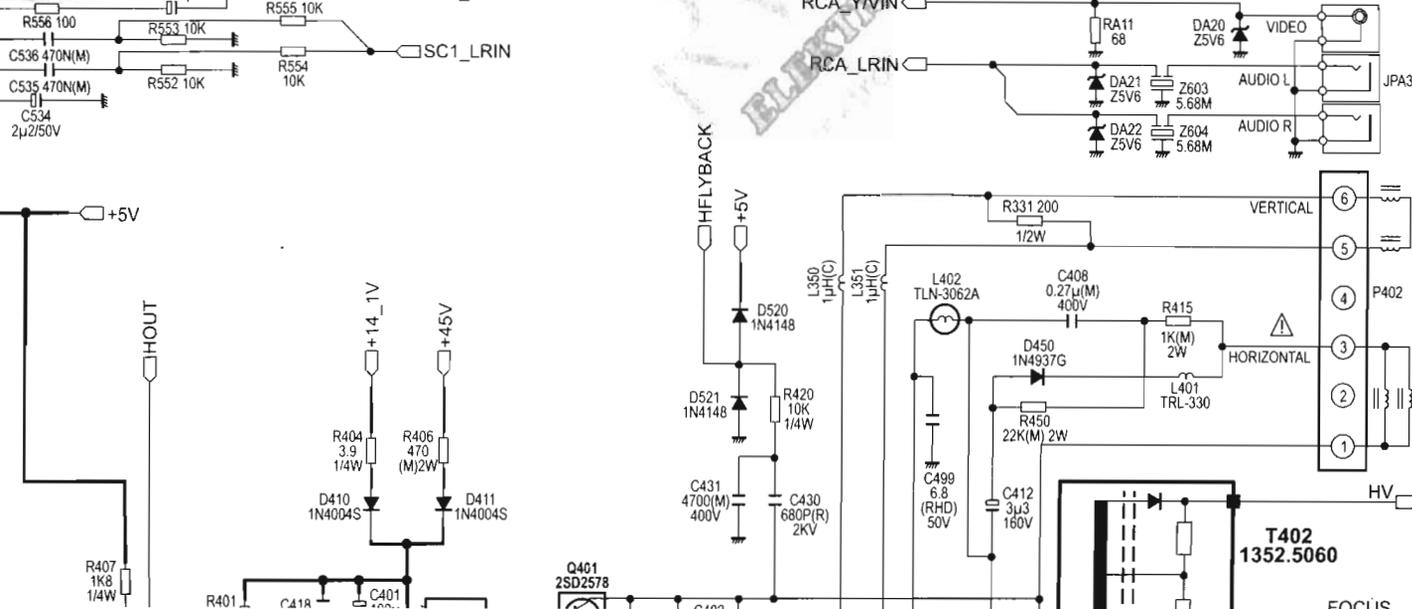
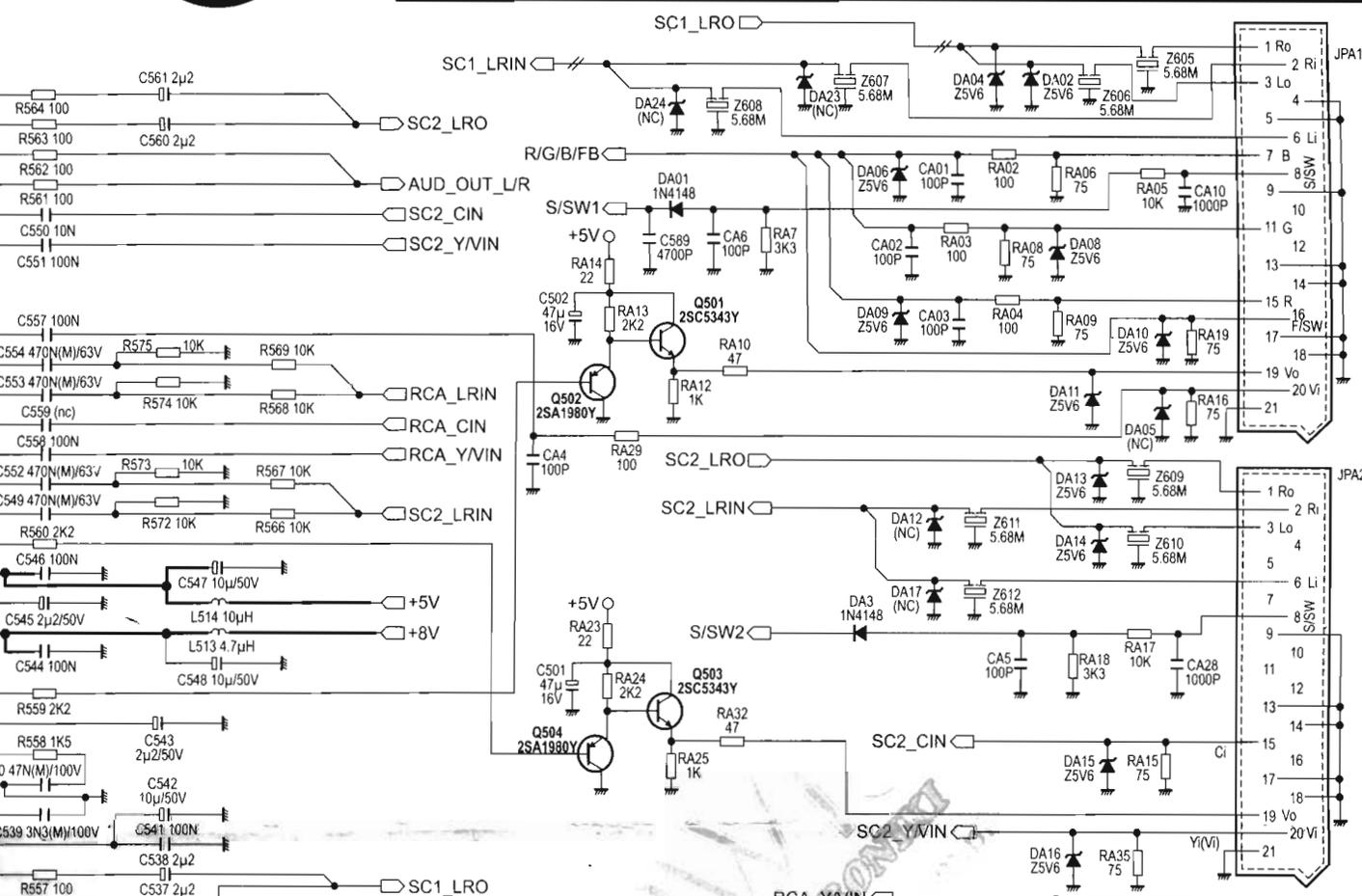
MODEL : DTZ-29G2K  
DTZ-29U7/U3  
DTZ-2881/28W8  
DTZ-2481

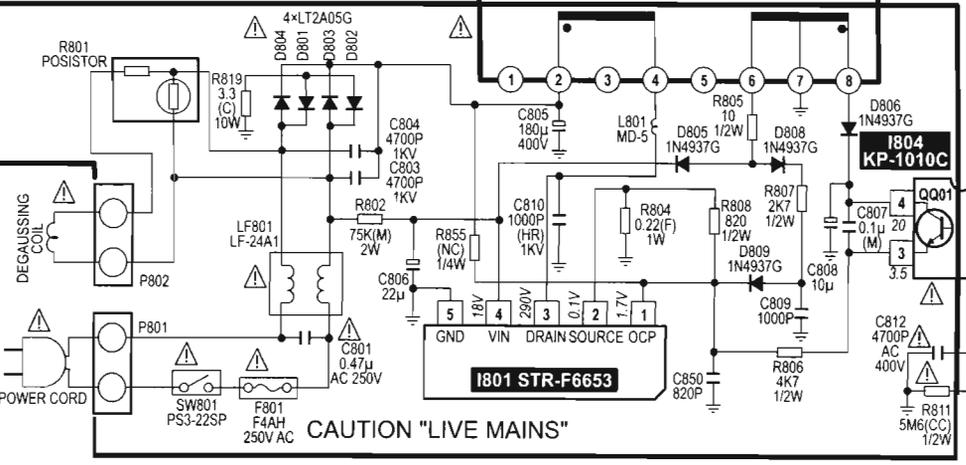
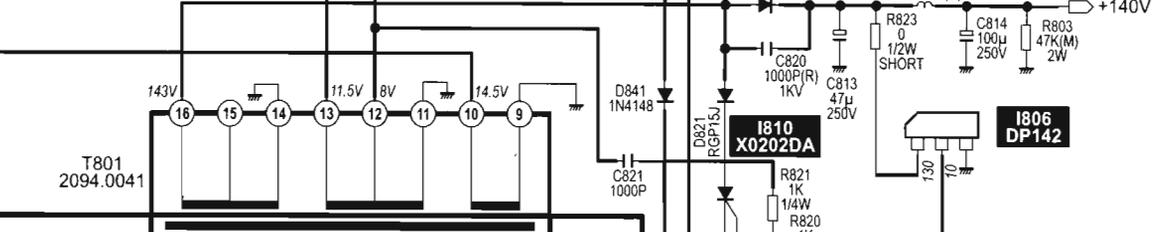
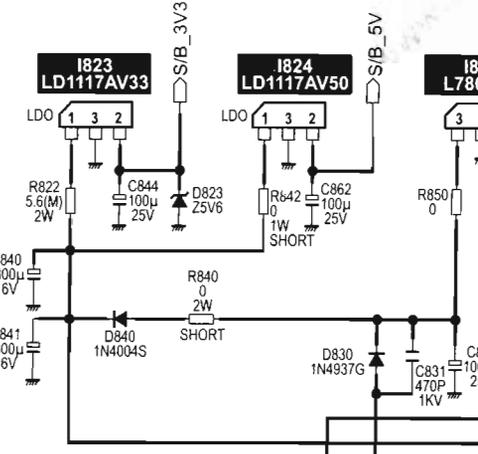
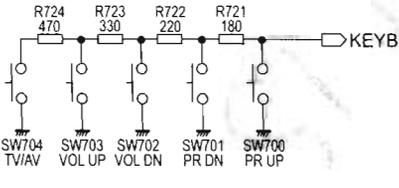
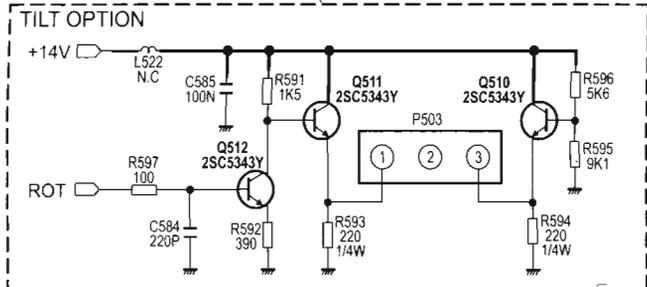
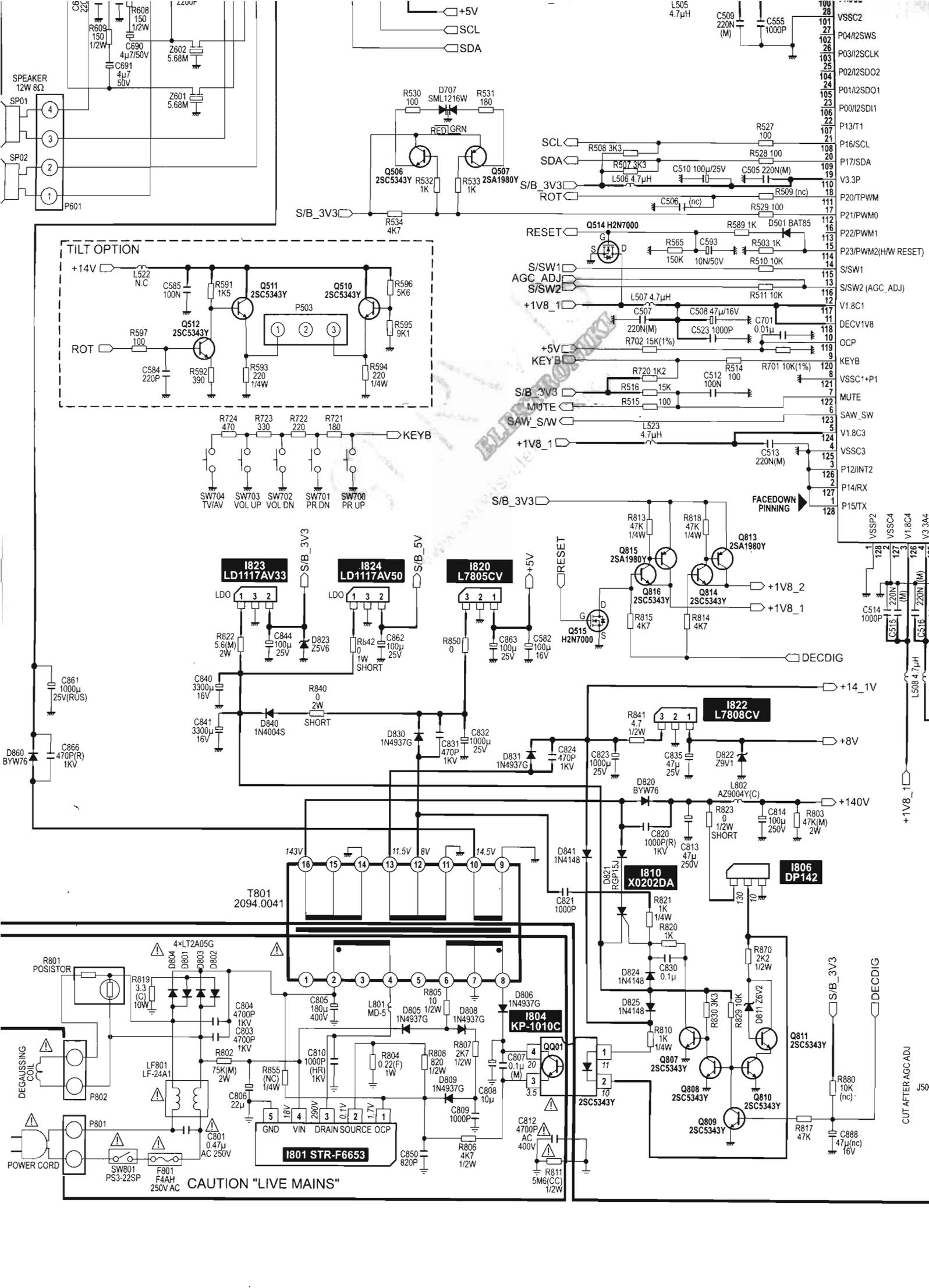


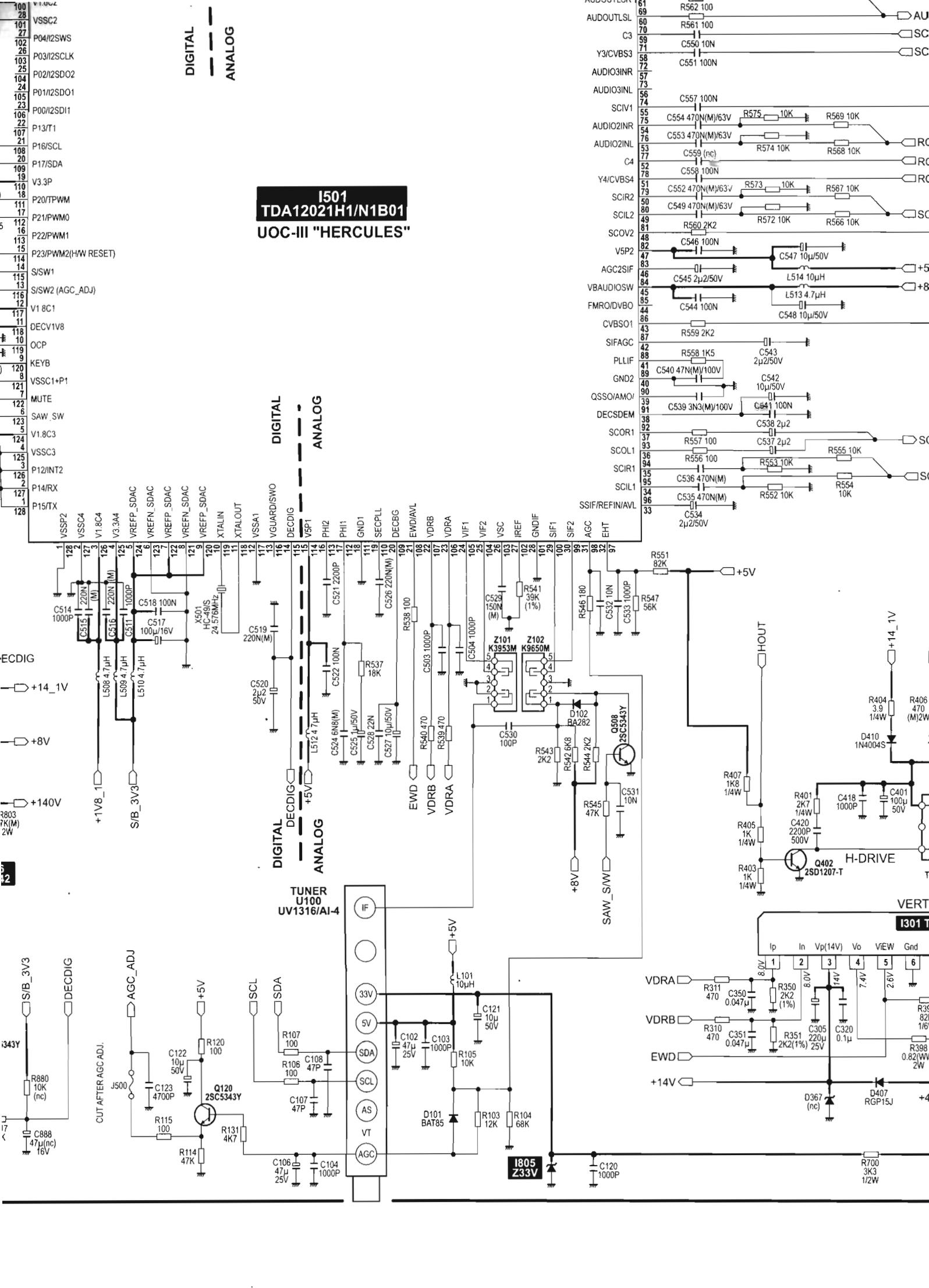




**301**







**I501  
TDA12021H1/N1B01  
UOC-III "HERCULES"**

DIGITAL  
ANALOG

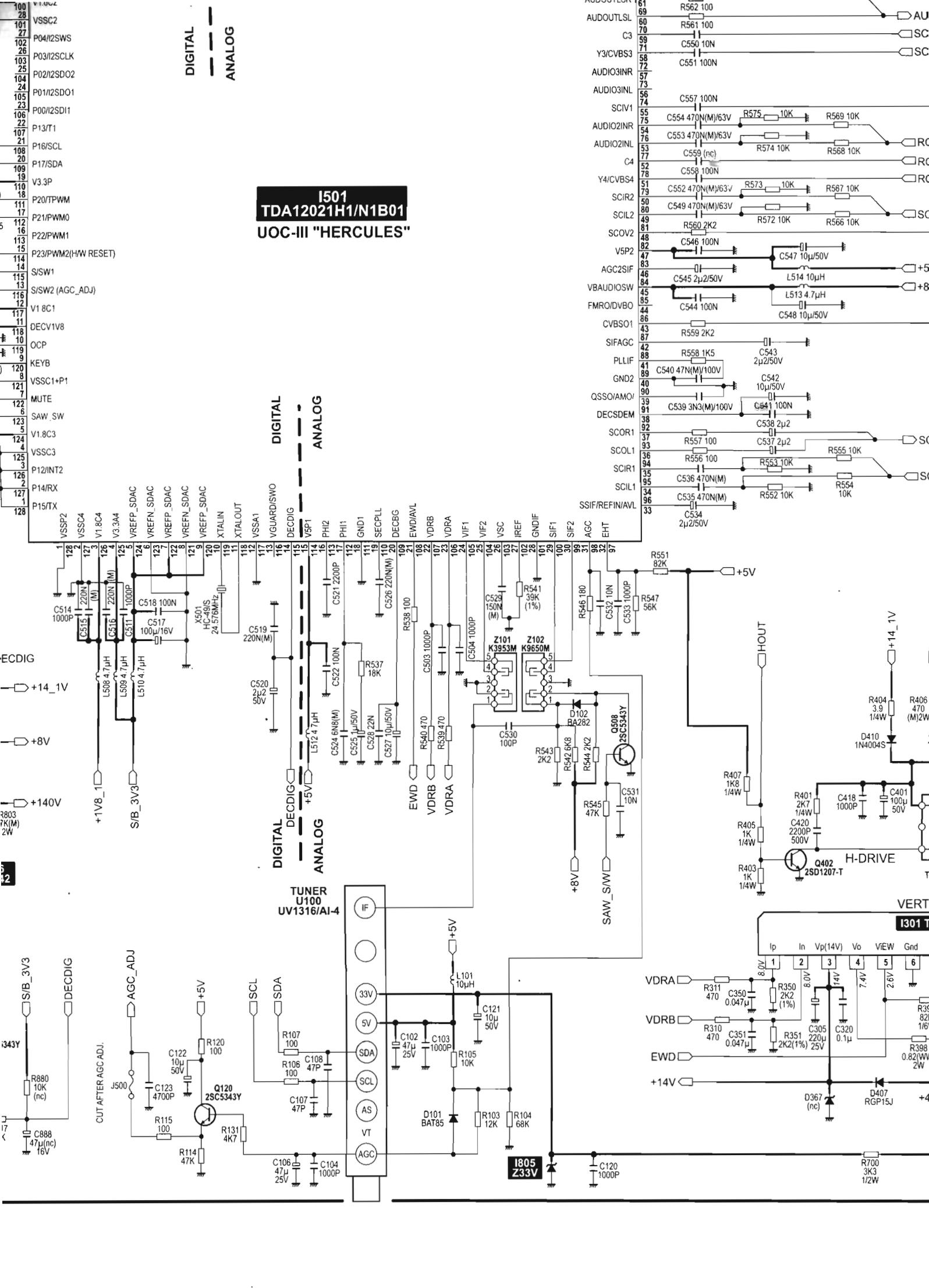
DIGITAL  
ANALOG

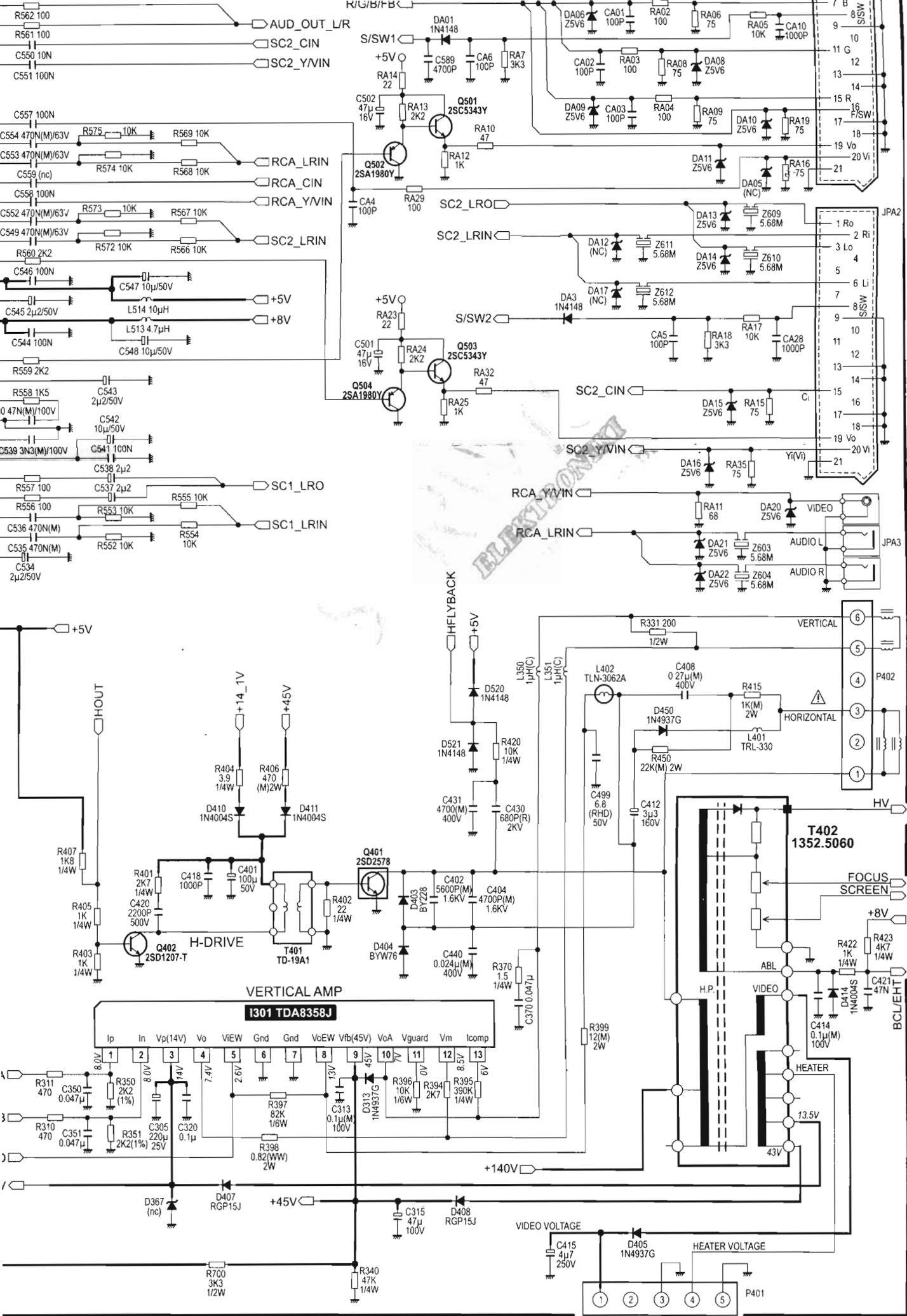
DIGITAL  
ANALOG

TUNER  
U100  
UV1316/AI-4

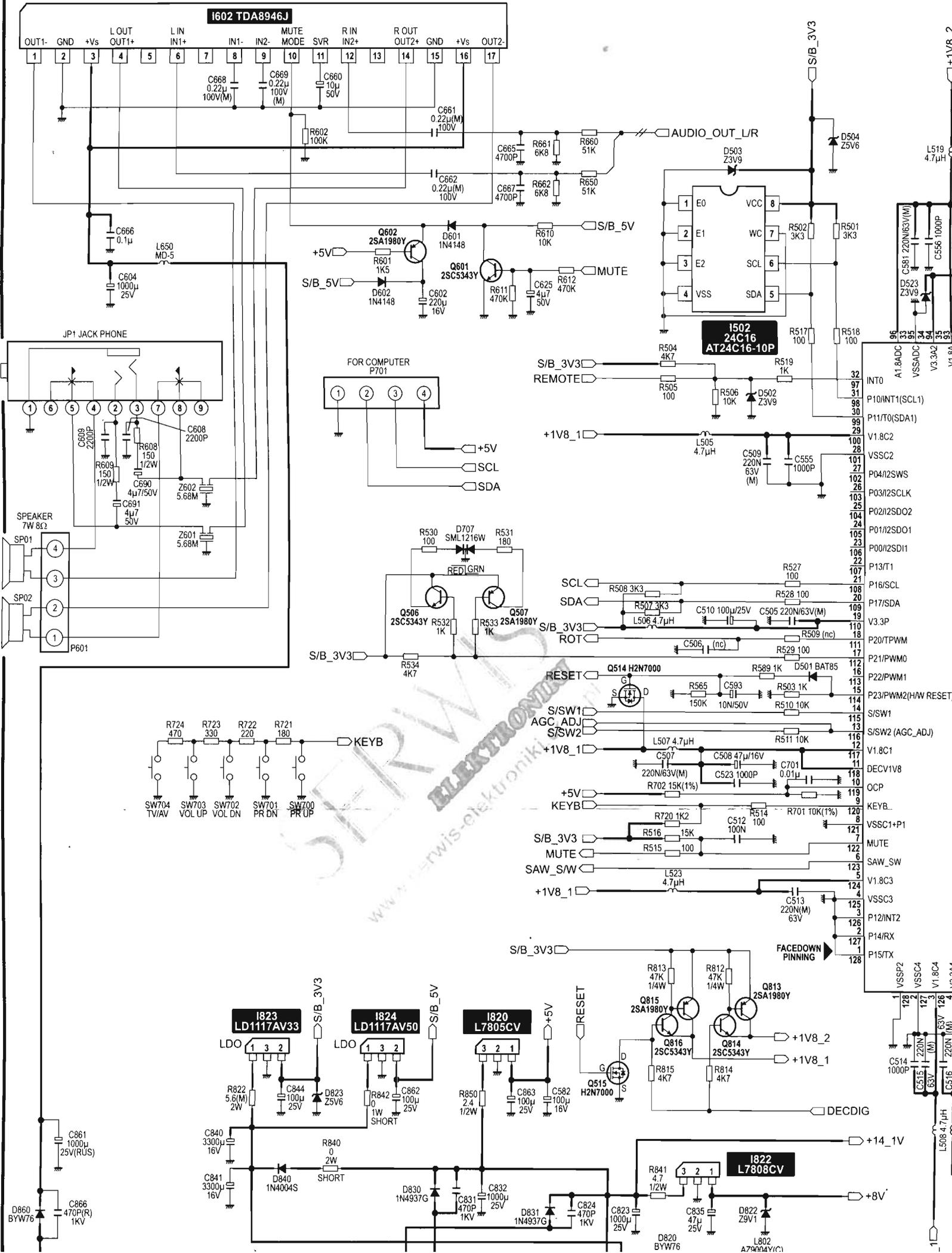
I805  
Z33V

VERT  
I301 T

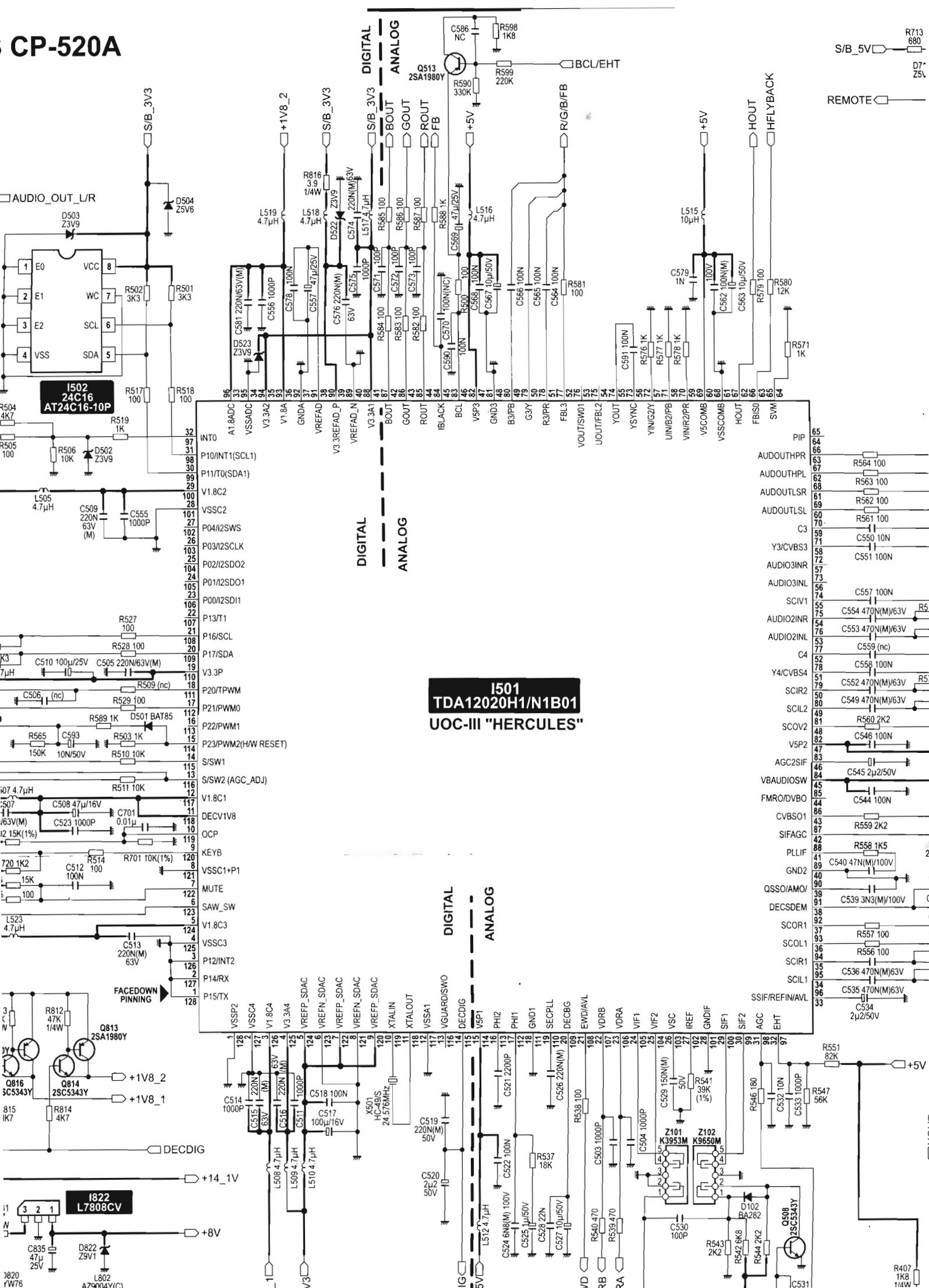




# CHASSIS CP-520A



# CP-520A

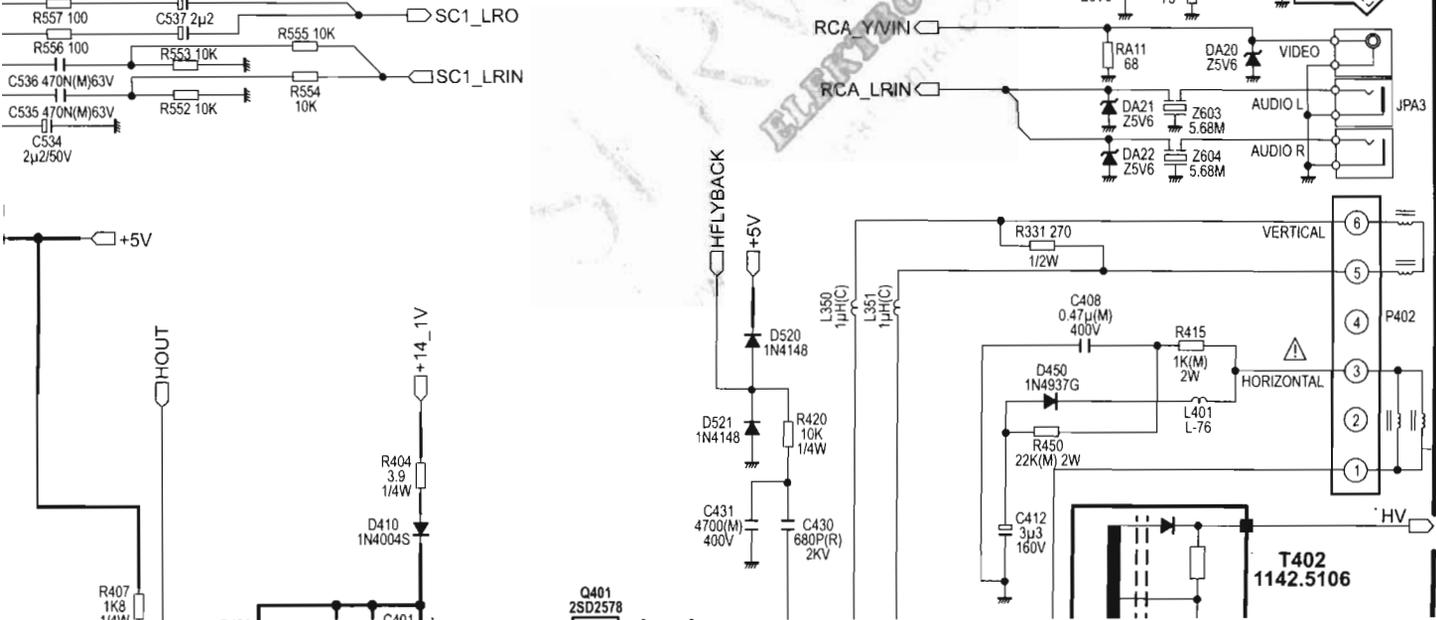
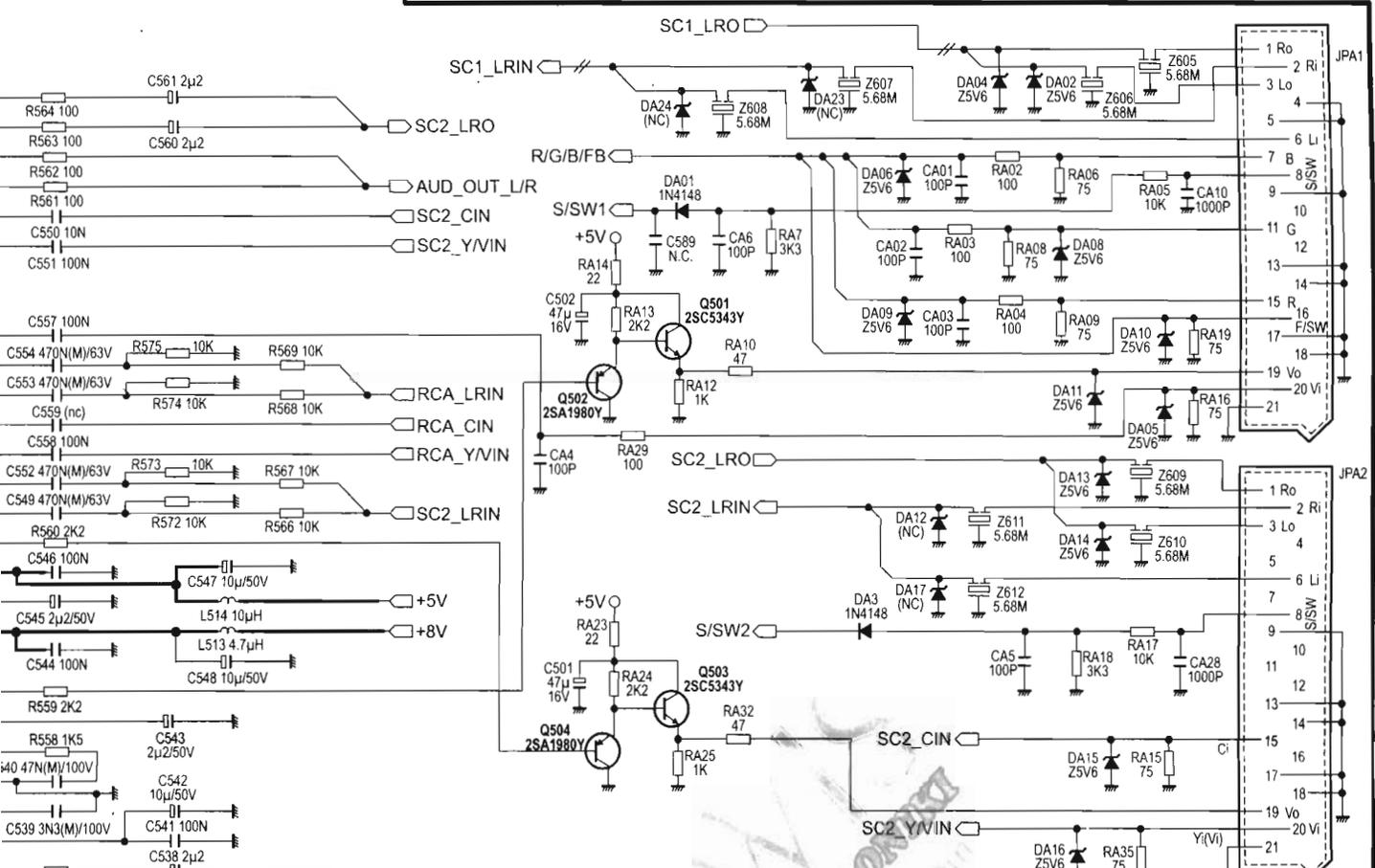
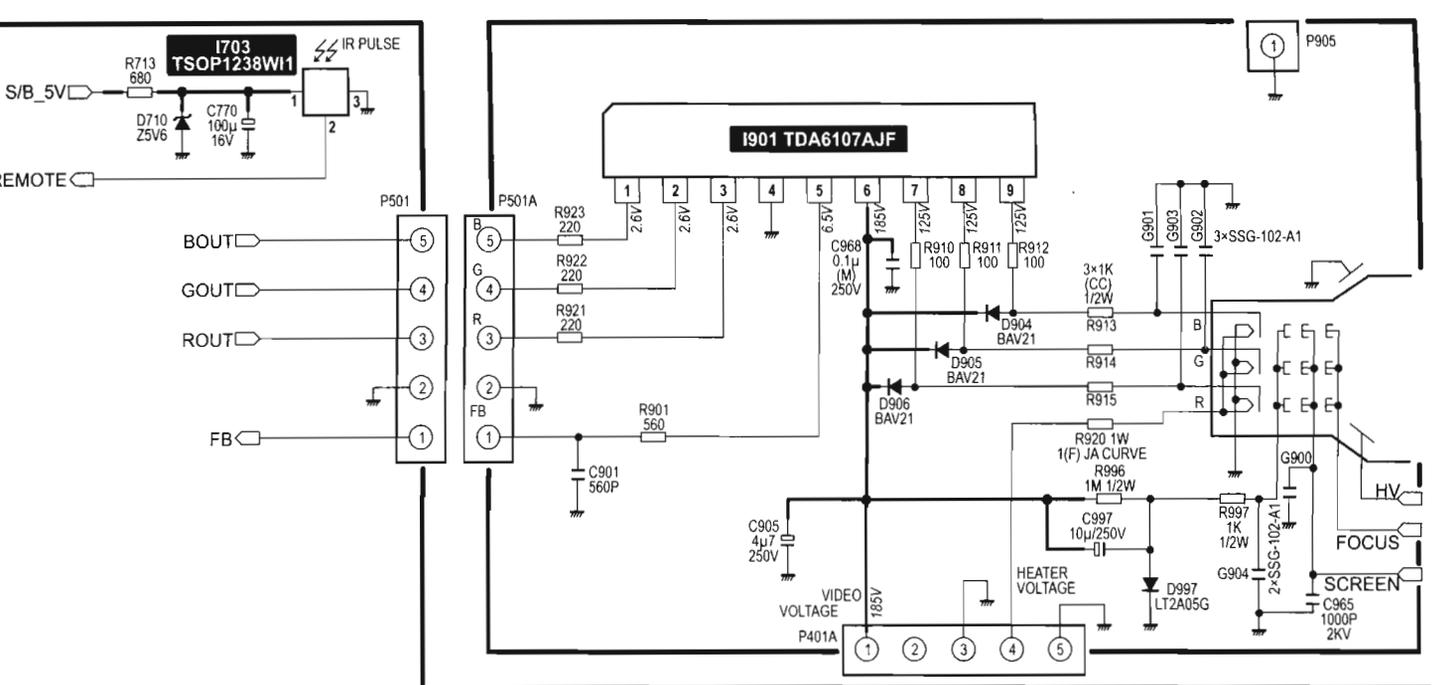


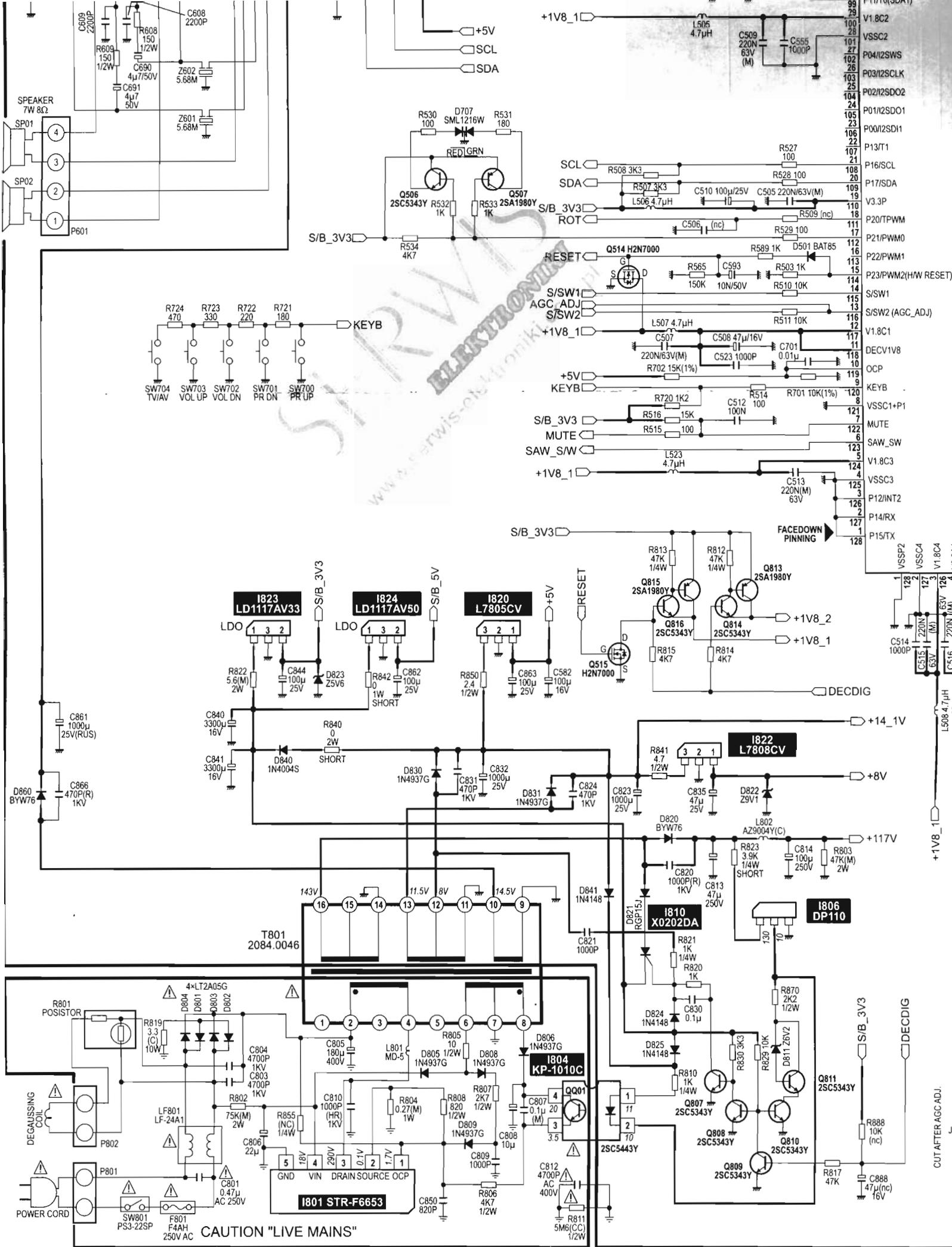
R713 680  
S/B\_5V  
D7 25V  
REMOTE

**I501 TDA12020H1/N1B01 UOC-III "HERCULES"**

DIGITAL  
ANALOG  
DIGITAL  
ANALOG  
DIGITAL  
ANALOG

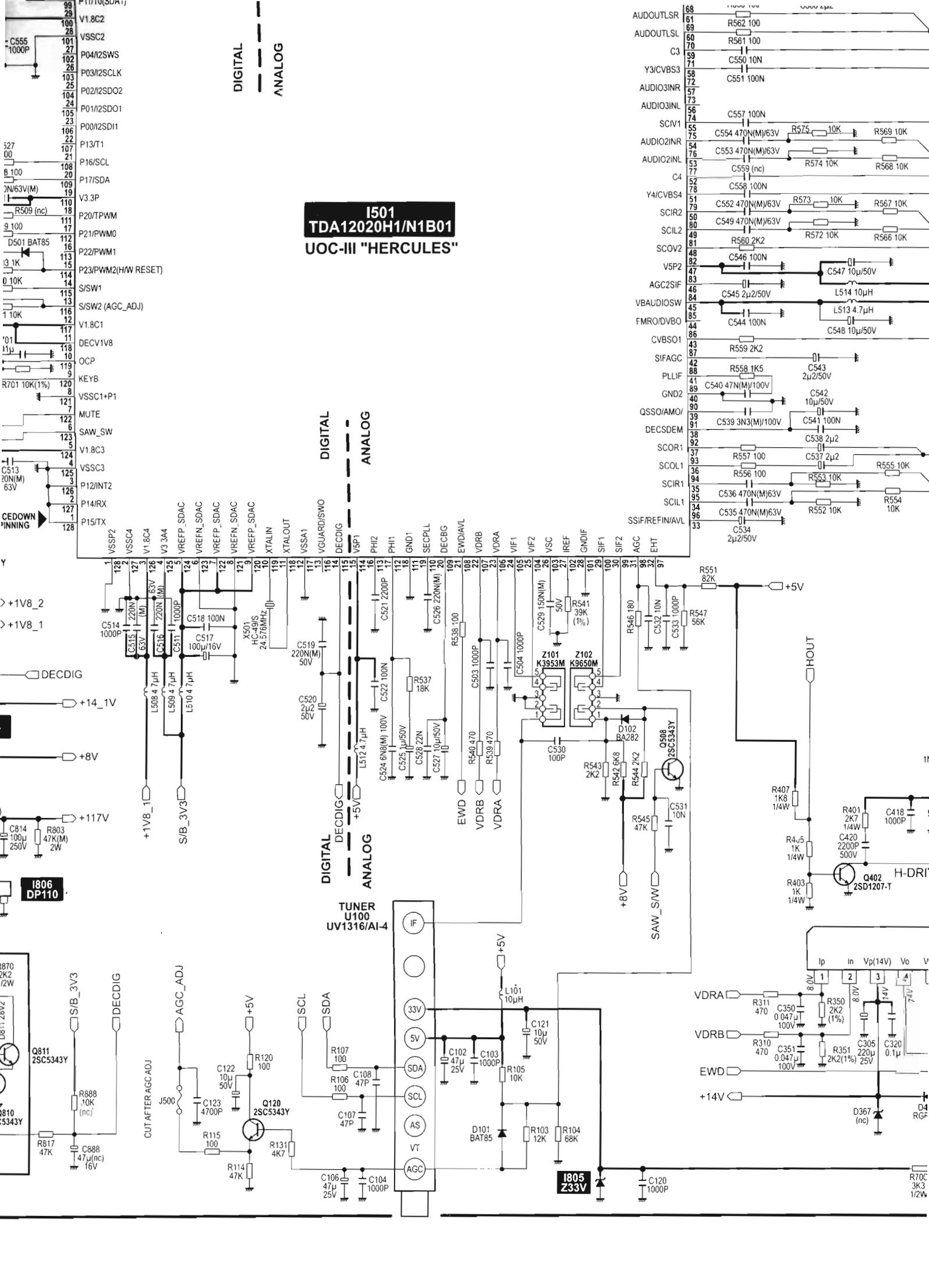
DIGITAL  
ANALOG





CAUTION "LIVE MAINS"

CUT AFTER AGC ADJ.



**I501  
TDA12020H1/N1B01  
UOC-III "HERCULES"**

DIGITAL  
ANALOG

DIGITAL  
ANALOG

DIGITAL  
ANALOG

TUNER  
U100  
UV1316/AI-4

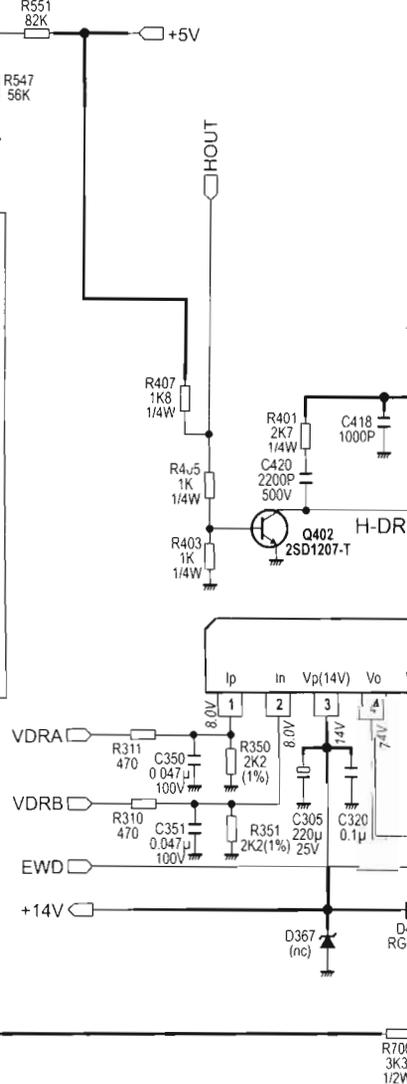
I805  
Z33V

- 99 V1.8C2
- 28 VSSC2
- 27 P04I2SWS
- 26 P03I2SCLK
- 25 P02I2SDO2
- 24 P01I2SDO1
- 23 P00I2SDI1
- 22 P13/T1
- 21 P16/SCL
- 20 P17/SDA
- 19 V3.3P
- 18 P20/TPWM
- 17 P21/PWM0
- 16 P22/PWM1
- 15 P23/PWM2(H/W RESET)
- 14 S/SW1
- 13 S/SW2 (AGC\_ADJ)
- 12 V1.8C1
- 11 DECV1V8
- 10 OCP
- 9 KEYB
- 8 VSSC1+P1
- 7 MUTE
- 6 SAW\_SW
- 5 V1.8C3
- 4 VSSC3
- 3 P12/INT2
- 2 P14/RX
- 1 P15/TX

- 68 AUDOUTLSR
- 61 R562 100
- 89 AUDOUTLSL
- 80 R581 100
- 70 C3
- 59 C550 10N
- 72 Y3/CVBS3
- 58 C551 100N
- 77 AUDIO3INR
- 57 AUDIO3INL
- 74 SCIV1
- 55 C554 470N(M)/63V
- 75 R575 10K
- 54 R569 10K
- 76 AUDIO2INR
- 53 C553 470N(M)/63V
- 77 R574 10K
- 53 C559 (nc)
- 77 R568 10K
- 52 C4
- 78 C558 100N
- 51 Y4/CVBS4
- 78 C552 470N(M)/63V
- 51 R573 10K
- 78 R567 10K
- 50 SCIR2
- 79 C549 470N(M)/63V
- 50 R572 10K
- 79 R566 10K
- 49 SCIL2
- 81 R560 2K2
- 48 SCOV2
- 81 C546 100N
- 47 V5P2
- 82 C547 10µ/50V
- 47 AGC2SIF
- 46 C545 2µ/50V
- 84 L514 10µH
- 45 VBAUDIOSW
- 85 C544 100N
- 86 L513 4.7µH
- 44 FMRO/DVBO
- 86 C548 10µ/50V
- 43 CVBSO1
- 87 R559 2K2
- 42 SIFAGC
- 88 R558 1K5
- 41 PLLIF
- 89 C543 2µ/50V
- 40 C540 47N(M)/100V
- 89 C542 10µ/50V
- 40 GND2
- 39 C539 3N3(M)/100V
- 89 C541 100N
- 39 QSSO/AMO/
- 91 C538 2µ2
- 38 DECSDEM
- 92 R557 100
- 37 SCOR1
- 93 C537 2µ2
- 36 SCOL1
- 94 R556 100
- 36 R553 10K
- 94 R555 10K
- 35 SCIR1
- 95 C536 470N(M)/63V
- 35 R554 10K
- 95 SCIL1
- 96 C535 470N(M)/63V
- 34 R552 10K
- 96 SSIF/REFIN/AVL
- 33 C534 2µ/50V

- 128 VSSP2
- 127 VSSC4
- 126 V1.8C4
- 125 V3.3A4
- 124 VREFP\_SDAC
- 123 VREFN\_SDAC
- 122 VREFP\_SDAC
- 121 VREFN\_SDAC
- 120 XTALIN
- 119 XTALOUT
- 118 VSSA1
- 117 VGUARD/SWO
- 116 DECDIG
- 115 V5P1
- 114 PHI2
- 113 PHI1
- 112 GND1
- 111 SECPLL
- 110 DECBG
- 109 EWD/AVL
- 108 VDRB
- 107 VDRA
- 106 VIF1
- 105 VIF2
- 104 VSC
- 103 IREF
- 102 GNDIF
- 101 SIF1
- 100 SIF2
- 99 AGC
- 98 EHT
- 97

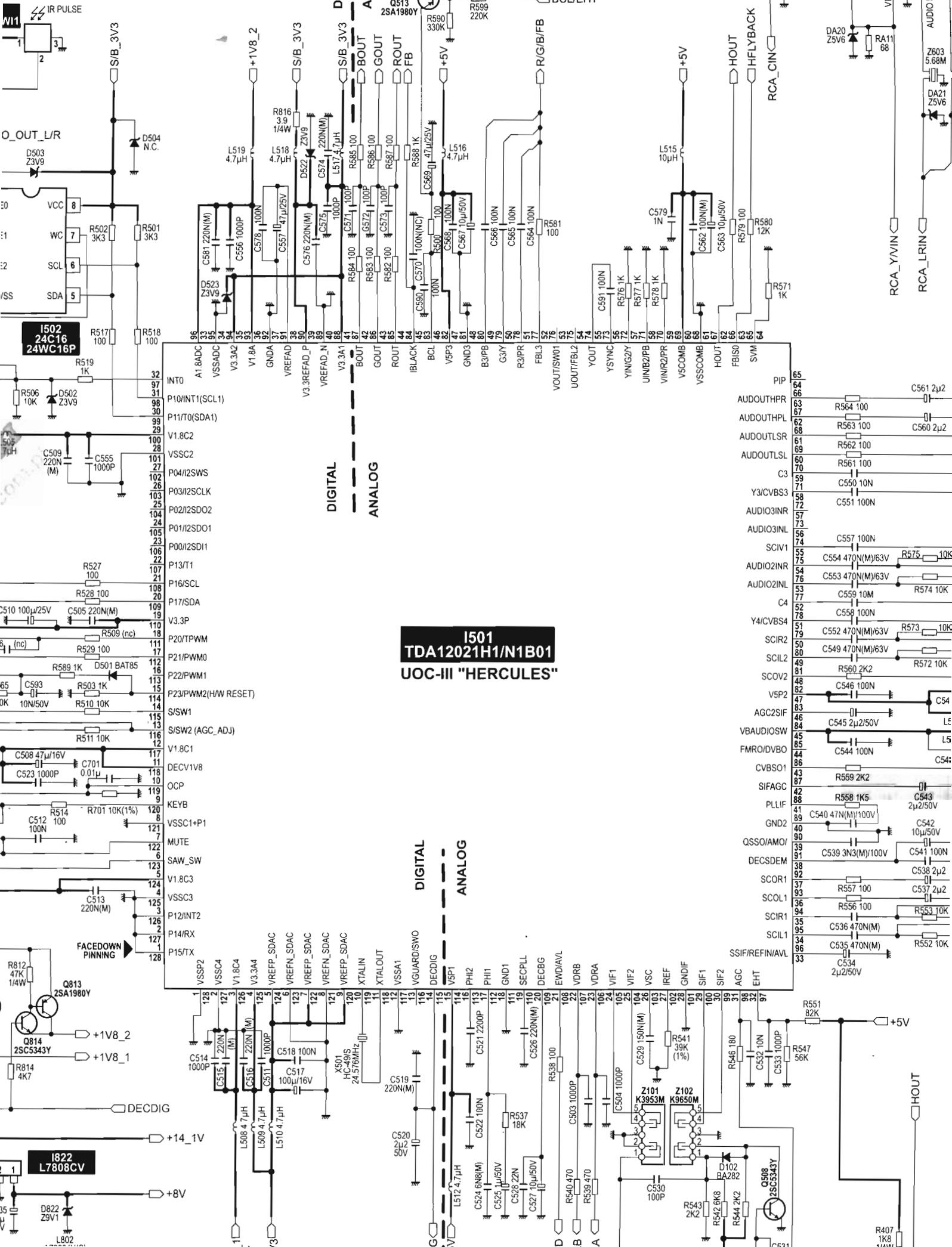
- 128 +1V8\_2
- 127 +1V8\_1
- 126 DECDIG
- 125 +14\_1V
- 124 +8V
- 123 +117V
- 122 C814 100µ 250V
- 121 R803 47K(M) 2W
- 120 I806 DP110
- 119 +1V8\_1
- 118 S/B\_3V3
- 117 DECDIG
- 116 Q811 2SC5343Y
- 115 R888 10K (nc)
- 114 R817 47K
- 113 C888 47µ(nc) 16V
- 112 Q810 2SC5343Y
- 111 R114 47K
- 110 R115 100
- 109 C123 4700P
- 108 C122 10µ 50V
- 107 R120 100
- 106 Q120 2SC5343Y
- 105 R114 47K
- 104 R115 100
- 103 C123 4700P
- 102 C122 10µ 50V
- 101 R120 100
- 100 Q120 2SC5343Y
- 99 R114 47K
- 98 R115 100
- 97 C123 4700P
- 96 C122 10µ 50V
- 95 R120 100
- 94 Q120 2SC5343Y
- 93 R114 47K
- 92 R115 100
- 91 C123 4700P
- 90 C122 10µ 50V
- 89 R120 100
- 88 Q120 2SC5343Y
- 87 R114 47K
- 86 R115 100
- 85 C123 4700P
- 84 C122 10µ 50V
- 83 R120 100
- 82 Q120 2SC5343Y
- 81 R114 47K
- 80 R115 100
- 79 C123 4700P
- 78 C122 10µ 50V
- 77 R120 100
- 76 Q120 2SC5343Y
- 75 R114 47K
- 74 R115 100
- 73 C123 4700P
- 72 C122 10µ 50V
- 71 R120 100
- 70 Q120 2SC5343Y
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- 68 R115 100
- 67 C123 4700P
- 66 C122 10µ 50V
- 65 R120 100
- 64 Q120 2SC5343Y
- 63 R114 47K
- 62 R115 100
- 61 C123 4700P
- 60 C122 10µ 50V
- 59 R120 100
- 58 Q120 2SC5343Y
- 57 R114 47K
- 56 R115 100
- 55 C123 4700P
- 54 C122 10µ 50V
- 53 R120 100
- 52 Q120 2SC5343Y
- 51 R114 47K
- 50 R115 100
- 49 C123 4700P
- 48 C122 10µ 50V
- 47 R120 100
- 46 Q120 2SC5343Y
- 45 R114 47K
- 44 R115 100
- 43 C123 4700P
- 42 C122 10µ 50V
- 41 R120 100
- 40 Q120 2SC5343Y
- 39 R114 47K
- 38 R115 100
- 37 C123 4700P
- 36 C122 10µ 50V
- 35 R120 100
- 34 Q120 2SC5343Y
- 33 R114 47K
- 32 R115 100
- 31 C123 4700P
- 30 C122 10µ 50V
- 29 R120 100
- 28 Q120 2SC5343Y
- 27 R114 47K
- 26 R115 100
- 25 C123 4700P
- 24 C122 10µ 50V
- 23 R120 100
- 22 Q120 2SC5343Y
- 21 R114 47K
- 20 R115 100
- 19 C123 4700P
- 18 C122 10µ 50V
- 17 R120 100
- 16 Q120 2SC5343Y
- 15 R114 47K
- 14 R115 100
- 13 C123 4700P
- 12 C122 10µ 50V
- 11 R120 100
- 10 Q120 2SC5343Y
- 9 R114 47K
- 8 R115 100
- 7 C123 4700P
- 6 C122 10µ 50V
- 5 R120 100
- 4 Q120 2SC5343Y
- 3 R114 47K
- 2 R115 100
- 1 C123 4700P
- 0 C122 10µ 50V







-520F



**I501**  
**TDA12021H/N1B01**  
**UOC-III "HERCULES"**

**I502**  
**24C16**  
**24WC16P**

**I822**  
**L7808CV**

DIGITAL  
 ANALOG

DIGITAL  
 ANALOG

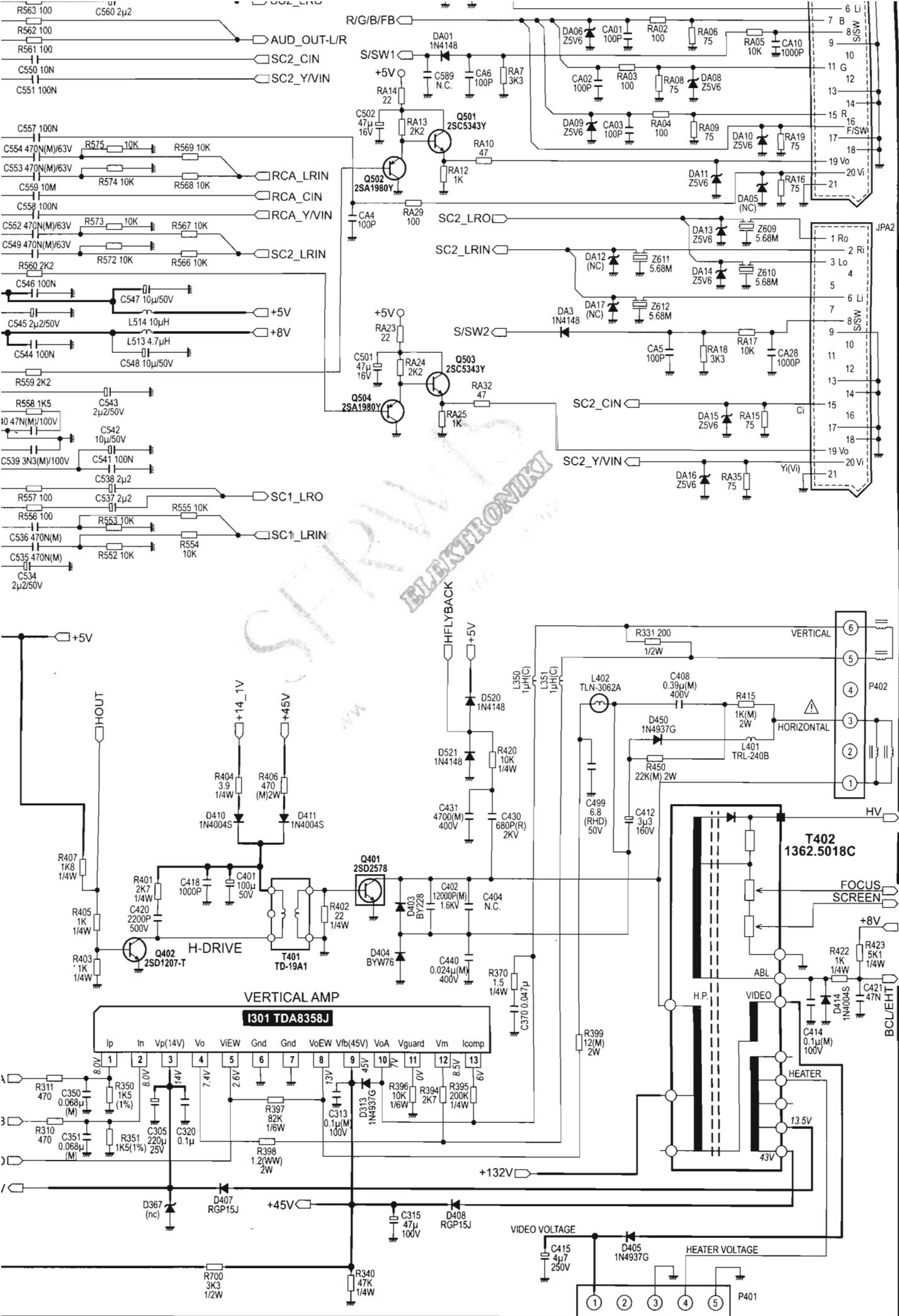
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 AUDOUTHPR  
 AUDOUTHPL  
 AUDOUTLSR  
 AUDOUTLSL  
 C3  
 Y3/CVBS3  
 AUDIO3INR  
 AUDIO3INL  
 SCIV1  
 AUDIO2INR  
 AUDIO2INL  
 C4  
 Y4/CVBS4  
 SCIR2  
 SCIL2  
 SCOV2  
 V5P2  
 AGC2SIF  
 VBAUDIOSW  
 FMRO/DVBO  
 CVBSO1  
 SIFAGO  
 PLLIF  
 GND2  
 QSSO/AMO/  
 DECSDEM  
 SCOR1  
 SCOL1  
 SCIR1  
 SCIL1  
 SSI/REFIN/AVL

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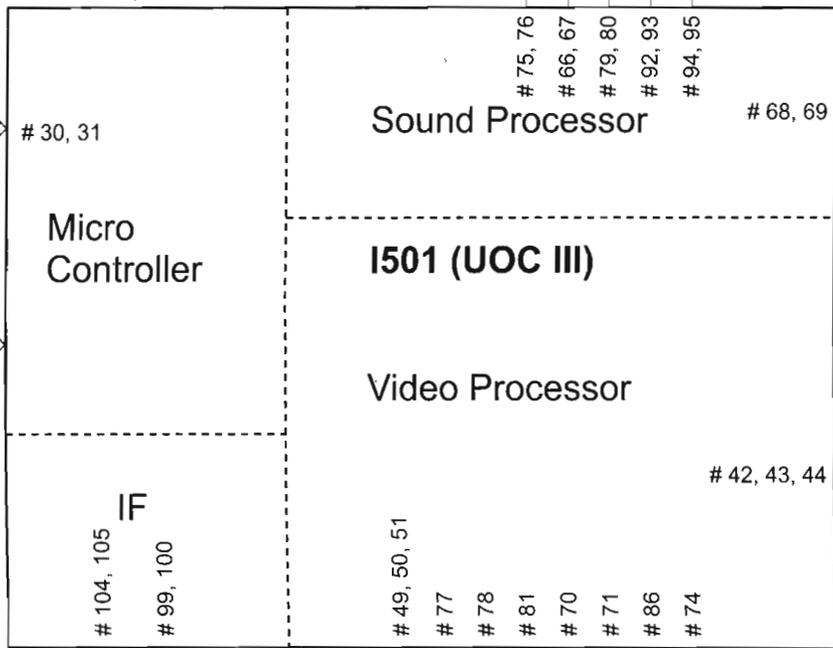
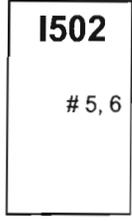




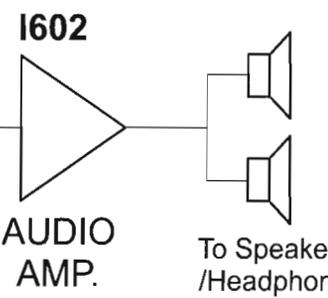




EEPROM

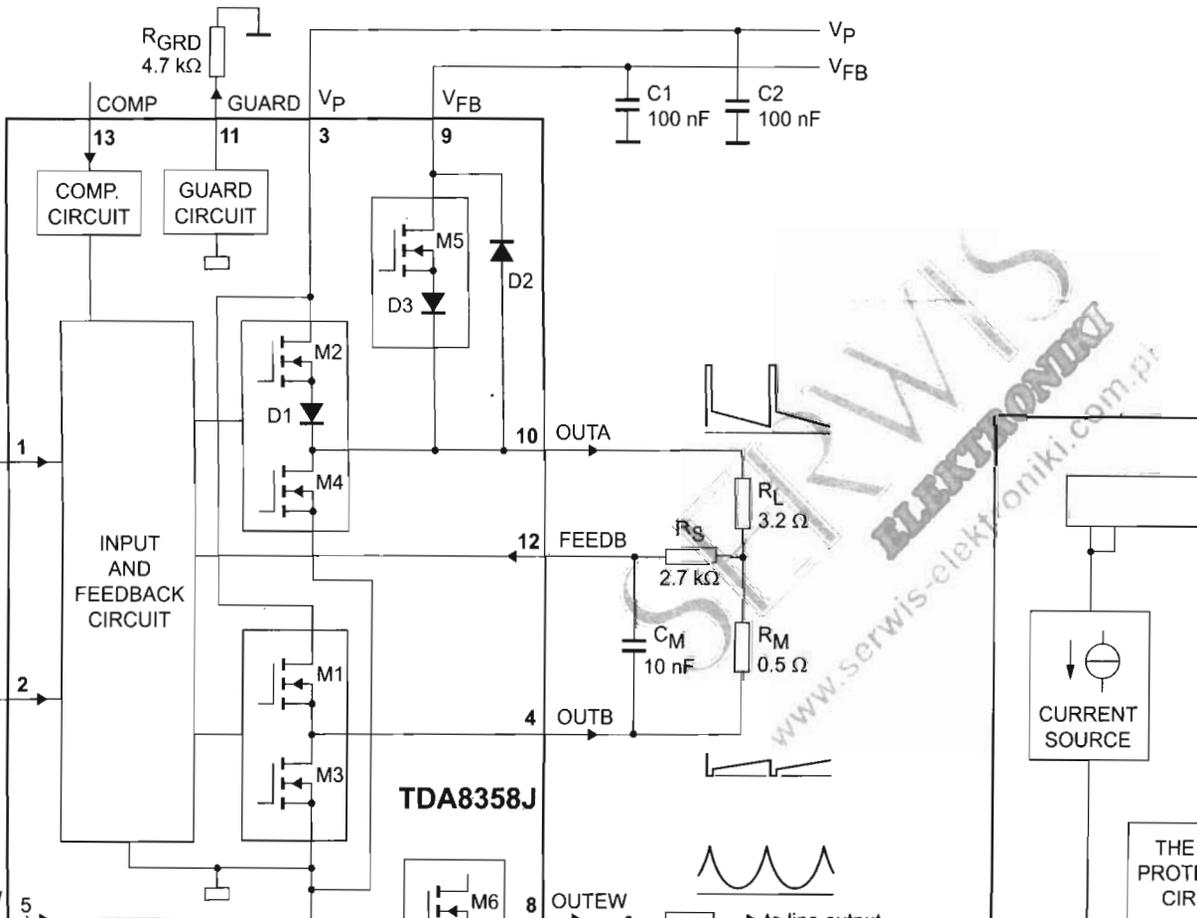
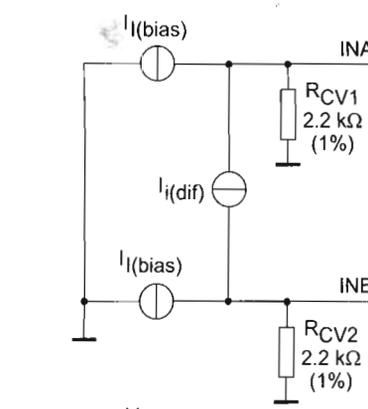
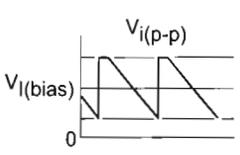
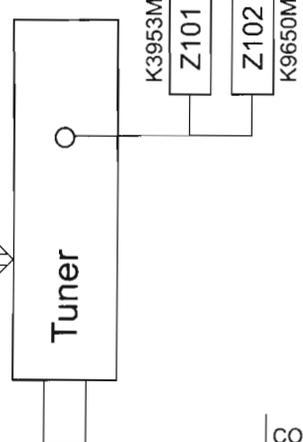


Audio In AV3  
Audio Out Scart 2  
Audio In Scart 2  
Audio Out Scart 1  
Audio In Scart1

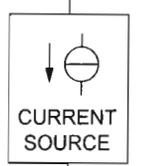


CVBS In Scart1  
CVBS Out Scart1  
CVBS/Y In Scart2  
Chroma In Scart2  
CVBS Out Scart2  
CVBS/Y In AV3  
Chroma In AV3  
RGB In Scart1

I2C BUS

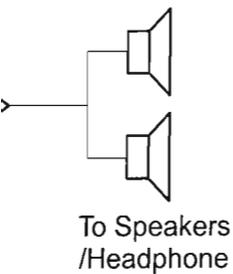


TDA8358J



THEI  
PROTI  
CIR

o In AV3  
o Out Scart 2  
o In Scart 2  
o Out Scart 1  
o In Scart1

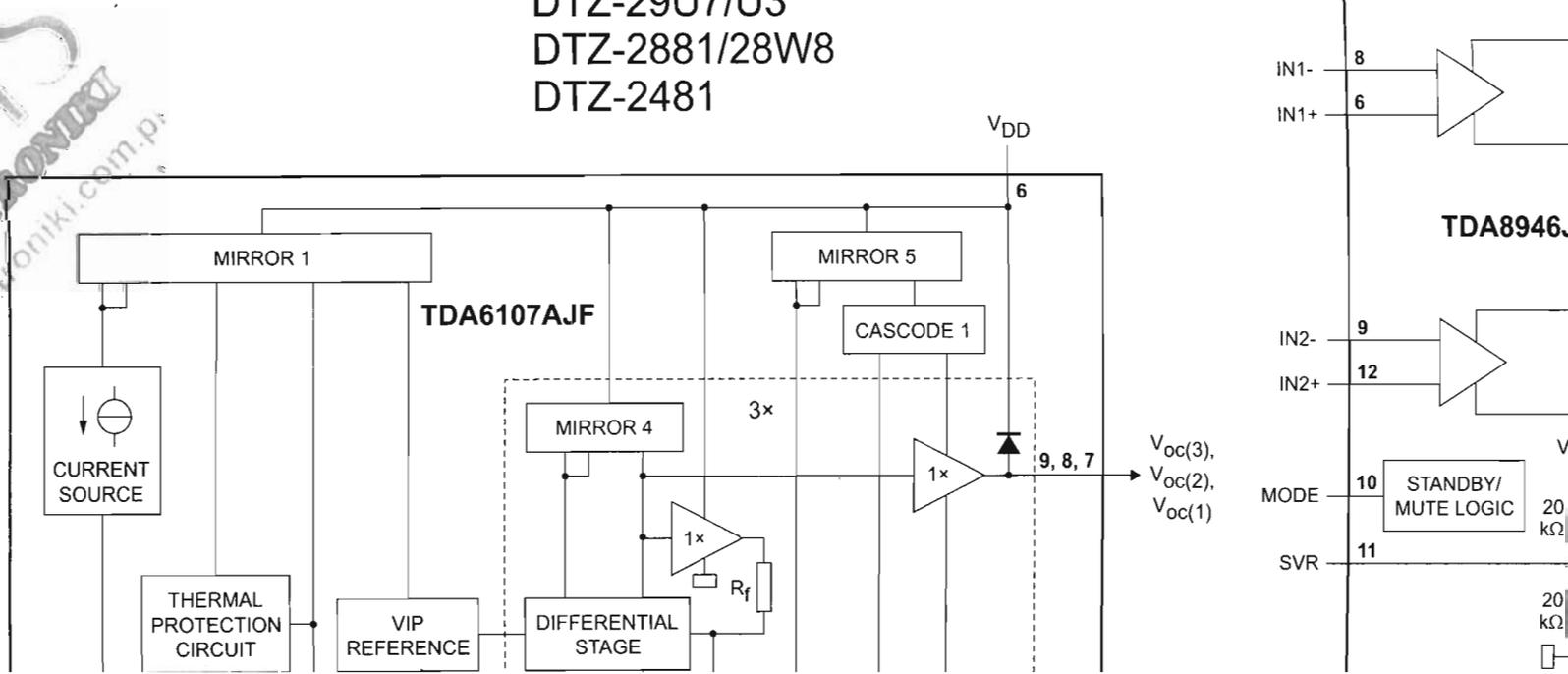
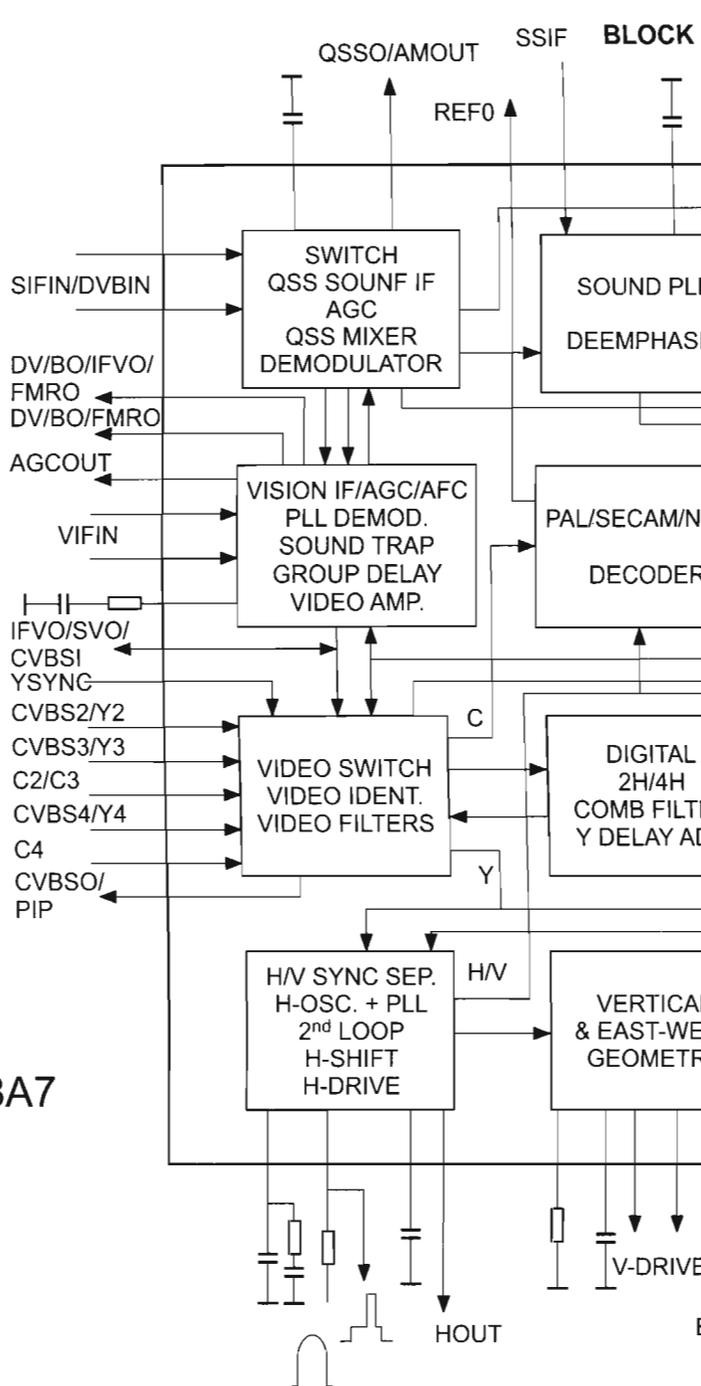


S In Scart1  
S Out Scart1  
S/Y In Scart2  
ma In Scart2  
S Out Scart2  
S/Y In AV3  
ma In AV3  
In Scart1

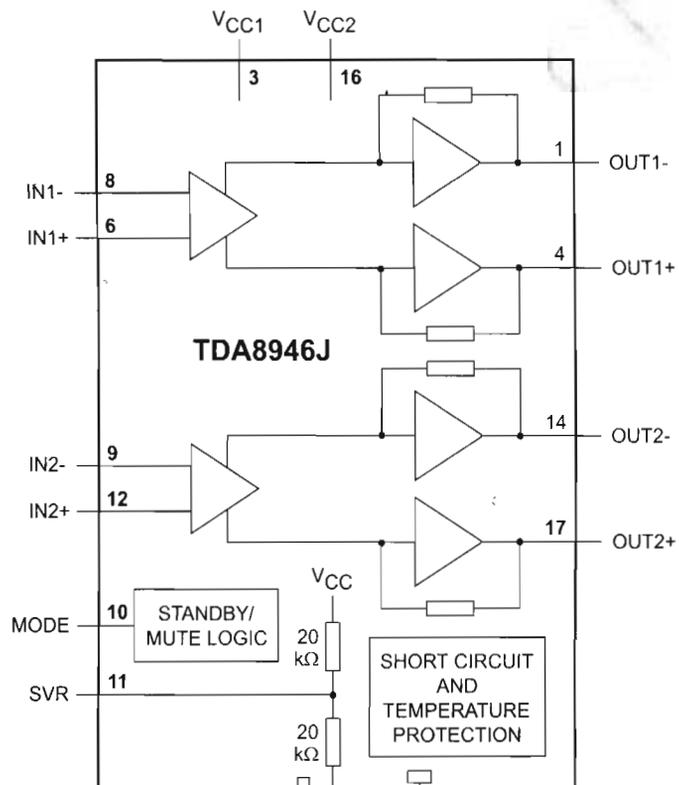
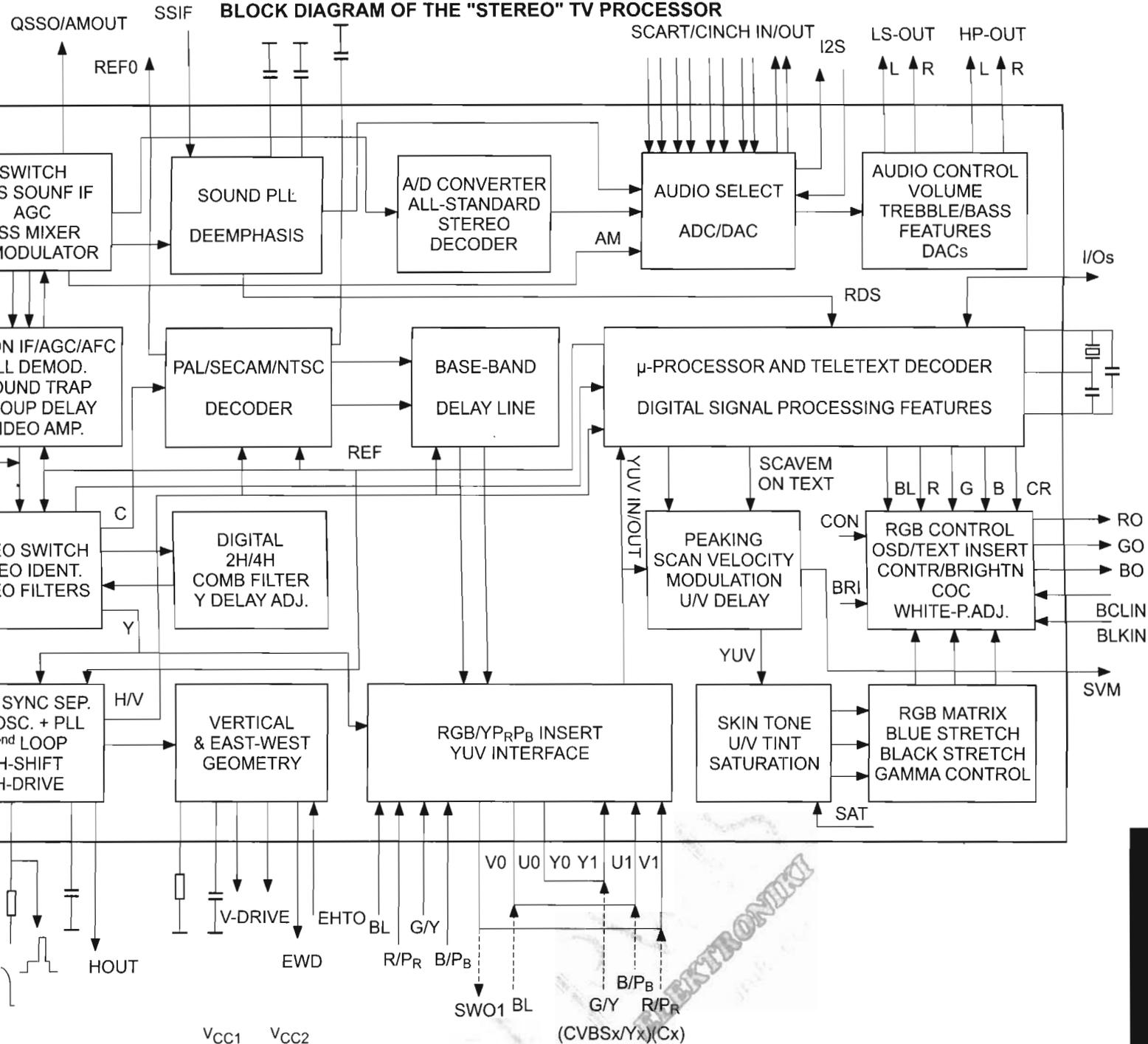
CHASSIS : CP-520  
MODEL : DTY-28A8/28A6/28A7  
DTY-2880/28W5  
DTY-21U7/21B4  
DTY-25G6/25G7

CHASSIS : CP-520A  
MODEL : DTC-20T2  
DTC-21Y1/21Y2

CHASSIS : CP-520F  
MODEL : DTZ-29G2K  
DTZ-29U7/U3  
DTZ-2881/28W8  
DTZ-2481

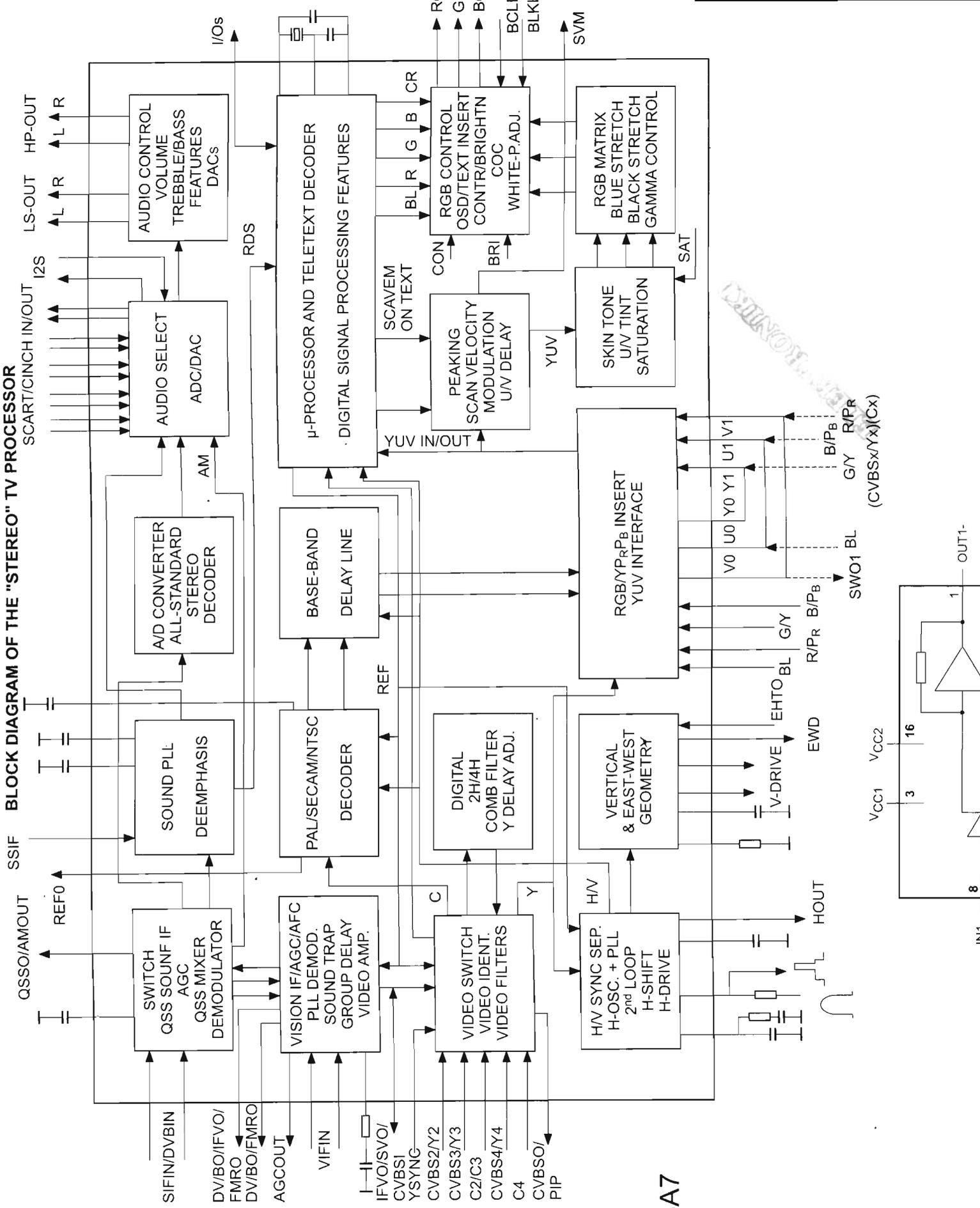


# BLOCK DIAGRAM OF THE "STEREO" TV PROCESSOR



CD 500/4/E

**BLOCK DIAGRAM OF THE "STEREO" TV PROCESSOR**



QSSO/AMOUT  
SSIF  
REF0

SIFIN/DV BIN  
DV/BO/IF/VO  
FMRO  
DV/BO/FMRO

AGCOUT  
VIFIN  
I-II  
IFVO/SVO/  
CVBS1  
YSYNG

CVBS2/Y2  
CVBS3/Y3  
C2/C3  
CVBS4/Y4  
C4  
CVBSO/  
PIP

REF  
RDS

YUV IN/OUT  
SCAVEM  
ON TEXT  
CON  
BRI

BL R G B CR  
RGB CONTROL  
OSD/TEXT INSERT  
CONTR/BRIGHTN  
COC  
WHITE-P.ADJ.

YUV  
SKIN TONE  
UV TINT  
SATURATION

RGB/Y/P/B INSERT  
YUV INTERFACE  
V0 U0 Y0 Y1  
U1 V1  
B/P/B  
GY R/P/R  
(CVBSx/Yx)(Cx)

SWO1 BL  
R/P/R B/P/B  
GY

V-DRIVE  
EHTO BL  
GY  
R/P/R B/P/B

EWD  
HOUT

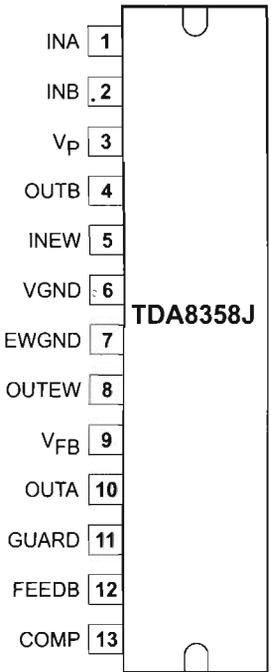
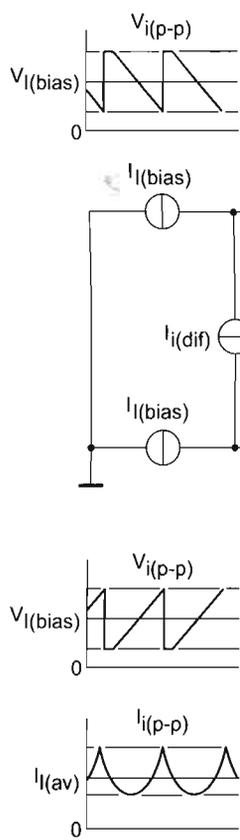
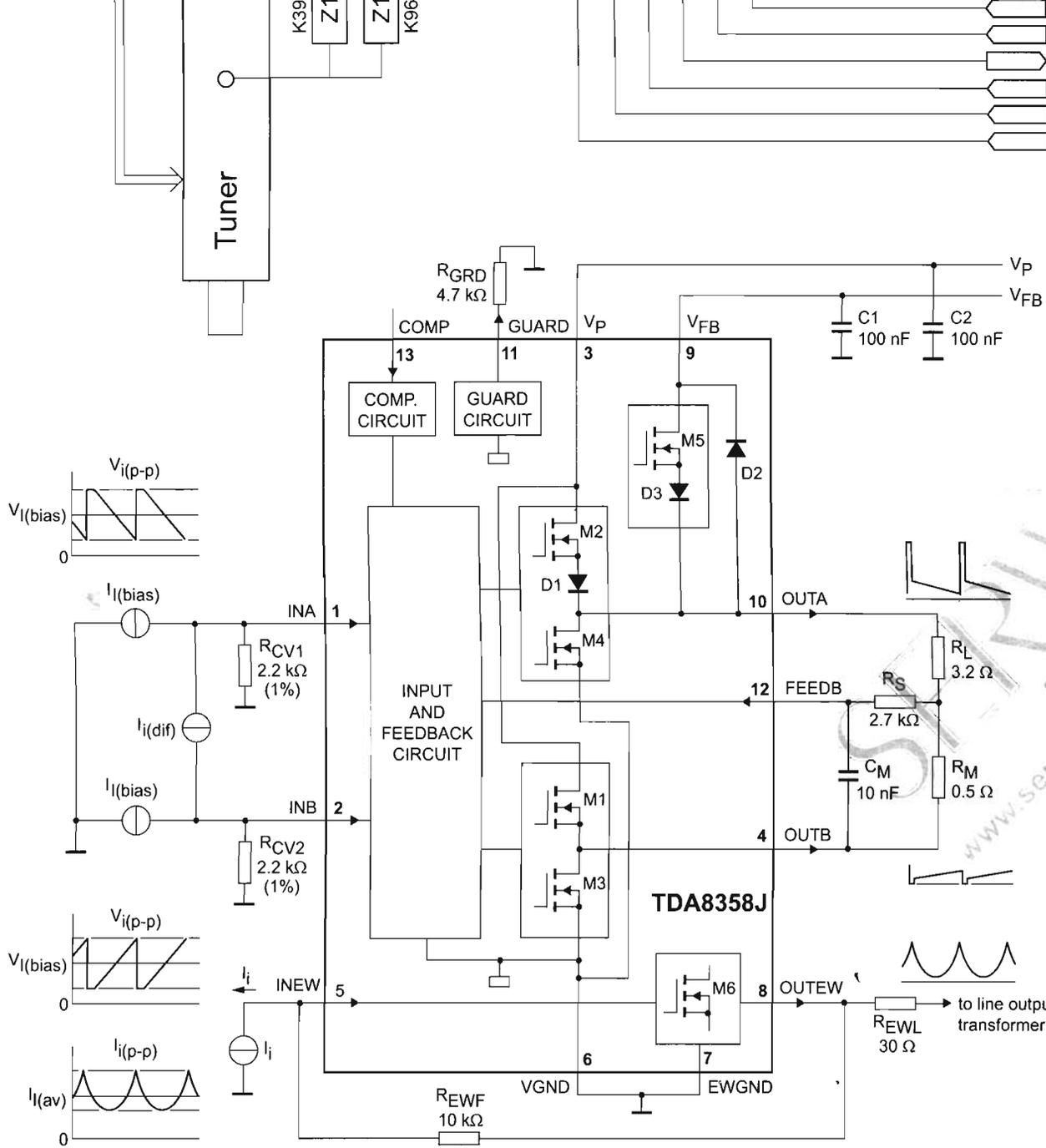
VCC1 VCC2  
3 16  
8

OUT1-

8/28A6/28A7  
J/28W5  
7/21B4  
6/25G7

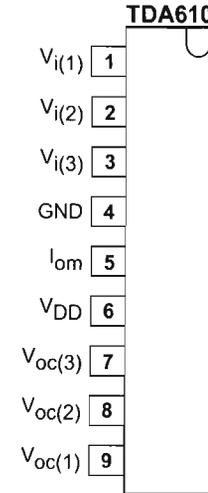
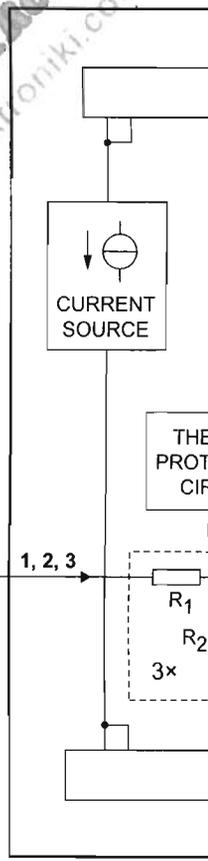
2  
1/21Y2  
2K  
7/U3  
1/28W8

CVBS/Y In Scart2  
 Chroma In Scart2  
 CVBS Out Scart2  
 CVBS/Y In AV3  
 Chroma In AV3  
 RGB In Scart1



**PINNING**

SYMBOL	PIN	DESCRIPTION
INA	1	Positive vertical input
INB	2	Negative vertical input
VP	3	supply voltage
OUTB	4	Vertical output voltage B
INEW	5	east-west input voltage
VGND	6	vertical ground
EWGND	7	east-west ground
OUTEW	8	east-west output voltage
VFB	9	flyback supply voltage
OUTA	10	Vertical output voltage A
GUARD	11	guard output voltage
FEEDB	12	measuring resistor
COMP	13	compensation input current

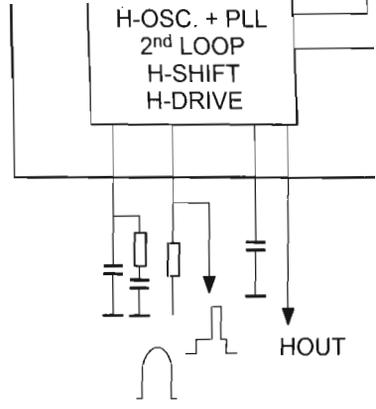


CVBS/Y In Scart2  
 Chroma In Scart2  
 CVBS Out Scart2  
 CVBS/Y In AV3  
 Chroma In AV3  
 RGB In Scart1

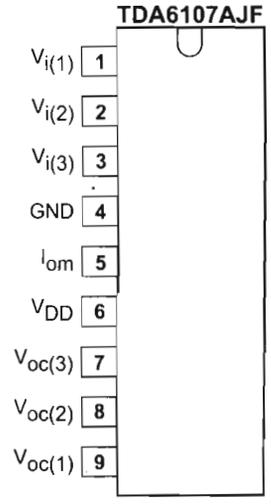
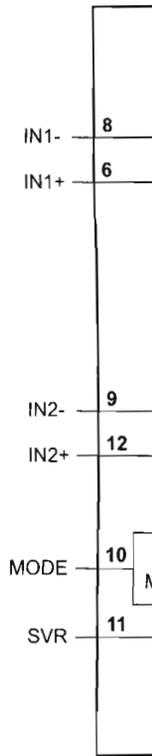
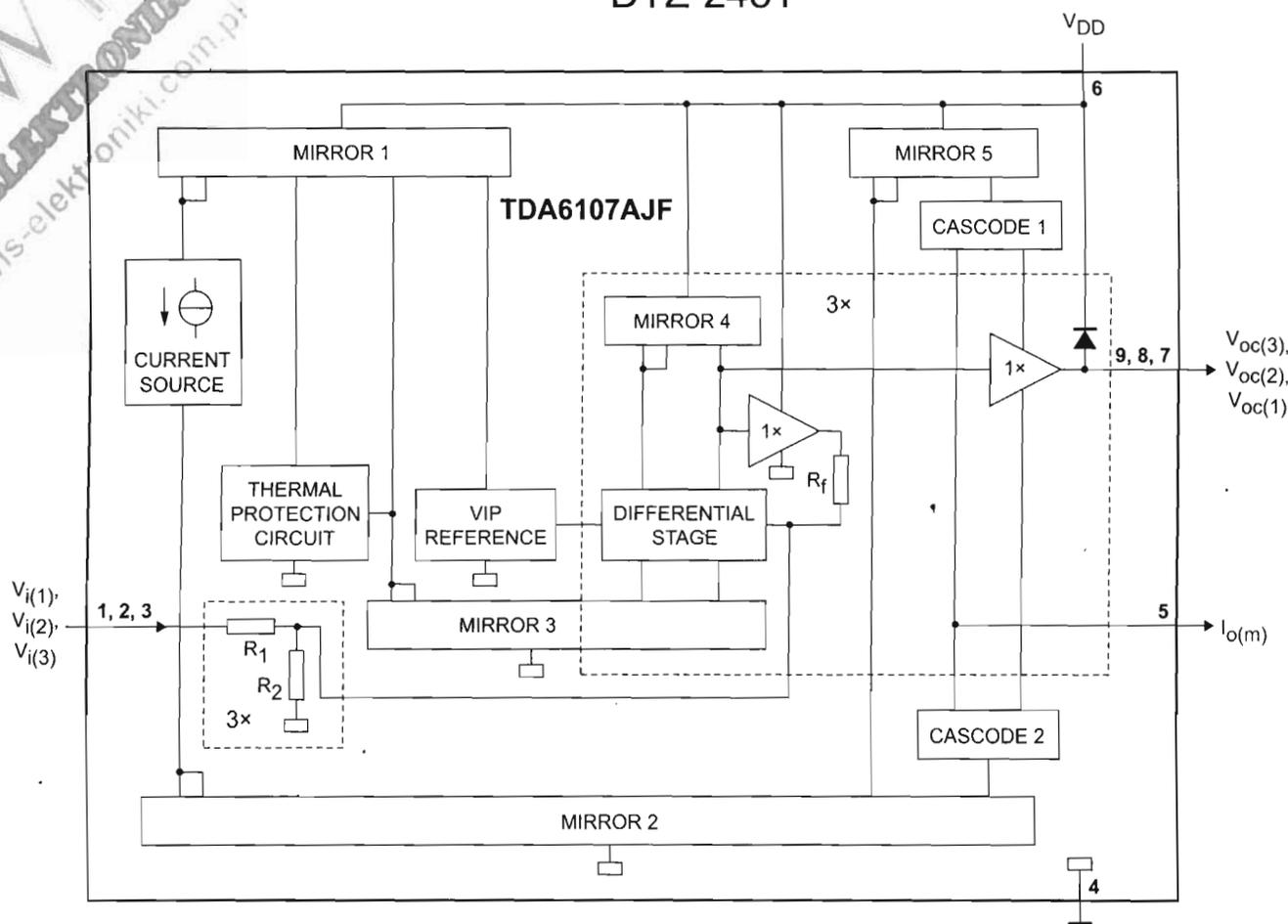
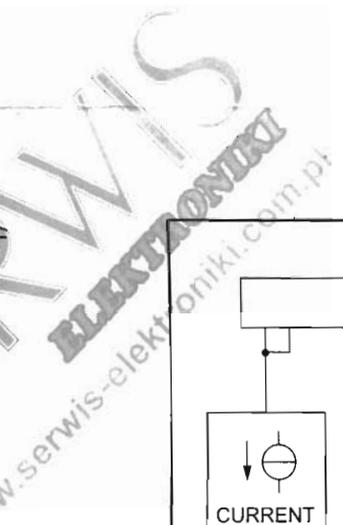
**CHASSIS : CP-520**  
**MODEL :** DTY-28A8/28A6/28A7  
 DTY-2880/28W5  
 DTY-21U7/21B4  
 DTY-25G6/25G7

**CHASSIS : CP-520A**  
**MODEL :** DTC-20T2  
 DTC-21Y1/21Y2

**CHASSIS : CP-520F**  
**MODEL :** DTZ-29G2K  
 DTZ-29U7/U3  
 DTZ-2881/28W8  
 DTZ-2481



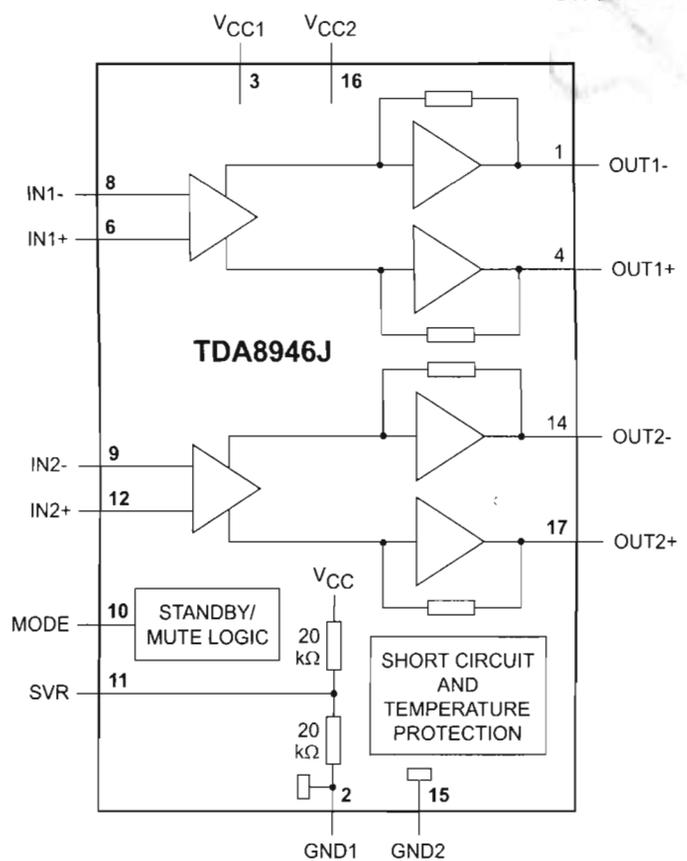
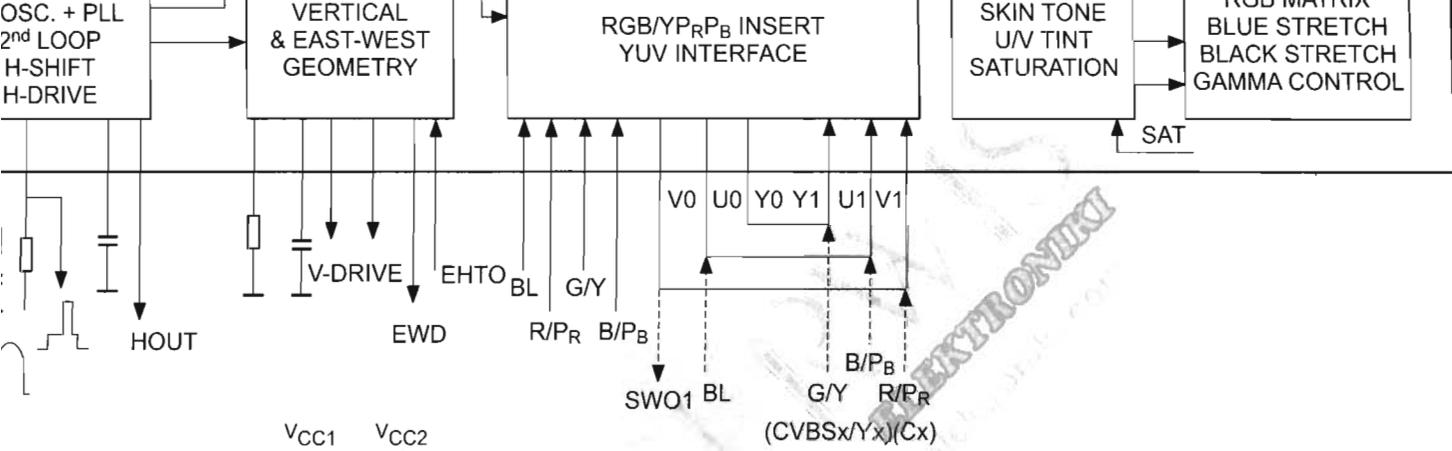
Vp  
 VFB



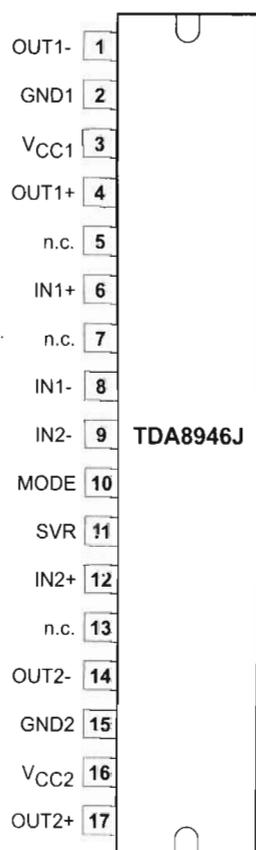
**PINNING**

SYMBOL	PIN	DESCRIPTION
$V_{i(1)}$	1	inverting input1
$V_{i(2)}$	2	inverting input2
$V_{i(3)}$	3	inverting input3
GND	4	ground (fin)
$I_{om}$	5	black-current measurement output
$V_{DD}$	6	supply voltage
$V_{oc(3)}$	7	cathode output 3
$V_{oc(2)}$	8	cathode output 2
$V_{oc(1)}$	9	cathode output 1

Symbol	
OUT1-	
GND1	
$V_{CC1}$	
OUT1+	
n.c.	
IN1+	
n.c.	
IN1-	
IN2-	
MODE	
SVR	
IN2+	
n.c.	
OUT2-	
GND2	
$V_{CC2}$	
OUT2+	



Symbol	Pin	Description
OUT1-	1	negative loudspeaker terminal 1
GND1	2	ground channel 1
V <sub>CC1</sub>	3	supply voltage channel 1
OUT1+	4	positive loudspeaker terminal 1
n.c.	5	not connected
IN1+	6	positive input 1
n.c.	7	not connected
IN1-	8	negative input 1
IN2-	9	negative input 2
MODE	10	mode selection input (standby, mute, operating)
SVR	11	half supply voltage decoupling (ripple rejection)
IN2+	12	positive input 2
n.c.	13	not connected
OUT2-	14	negative loudspeaker terminal 2
GND2	15	ground channel 2
V <sub>CC2</sub>	16	supply voltage channel 2
OUT2+	17	positive loudspeaker terminal 2



OTYC D...: CD 500/1...