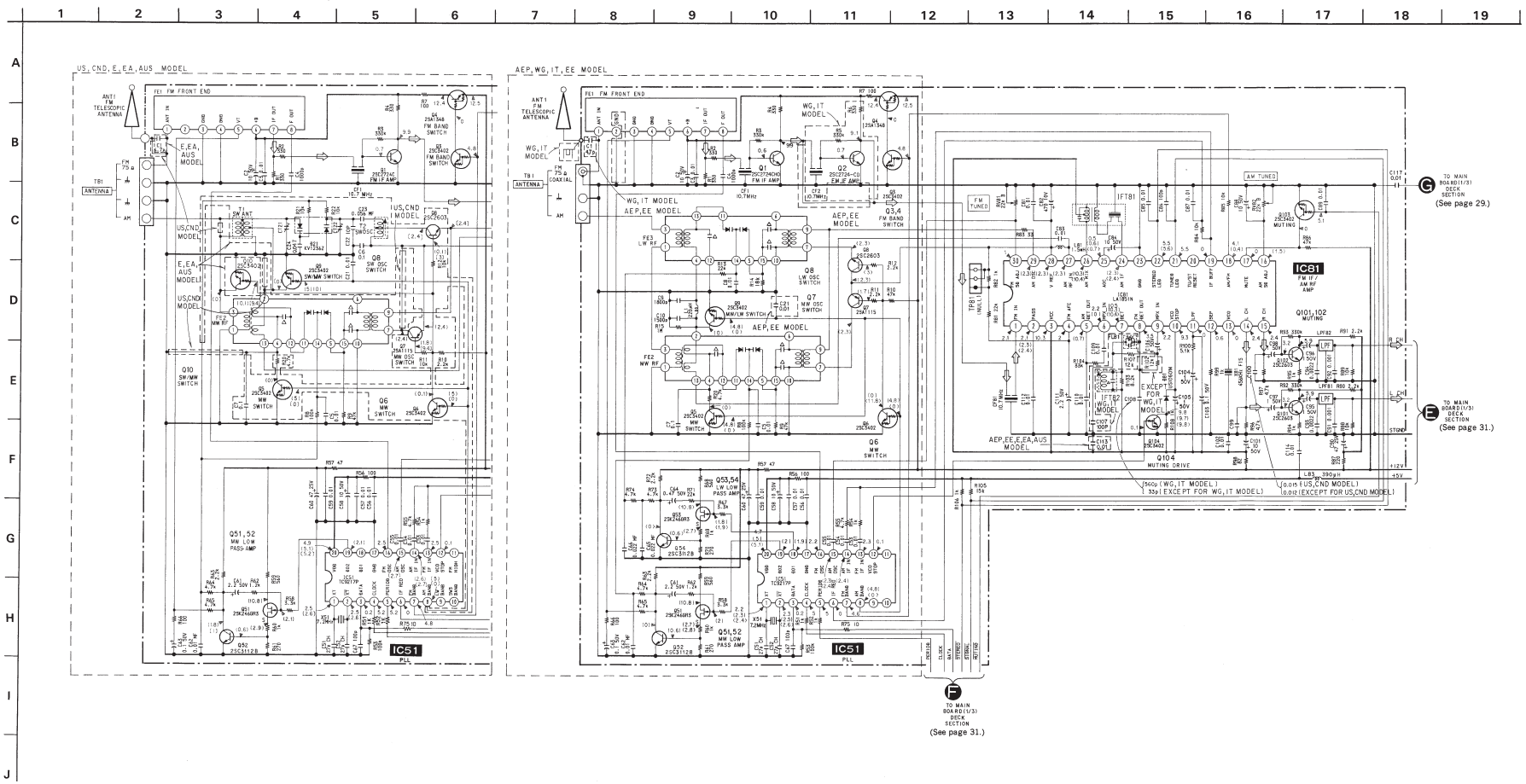
6-4. SCHEMATIC DIAGRAM—Tuner Section—

Refer to page 39 for IC Block Diagrams.
Refer to page 42 for FE1 FM Front End.

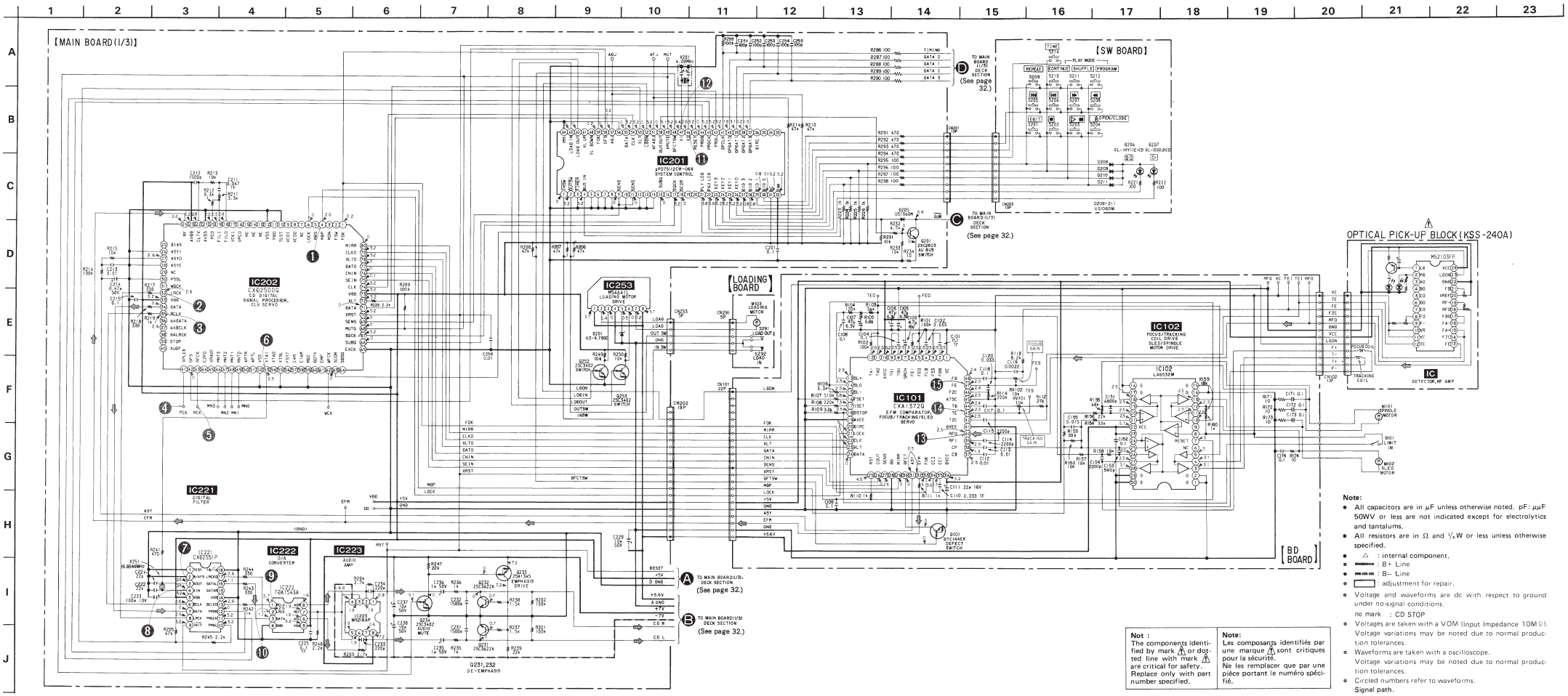


Note:

- All capacitors are in μF unless otherwise noted. pF: μμF 50WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and ¼W or less unless otherwise specified.
- ▬▬▬ : B+ Line
- ▬▬▬ : B− Line
- ▭ : adjustment for repair.
- Voltage is dc with respect to ground under no-signal (detuned) conditions.
  no mark : FM
  ( ) : MW
  < > : LW
  [ ] : SW
- Voltages are taken with a VOM (Input Impedance 10MΩ). Voltage variations may be noted due to normal production tolerances.
- Signal path.
  ⇨ : FM
- CND : Canadian model
  WG : West Germany model
  IT : Italian model
  EE : East European model
  EA : Saudi Arabia model
  AUS : Australian model

6-5. SCHEMATIC DIAGRAM—CD Section—

• Refer to page 39 for IC Block Diagrams.



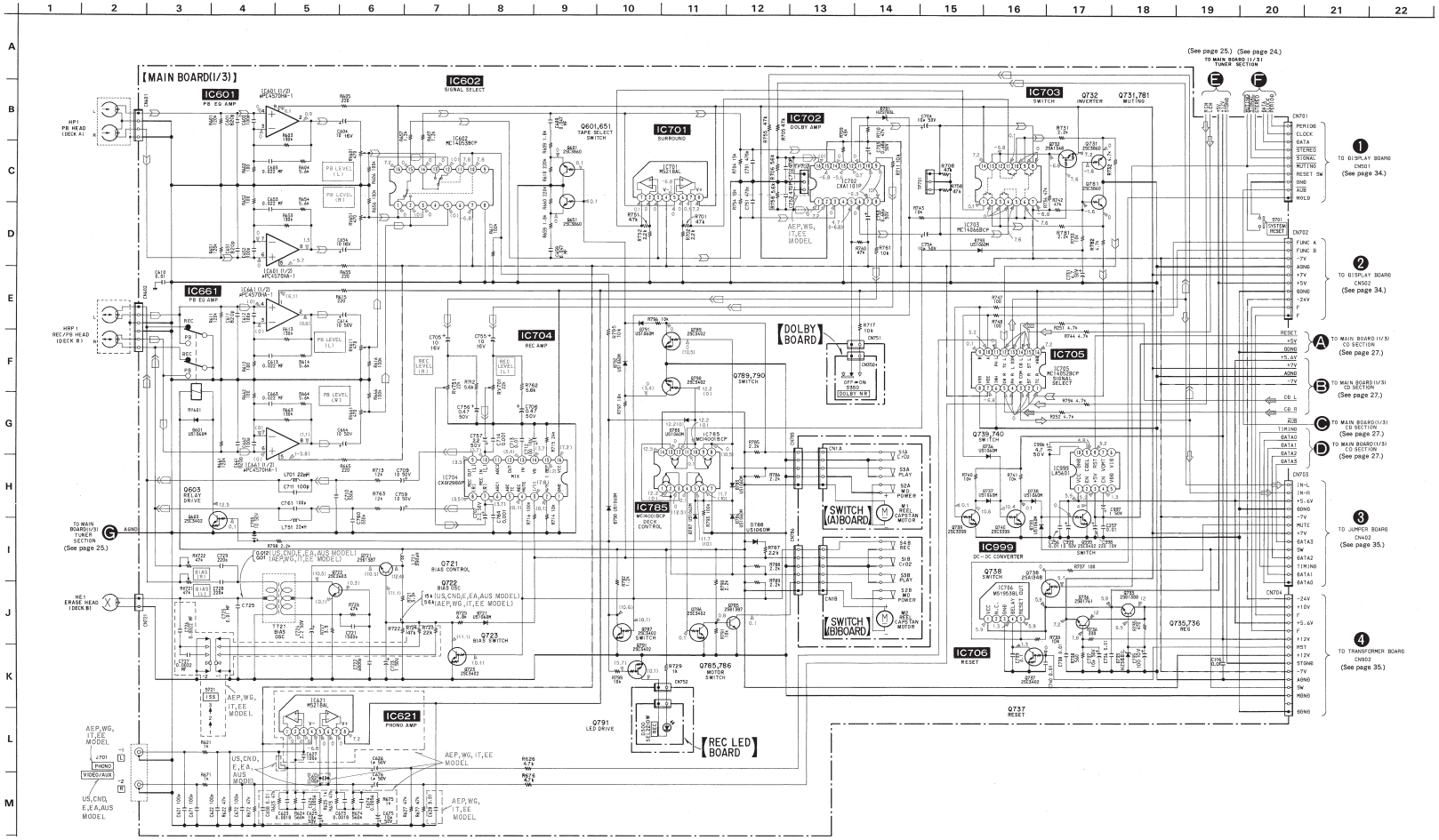
Note:

- All capacitors are in μF unless otherwise noted. pF: μμF 50WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and ⅙W or less unless otherwise specified.
- △ : internal component.
- ▬▬ : B+ Line
- ▬▬ : B− Line
- ▭ : adjustment for repair.
- Voltage and waveforms are dc with respect to ground under no-signal conditions.
  no mark : CD STOP
- Voltages are taken with a VOM (Input Impedance 10MΩ). Voltage variations may be noted due to normal production tolerances.
- Waveforms are taken with a oscilloscope. Voltage variations may be noted due to normal production tolerances.
- Circled numbers refer to waveforms.
  Signal path.
  ⇨ : CD

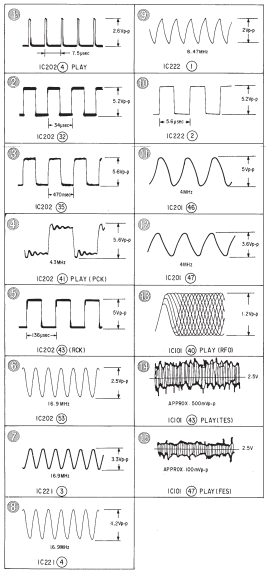
Not :	Note:
The components identified by mark ⚠ or dotted line with mark ⚠ are critical for safety. Replace only with part number specified.	Les composants identifiés par une marque ⚠ sont critiques pour la sécurité. Ne les remplacer que par une pièce portant le numéro spécifié.

6-6. SCHEMATIC DIAGRAM—Deck Section—

● Refer to page 39 for IC Block Diagrams.



● Waveforms



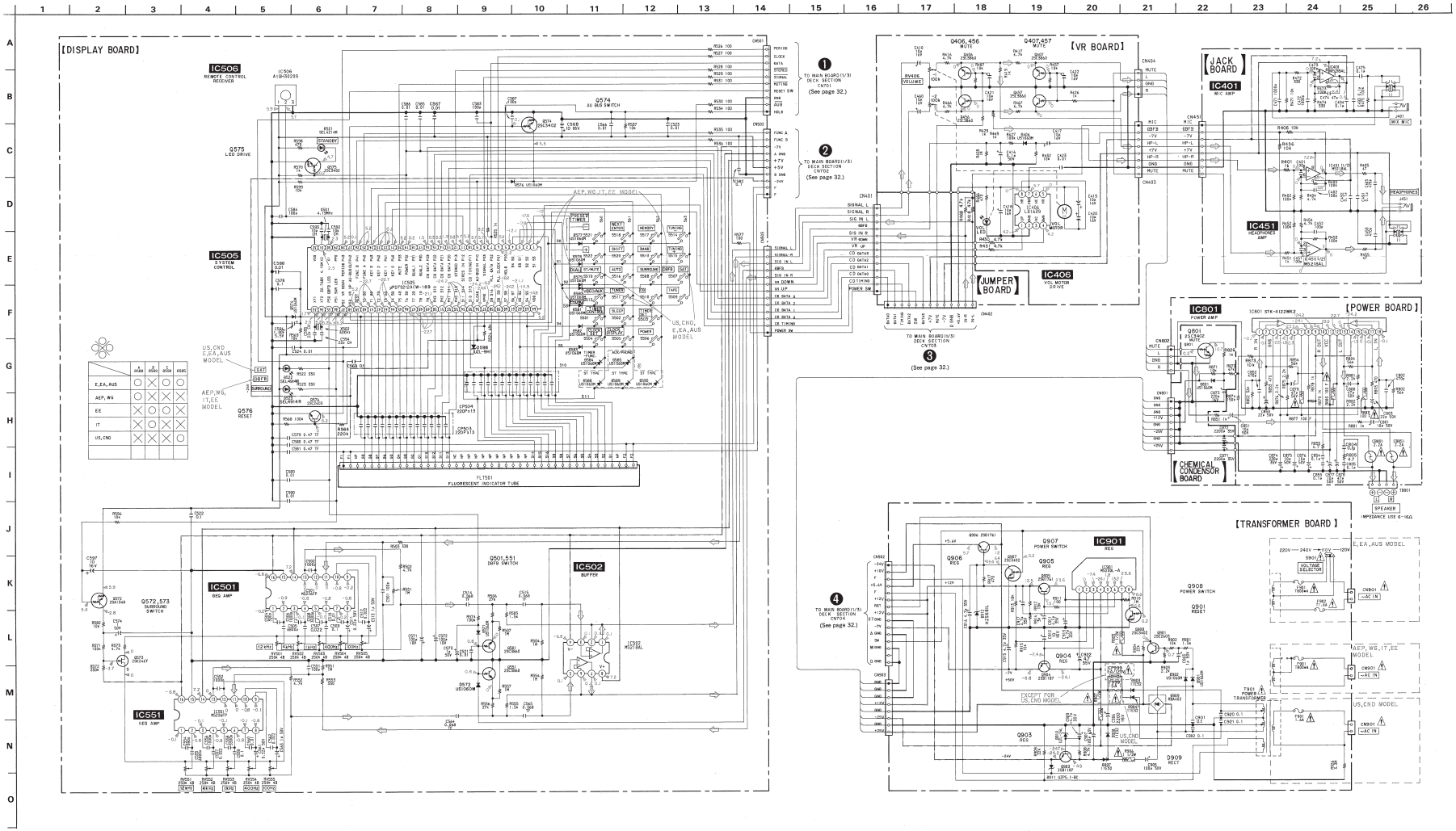
Note:

- All capacitors are in μF unless otherwise noted. pF : μμF 50WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and ¼W or less unless otherwise specified.
- ▬▬ : B+ Line
- ▬▬ : B− Line
- ▭ : adjustment for repair.
- Voltage is dc with respect to ground under no-signal conditions.
  no mark : POWER ON
  ( ) : PLAY (DECK A)
  < > : REC
- Voltages are taken with a VOM (Input impedance 10MΩ). Voltage variations may be noted due to normal production tolerances.
- **Signal path.**
  ⇨ : FM
  ⇨ : PB (DECK A)
  ⇨ : CD
  ⇨ : PB (DECK B)
  ⇨ : REC

CND : Canadian model
WG : West Germany model
IT : Italian model
EE : East European model
EA : Saudi Arabia model
AUS : Australian model

6-7. SCHEMATIC DIAGRAM—Power/Amplifier/Display Section—

● Refer to page 39 for IC Block Diagrams.



● Semiconductor Location

Ref. No.	Location	Ref. No.	Location
D406	G-13	IC502	G-9
D521	F-2	IC505	H-5
D522	E-2	IC506	F-2
D523	E-2	IC551	F-5
D571	G-4	IC801	C-14
D572	G-7	IC901	C-7
D574	I-7		
D576	I-6	Q406	H-13
D577	H-7	Q407	G-14
D578	H-7	Q456	H-13
D579	H-7	Q457	G-14
D580	H-3	Q501	G-8
D581	H-3	Q551	G-8
D582	H-3	Q572	G-4
D583	H-3	Q573	F-7
D584	H-4	Q574	I-4
D585(※1)	H-3	Q575	G-2
D588(※2)	H-3	Q576	H-4
D589(※3)	H-3	Q801	C-10
D590(※4)	H-3	Q901	A-8
D598	I-7	Q903	D-6
D801	C-10	Q904	D-6
D901	B-8	Q905	D-7
D902	B-8	Q906	D-8
D903	C-4	Q907	C-8
D904	C-5	Q908	C-8
D907	C-6		
D908	C-5		
D909	B-8		
D910	C-6		
D911	D-6		
D912	C-8		
IC401	I-13		
IC406	G-12		
IC451	J-13		
IC501	F-3		

※1 : Used on US, CND, E,EA and AUS model.
※2 : Used on AEP, WG, E, EA and AUS model.
※3 : Used on IT and EE model.
※4 : Used on EE, E, EA and AUS model.

Note:

- All capacitors are in μF unless otherwise noted. pF: μμF 50WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and ¼W or less unless otherwise specified.
- ⚠ : fusible resistor.

Note:	Note:
The components identified by mark ⚠ or dotted line with mark ⚠ are critical for safety. Replace only with part number specified.	Les composants identifiés par une marque ⚠ sont critiques pour la sécurité. Ne les remplacer que par une pièce portant le numéro spécifié.

- ▭ : B+ Line
- ▭ : B- Line
- Voltage is dc with respect to ground under no-signal conditions. no mark : POWER ON
- Voltages are taken with a VOM (Input Impedance 10MΩ). Voltage variations may be noted due to normal production tolerances.
- Signal path.
  ⇨ : FM
- CND : Canadian model
  WG : West Germany model
  IT : Italian model
  EE : East European model
  EA : Saudi Arabia model
  AUS : Australian model