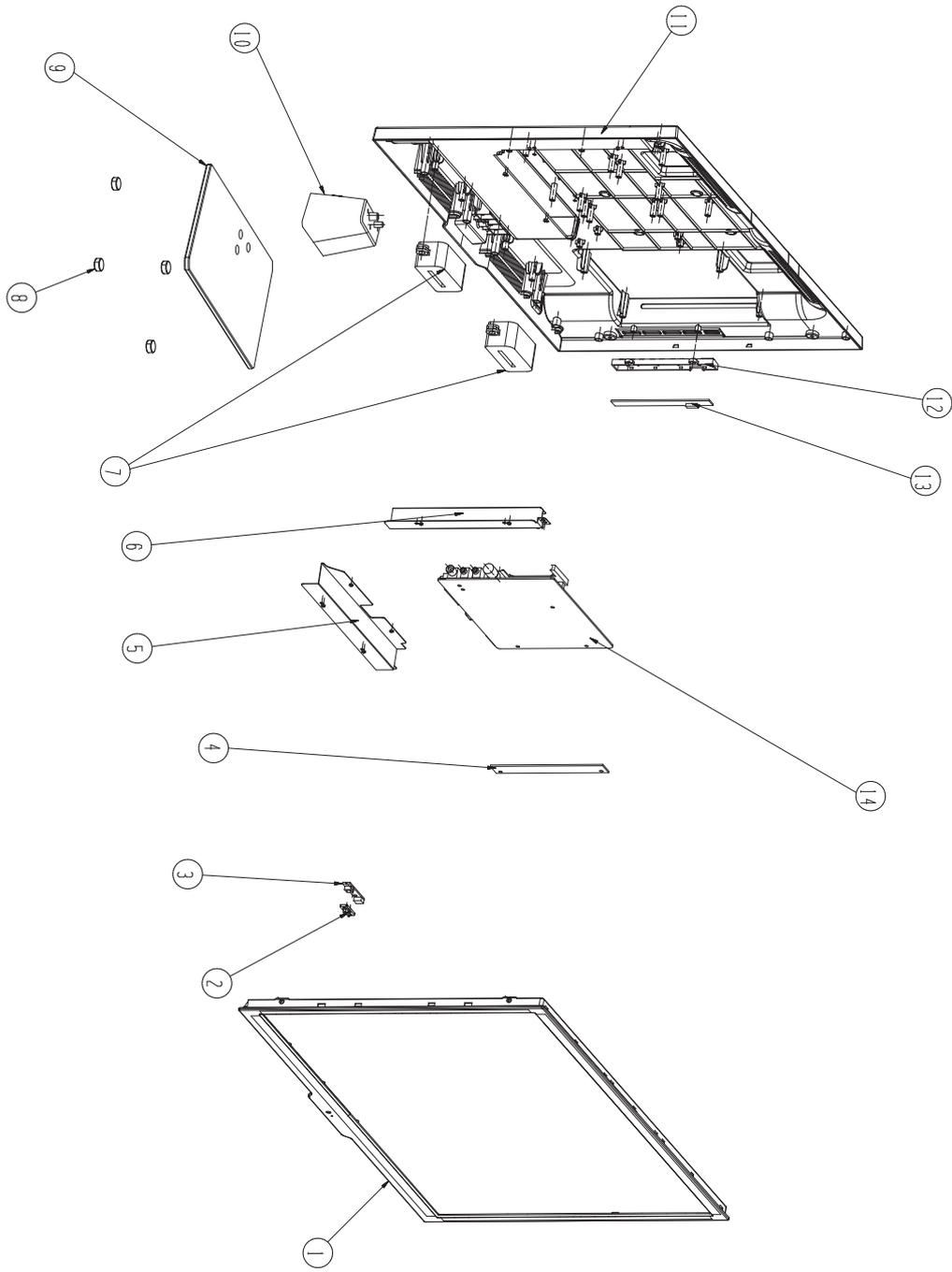


**LED COLOR TV
Service Manual**

Model No.: TLH19

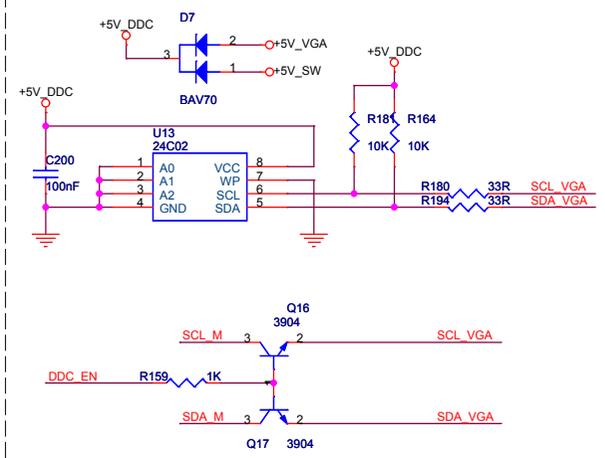
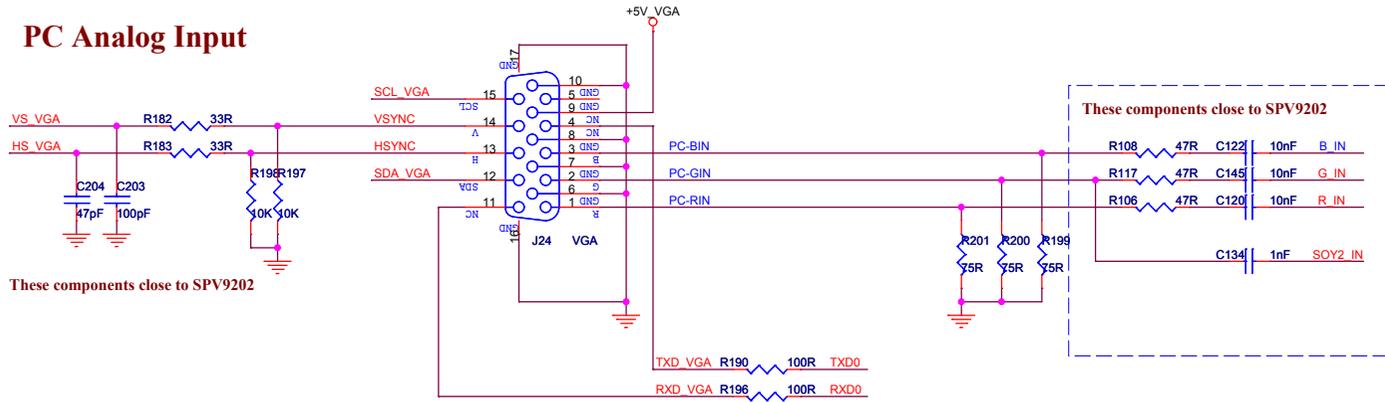
Exploded view and mechanical parts list



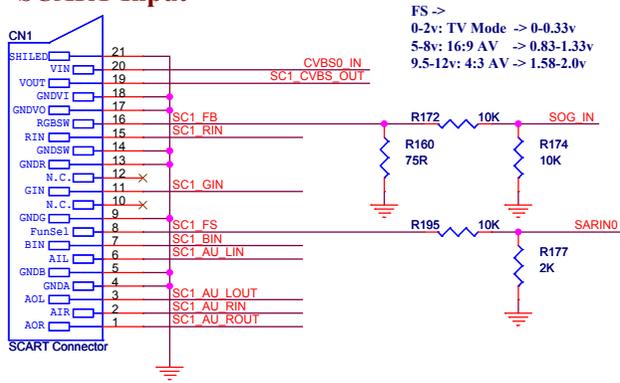
NO.	Part Name	QTY
1	PANEL MODULE	1
2	LAMP LENS	1
3	TV REMOTE ASS'Y	1
4	INVERTER	1
5	TERMINAL BASE	1
6	TERMINAL BASE	1
7	SPEAKER	2
8	RUBBER PAD CORD	4
9	GLASS BASE	1
10	DISPLAY BRACKET	1
11	TV CAB BLACK	1
12	FUNCTION KEY ASS'Y	1
13	TV CONTROL KEY PCB ASS'Y	1
14	DRIVE PCB	1

Driver PCB Principle Diagram

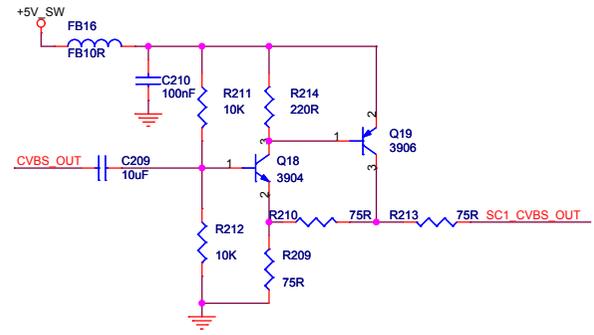
PC Analog Input



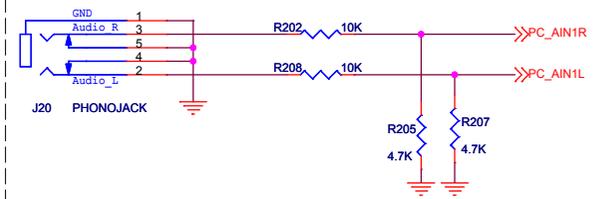
SCART Input



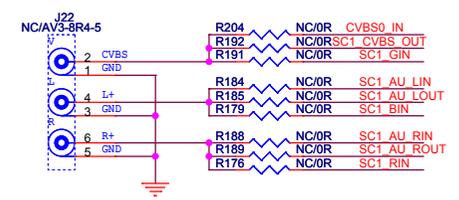
SCART_CVBS_OUT



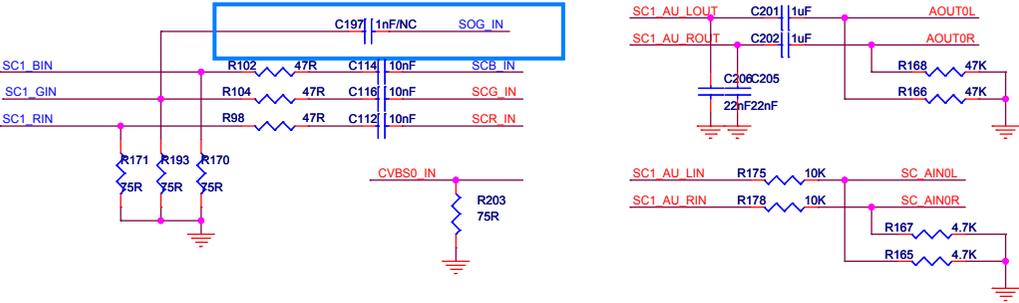
PC AUDIO



AV IN /AV OUT /YPbPr

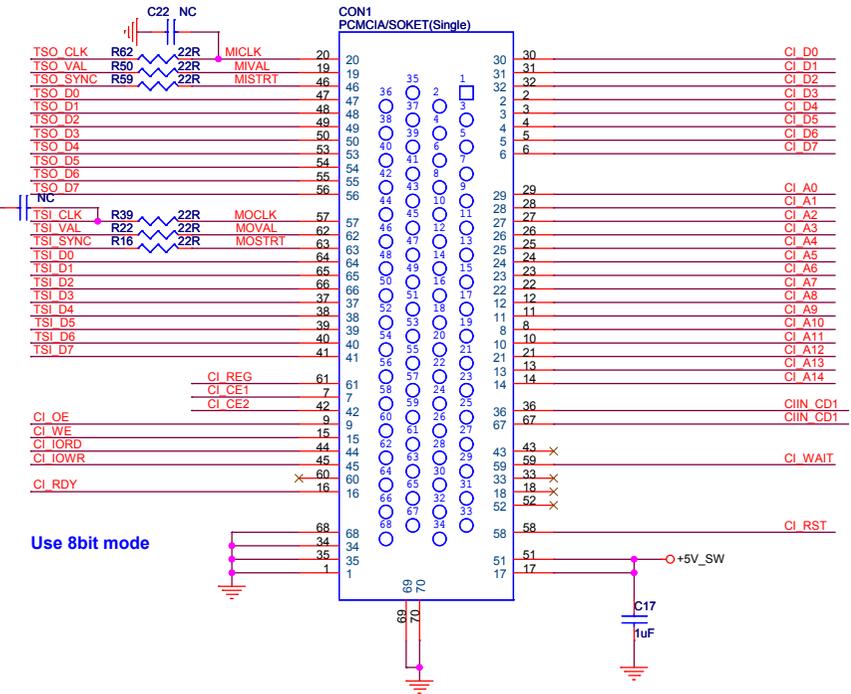


These components close to SPV9202

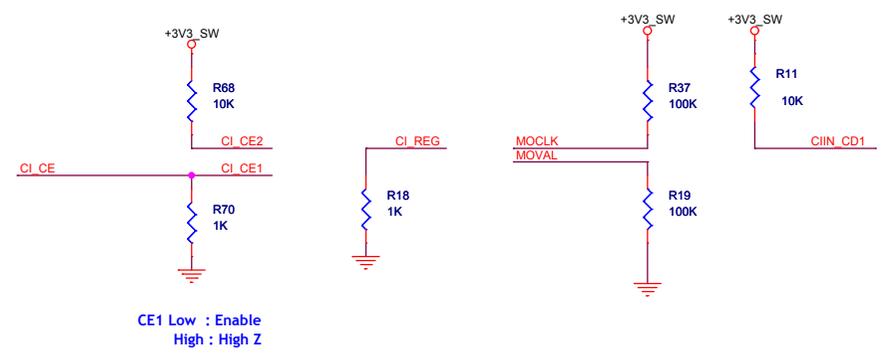


DDC_EN	>> DDC_EN	7,9,11
SCL_M	>> SCL_M	7,9,11
SDA_M	>> SDA_M	7,9,11
CVBS_OUT	<< CVBS_OUT	11
SC AIN0R	<< SC AIN0R	8
SC AIN0L	<< SC AIN0L	8
AOUT0L	<< AOUT0L	11
AOUT0R	<< AOUT0R	11
SARIN0	<< SARIN0	11
SOG_IN	<< SOG_IN	11
B_IN	>> B_IN	11
G_IN	>> G_IN	11
R_IN	>> R_IN	11
VS_VGA	>> VS_VGA	11
HS_VGA	>> HS_VGA	11
TXD0	>> TXD0	7,9,11
RXD0	>> RXD0	7,9,11
SOY2_IN	>> SOY2_IN	7,9,11
SCB_IN	>> SCB_IN	7,9,11
SCG_IN	>> SCG_IN	11
SCR_IN	>> SCR_IN	11
CVBS0_IN	>> CVBS0_IN	7,9,11

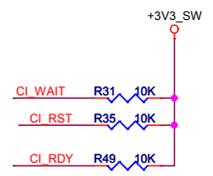
CI 靠近CPU放



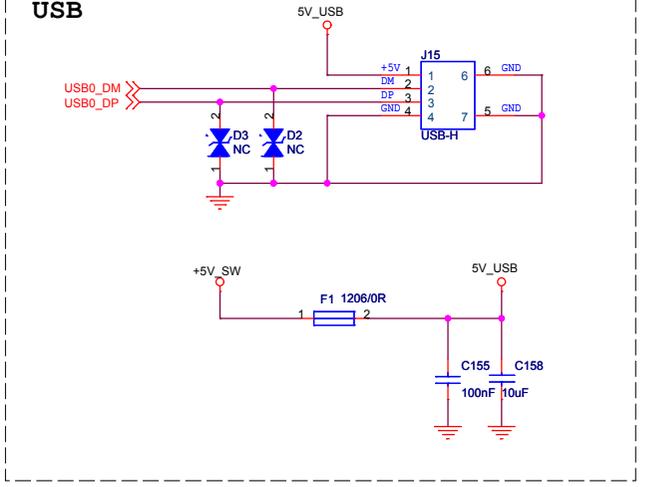
靠近CI卡座放



CE1 Low : Enable
High : High Z

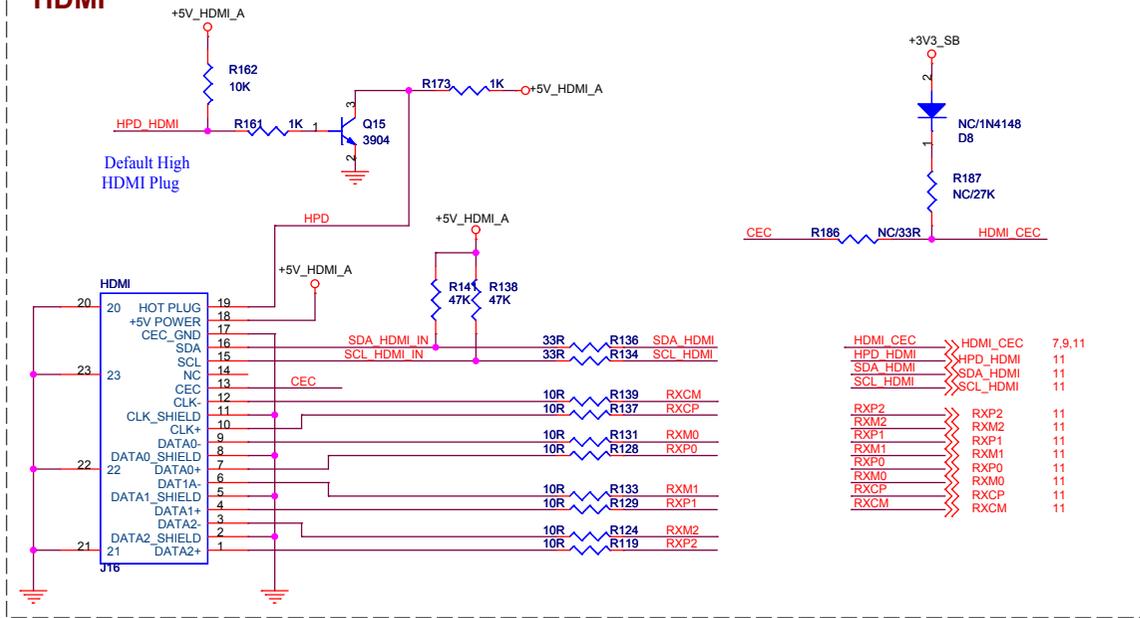


USB

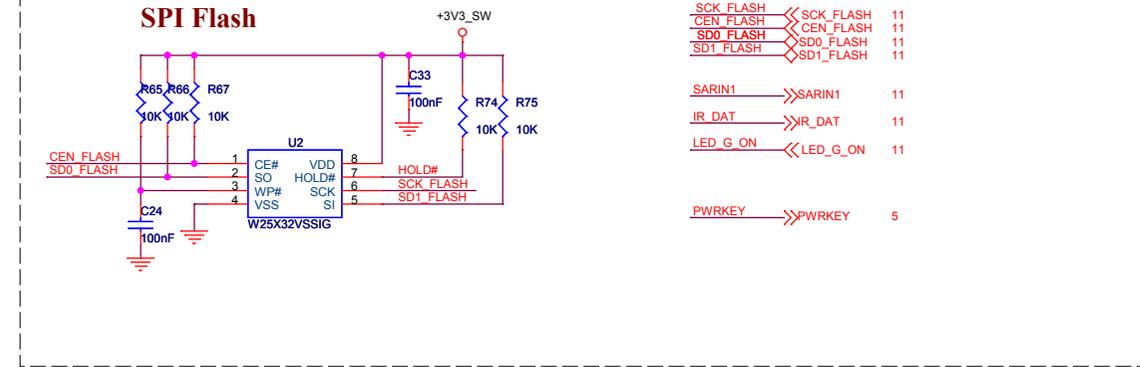


- CI_A[0..14] 11
- CI_D[0..7] 11
- CIIN_CD1 >> CIIN_CD1 11
- CI_WE >> CI_WE 11
- CI_OE >> CI_OE 11
- CI_IORD >> CI_IORD 11
- CI_IOWR >> CI_IOWR 11
- CI_WAIT >> CI_WAIT 11
- CI_RST >> CI_RST 11
- CI_RDY >> CI_RDY 11
- CI_CE >> CI_CE 11
- TSI_D[0..7] 11
- TSO_SYNC >> TSO_SYNC 11,15
- TSO_CLK >> TSO_CLK 11,15
- TSO_VAL >> TSO_VAL 11,15
- TSI_SYNC >> TSI_SYNC 11
- TSI_CLK >> TSI_CLK 11
- TSI_VAL >> TSI_VAL 11

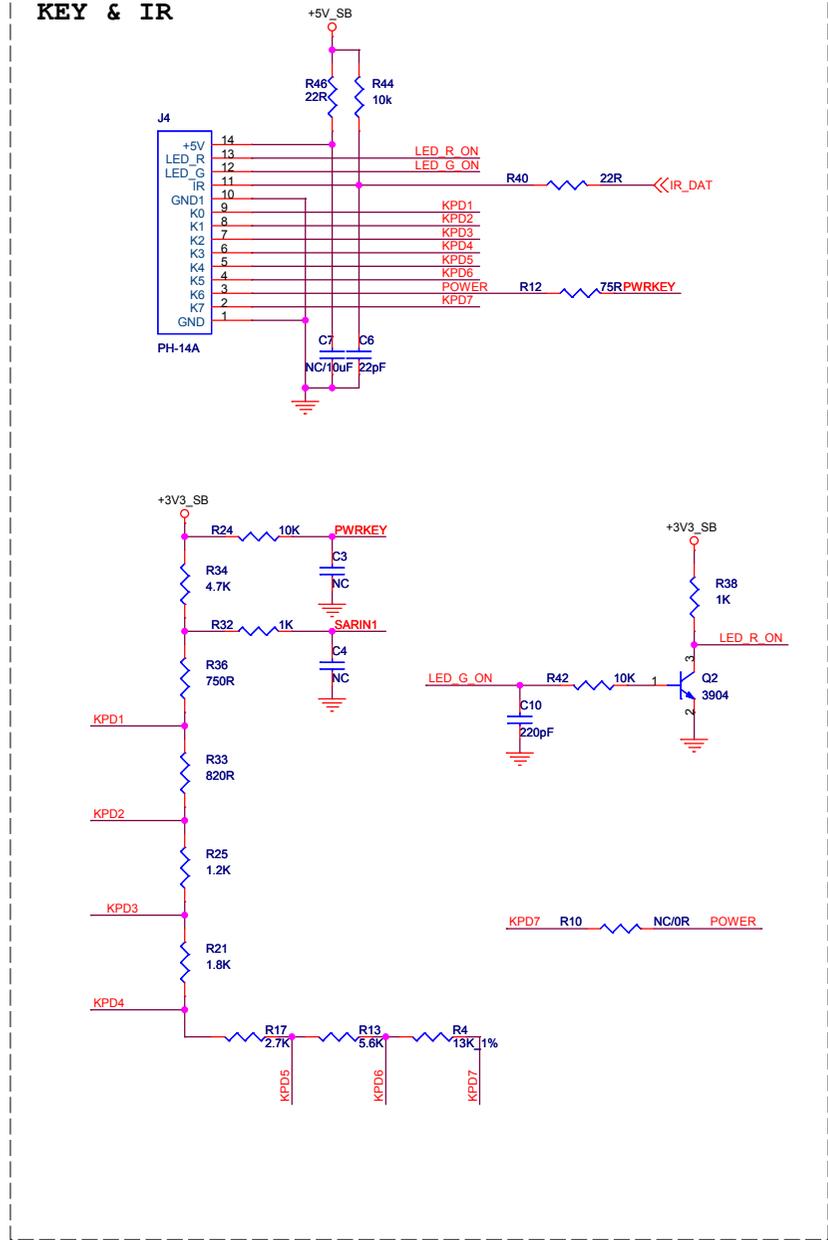
HDMI



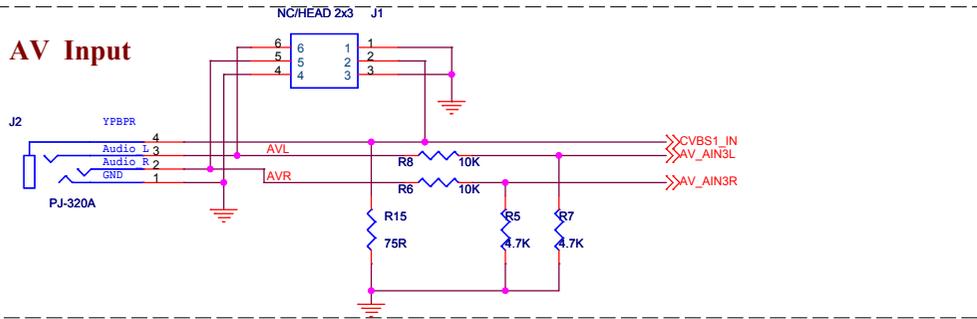
SPI Flash



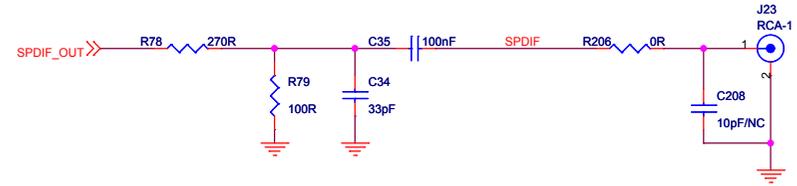
KEY & IR



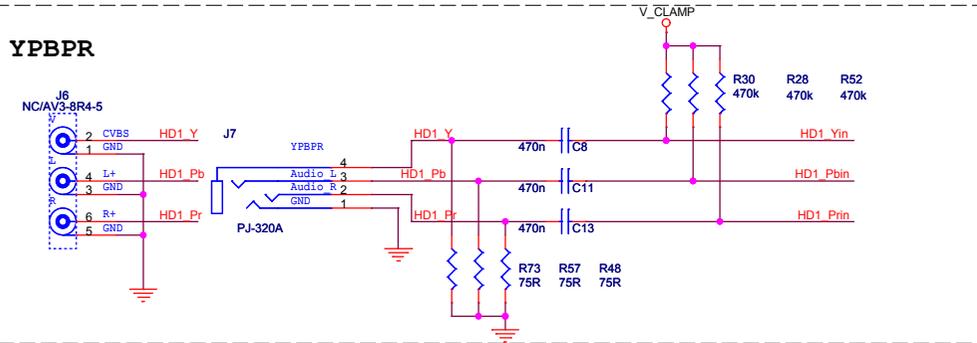
AV Input



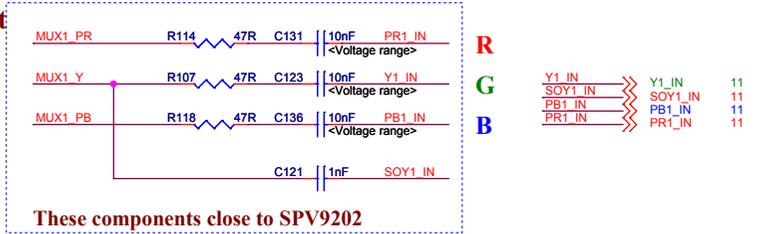
COAX



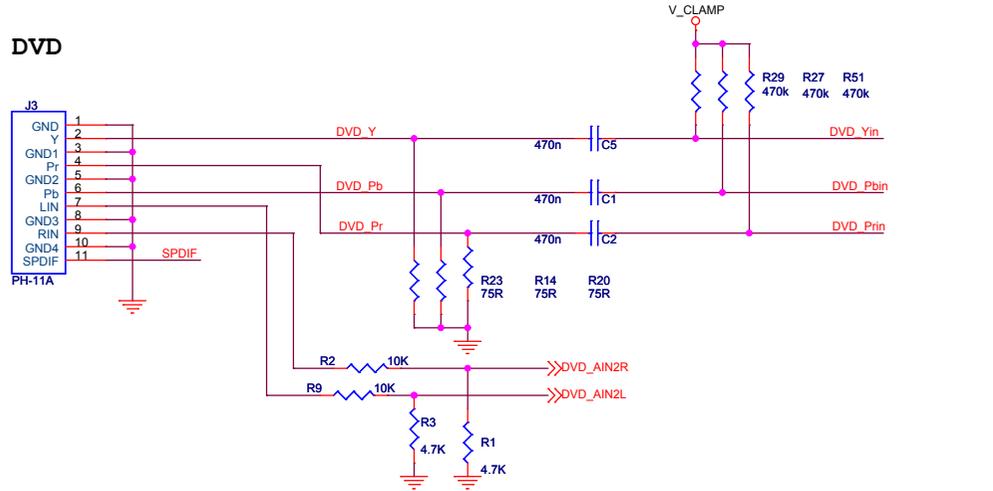
YPBPR



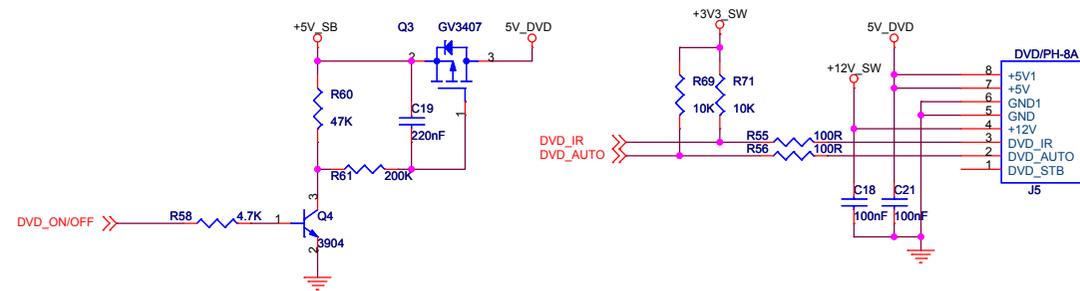
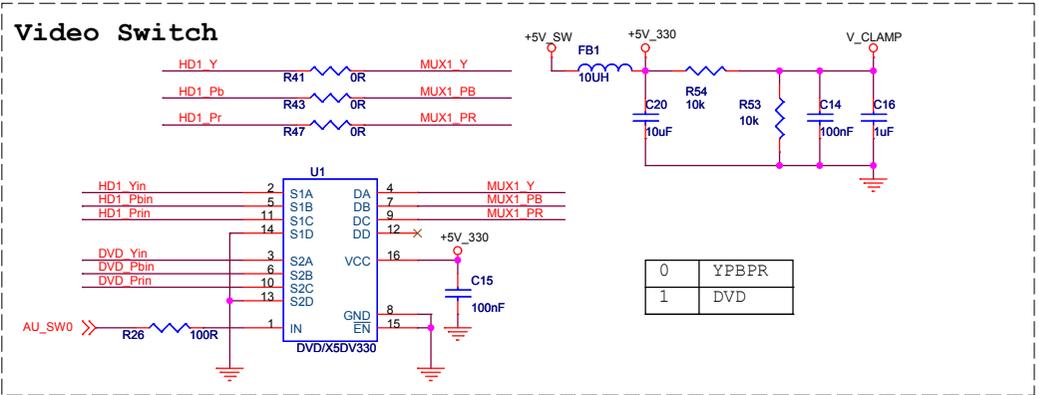
YUV Input

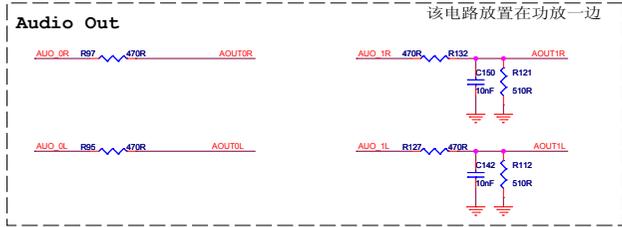
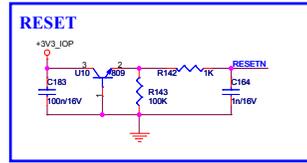
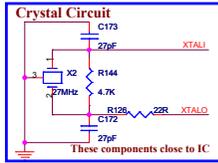


DVD



Video Switch





DDR Interface	
10 A_DDR3_M0[0..15]	
10 A_DDR3_M0[0..13]	
10 A_DDR3_ODT	A_DDR3_ODT
10 A_DDR3_MWE_B	A_DDR3_MWE_B
10 A_DDR3_MCAS_B	A_DDR3_MCAS_B
10 A_DDR3_MRAS_B	A_DDR3_MRAS_B
10 A_DDR3_DQSH	A_DDR3_DQSH
10 A_DDR3_DQSH#	A_DDR3_DQSH#
10 A_DDR3_DQSL#	A_DDR3_DQSL#
10 A_DDR3_DQSH#	A_DDR3_DQSH#
10 A_DDR3_DQML	A_DDR3_DQML
10 A_DDR3_DQML#	A_DDR3_DQML#
10 A_DDR3_CLK	A_DDR3_CLK
10 A_DDR3_CLK#	A_DDR3_CLK#
10 A_DDR3_CKE	A_DDR3_CKE
10 A_DDR3_MBA0	A_DDR3_MBA0
10 A_DDR3_MBA1	A_DDR3_MBA1
10 A_DDR3_MBA2	A_DDR3_MBA2
10 A_DDR3_MRESET_B	A_DDR3_MRESET_B

D-Sub	
5 HS_VGA	HS_VGA
5 VS_VGA	VS_VGA
5 B_IN	G_IN
5 G_IN	G_IN
5 R_IN	R_IN
5 SDG_IN	SDG_IN

HDMI	
5 SDA_HDMI	SDA_HDMI
5 SCL_HDMI	SCL_HDMI
5 HPD_HDMI	HPD_HDMI
5 RXCM	RXCM
5 RXCP	RXCP
5 RXM0	RXM0
5 RXM1	RXM1
5 RXP1	RXP1
5 RXM2	RXM2
5 RXP2	RXP2
5 HDMI_CEC	HDMI_CEC

SPI Flash	
9 SD_FLASH	SDI_FLASH
9 SD0_FLASH	SDI_FLASH
9 CE_FLASH	CE_FLASH
9 SCK_FLASH	SCK_FLASH

HDTV Input	
7 Y1_IN	Y1_IN
7 SOY1_IN	SOY1_IN
7 PR1_IN	PR1_IN
7 PR2_IN	PR2_IN

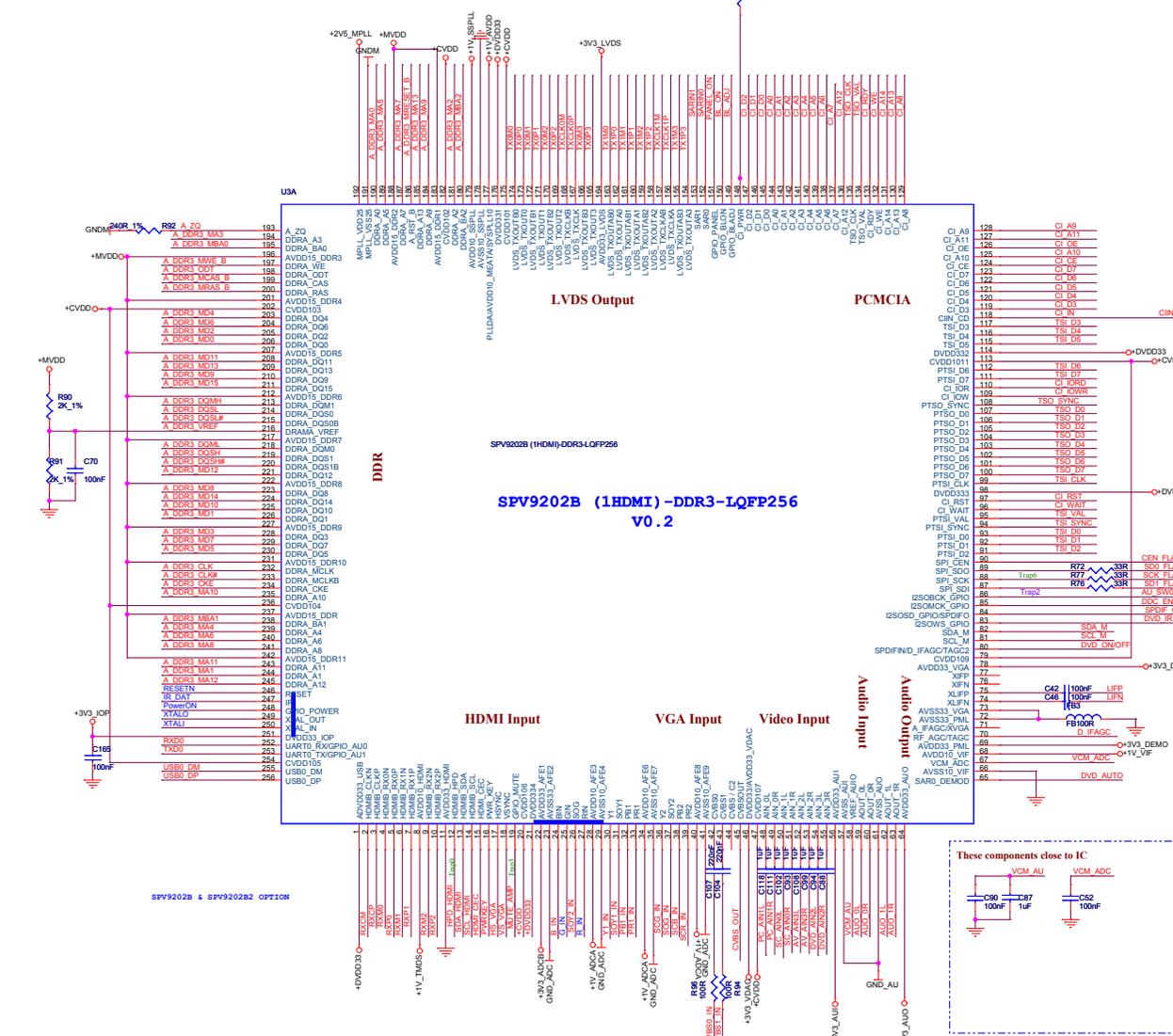
LS_ADC Input	
5 CVBS0_IN	CVBS0_IN
5 CVBS1_IN	CVBS1_IN

Audio Input	
AV_ANSL	AV_ANLR
AV_ANSL	AV_ANLR
SC_ANSL	SC_ANLR
SC_ANSL	SC_ANLR
PC_ANTR	PC_ANLR
PC_ANTR	PC_ANLR
DVD_ANTR	DVD_ANLR
DVD_ANTR	DVD_ANLR

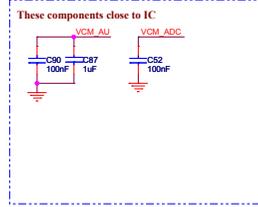
SAR_ADC Input	
5 SARND	SARND
5 SARNT	SARNT

USB Input	
9 USBDM	USBDM
6 USBDP	USBDP
5.7.9 RXD0	RXD0
5.7.9 TXD0	TXD0
5 IR_DAT	IR_DAT

6.15 TSO_D0[7]	
9 PWKEY	PWRKEY
14 MUTE_AMP	MUTE_AMP
6.15 TSO_SYNC	TSO_SYNC
6.15 TSO_VAL	TSO_VAL
6.15 TSO_CLK	TSO_CLK
6.15 TSO_LVL	TSO_LVL



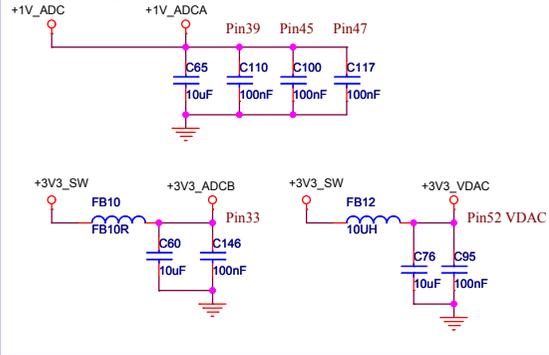
DDC_EN	<< DDC_EN	6
DVD_AUTO	<< DVD_AUTO	6
SPDIF_OUT	<< SPDIF_OUT	14
PANEL_ON	<< PANEL_ON	13
PowerON	<< PowerON	13
BL_ADJ	<< BL_ADJ	13
BL_ON	<< BL_ON	13
AU_SWDO	<< AU_SWDO	5.8
CI_CE	<< CI_CE	6
LED_G_ON	<< LED_G_ON	9
CI_A0[0..14]	<< CI_A0[0..14]	6
CI_D0[7]	<< CI_D0[7]	6
TXM0	<< TXM0	13
TXM1	<< TXM1	13
TXM2	<< TXM2	13
TXM3	<< TXM3	13
TXM4	<< TXM4	13
TXM5	<< TXM5	13
TXM6	<< TXM6	13
TXM7	<< TXM7	13
TXM8	<< TXM8	13
TXM9	<< TXM9	13
TXM10	<< TXM10	13
TXM11	<< TXM11	13
TXM12	<< TXM12	13
TXM13	<< TXM13	13
TXM14	<< TXM14	13
TXM15	<< TXM15	13
CI_WE	<< CI_WE	6
CI_RST	<< CI_RST	6
CI_WAIT	<< CI_WAIT	6
CI_IORD	<< CI_IORD	6
CI_LOWR	<< CI_LOWR	6
CI_WAIT	<< CI_WAIT	6
CI_IN_CDI	<< CI_IN_CDI	6
CI_RDY	<< CI_RDY	6
DVD_IR	<< DVD_IR	6
D_IFAGC	<< IFAGC	15
LIFP	<< LIFP	15
LIFN	<< LIFN	15
ADUTL	<< ADUTL	14
ADUTR	<< ADUTR	14
TSI_SYNC	<< TSI_SYNC	6
TSI_CLK	<< TSI_CLK	6
TSI_VAL	<< TSI_VAL	6
CVBS_OUT	<< TSI_D0[7]	6
SCL_M	<< SCL_M	9
SDA_M	<< SDA_M	9
ADUTL	<< ADUTL	14
ADUTR	<< ADUTR	14
ADUTL	<< ADUTL	14
ADUTR	<< ADUTR	14
SOY2_IN	<< SOY2_IN	5
SCG_IN	<< SCG_IN	5
SCB_IN	<< SCB_IN	5
SCR_IN	<< SCR_IN	5
DVD_ONOFF	<< DVD_ONOFF	2



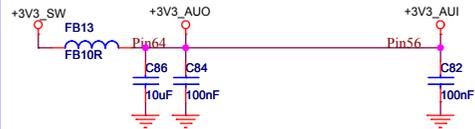
If need use SIF or RF AGC should use 1uF Cap close those pins

ADC POWER/GND (1.0V/3.3V)

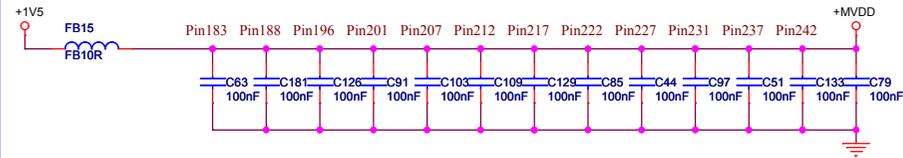
FB for ADC need $R_{dc} < 0.015$, $Z = 120/100\text{MHz}$, $I_{dc} > 3A$
 Default part MHC3216S121W



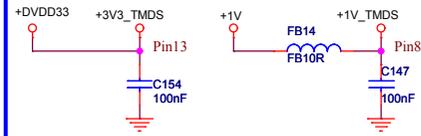
AUDIO POWER/GND (3.3V)



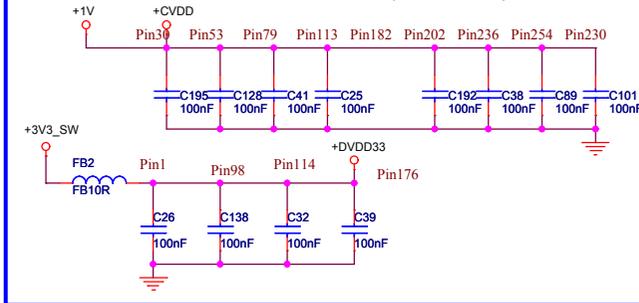
DDR-3 Power



TMDS POWER/GND (1.0V/3.3V)

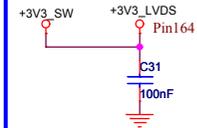


SOURCE POWER/GND (1.0V/3.3V)

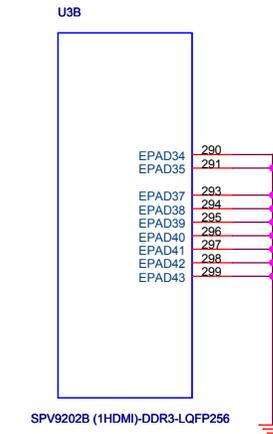


ALL FB except ADC, CVDD: need $R_{dc} < 0.2$, $Z = 300/100\text{MHz}$, $I_{dc} > 500\text{mA}$
 Default part: MCB2012S301H

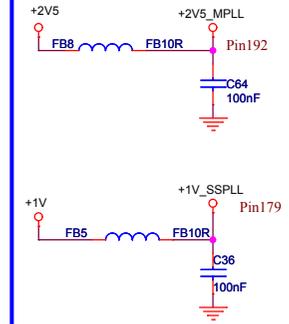
LVDS PWR



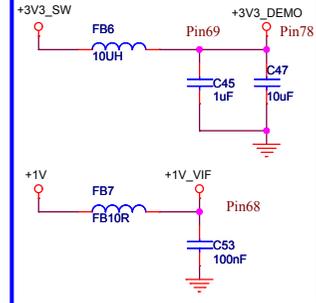
SPV9202E E-Pad



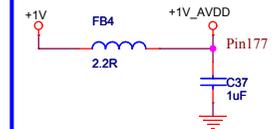
PLL POWER/GND (1.0V/2.5V)



DEMO/VIF PWR



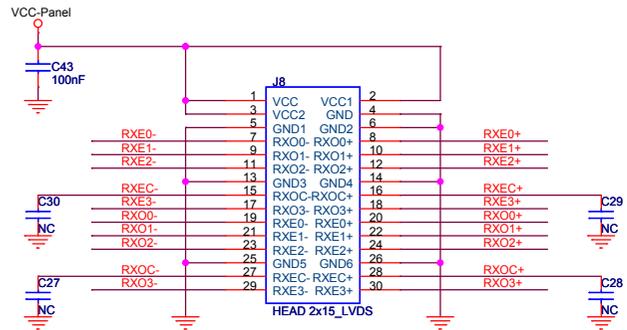
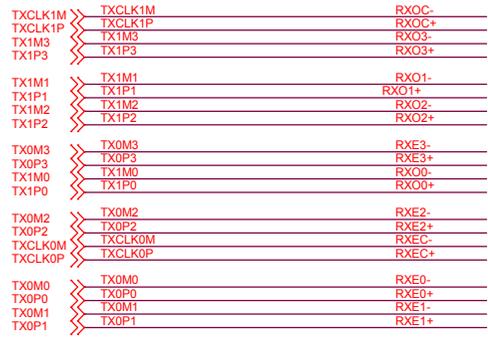
SYSTEM PLL



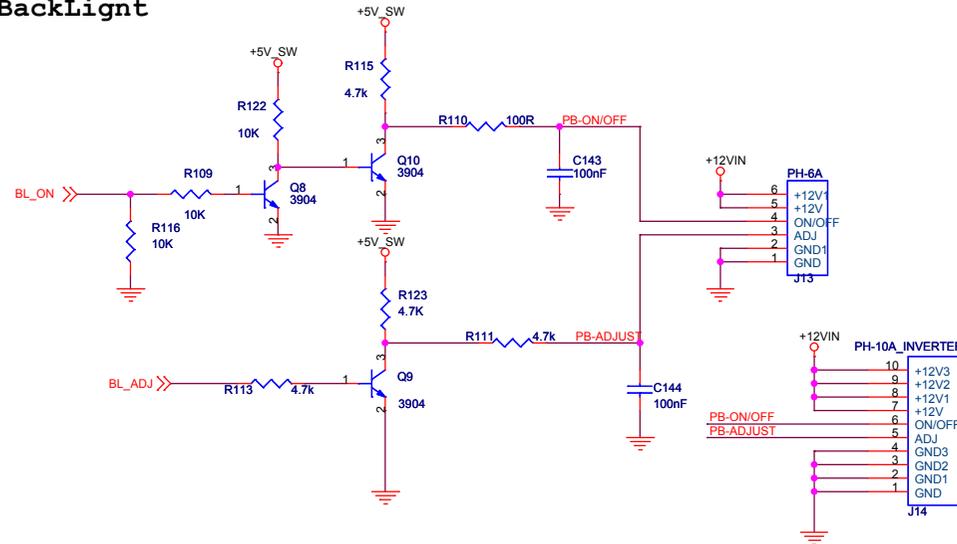
*Short these ground planes on PCB



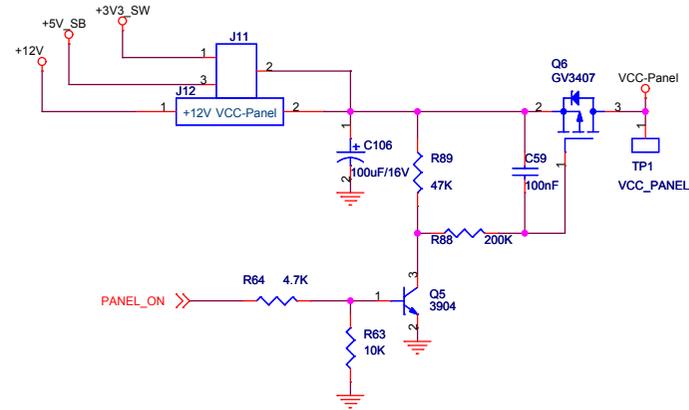
LVDS



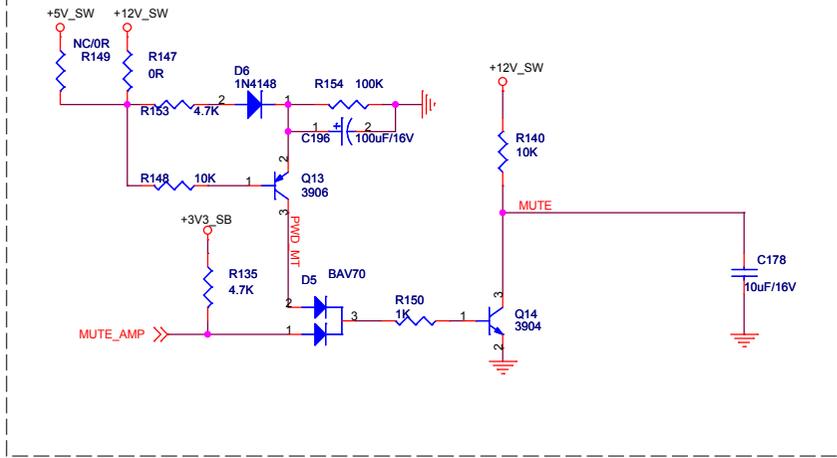
BackLight



Panel Power

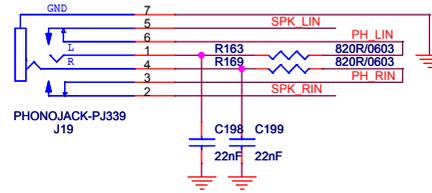
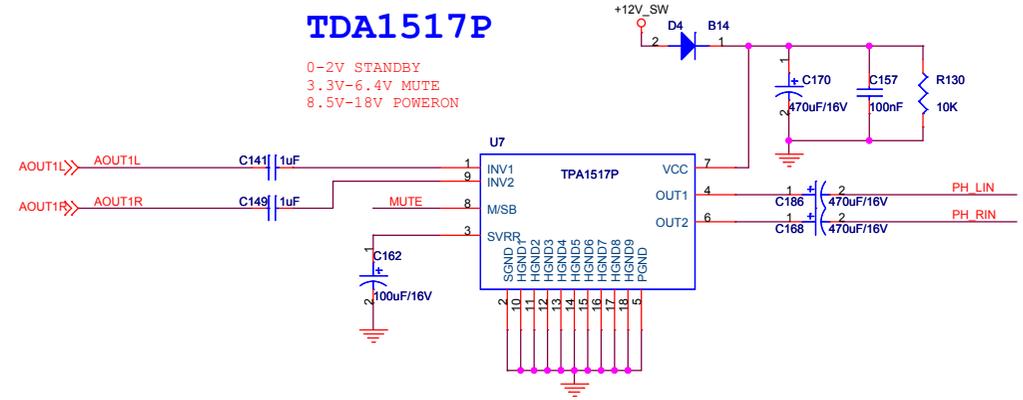


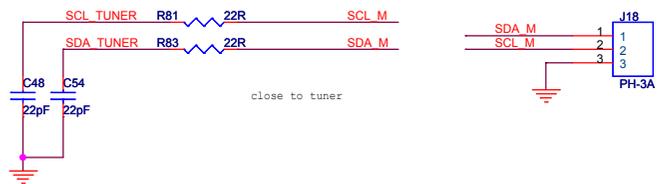
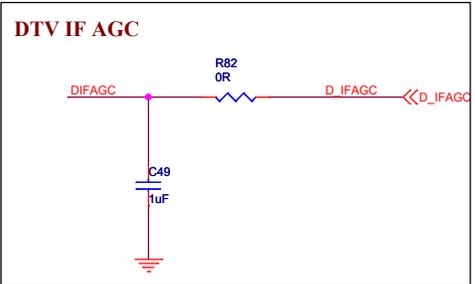
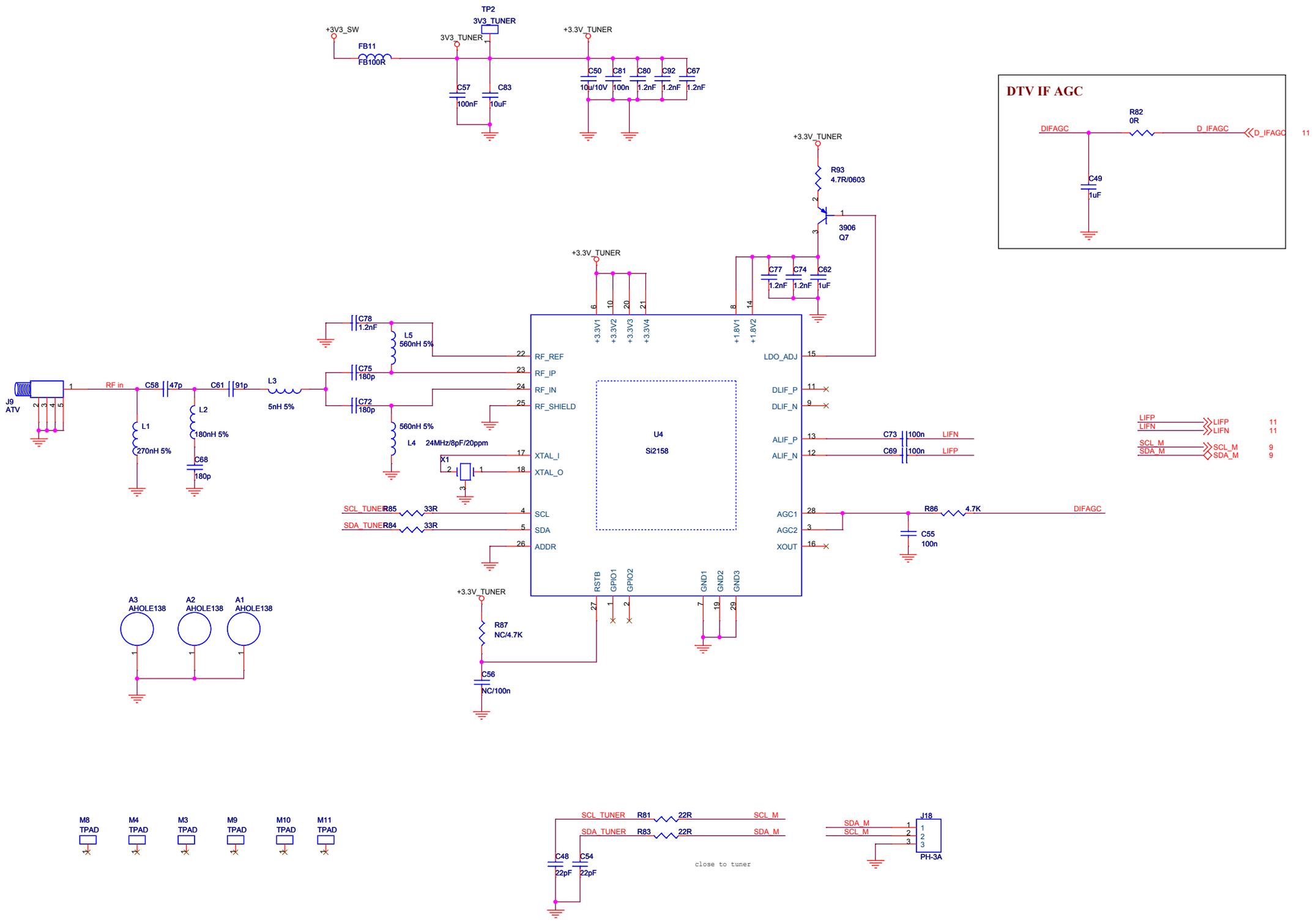
MUTE



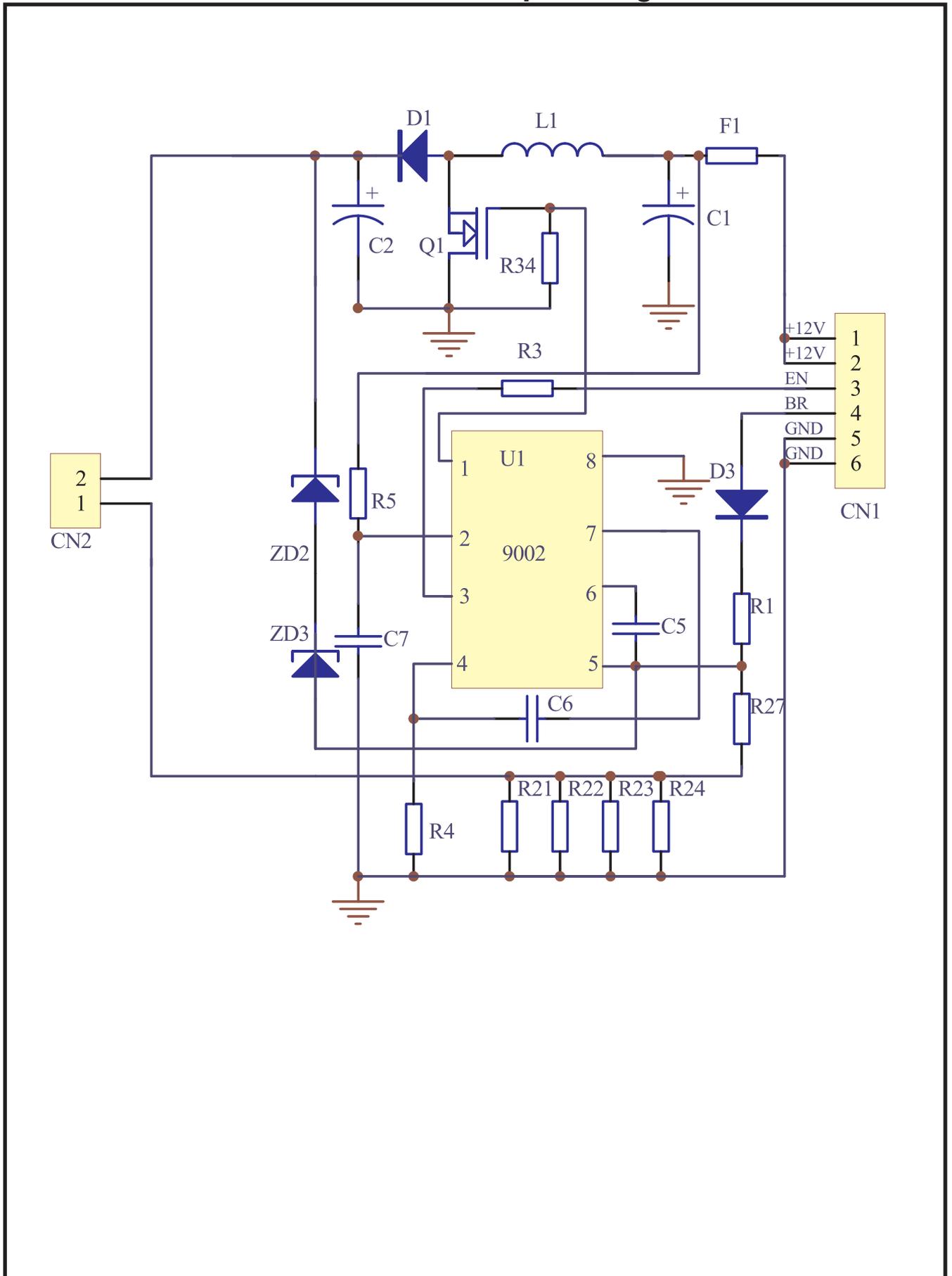
TDA1517P

0-2V STANDBY
 3.3V-6.4V MUTE
 8.5V-18V POWERON





Inverter PCB Principle Diagram



Inverter PCB Principle Diagram

