

# 42V7 PLASMA PANEL

## QUICK REFERENCE

### ALIGNMENT HAND BOOK

#### MODELS USING THE 42V7 PANEL

**42PC3DVUD**

**42PM2DS / 2DW**

**42PM3MVATA / MVHMC / MVMC / MVTA / MVZA**

**42PM3RV / RV1NC / RVA / RVANC / RVNC / RVS / RVW**

**42PX3DV / DVA / DVANC / DVAW /**

**42PX3DVB / DVBNC / DVBW**

**42PX3DVNC / DVW**

**42PX3RVMC / RVZA**

**42PX4DVAZC / DVEA / DW**

**42PX4MVHTA**

**42PX4RVHTA / RVMC / RVTA / RVZA**



**LG**

Life's Good

Intentionally left blank

**(42V7 PANEL)** The Following Models use the 42V7 Panel (2006)

# 42V7 SMPS BOARD ADJUSTMENT POINTS

## Part Number: 6709V00003A

These two voltages are adjustable and should be adjusted to the correct values as indicated by the panel label.

Example shown below just above the PWB drawing.

Always adjust “Highest to Lowest” voltages.

VS and VA adjustment resistors are shown in the drawing below.

They are located at the top left of the board.

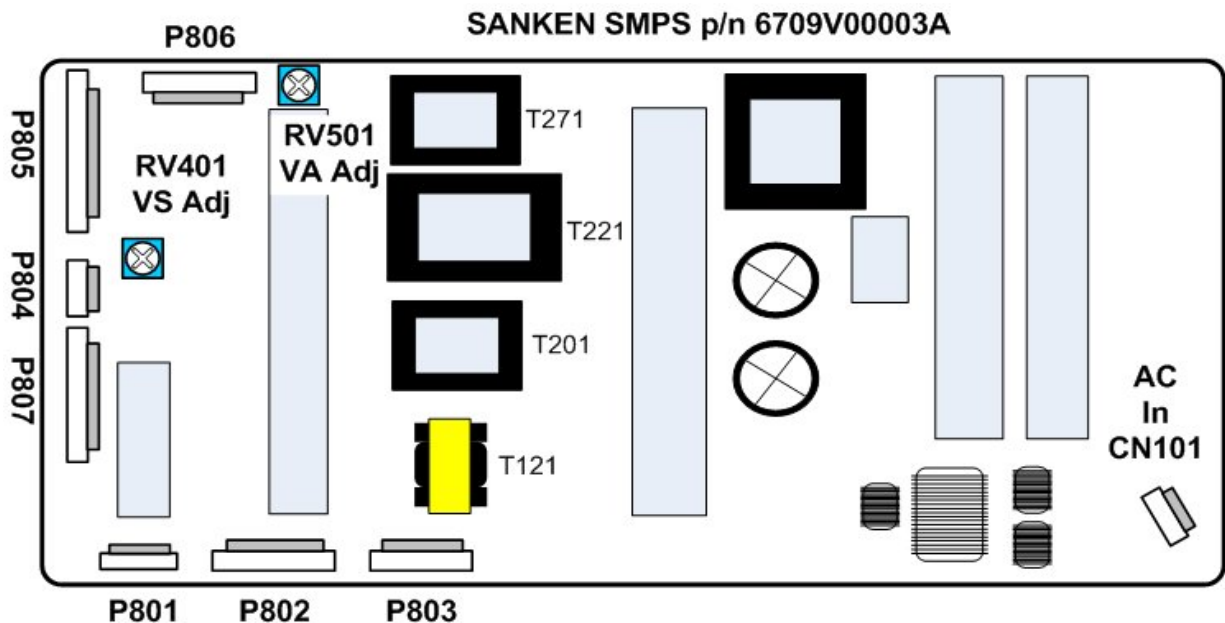
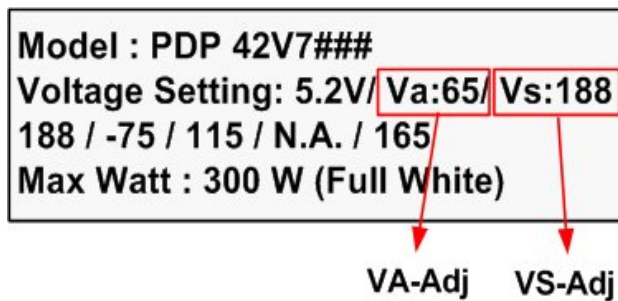
RV401 is the VS adjustment pot.

RV501 is the VA adjustment pot.

Set should be in “Full White Raster”

1) **VS ADJUST:** Connect DVM to pin 1, 2 or 3 of P805. Adjust RV401 until the voltage matches the panel’s voltage label.

2) **VA ADJUST:** Connect DVM to pin 9 or 10 of P805. Adjust RV501 until the voltage matches the panel’s voltage label.



# 42V7 SMPS BOARD ADJUSTMENT POINTS

Part Number: EAY32808901

These two voltages are adjustable and should be adjusted to the correct values as indicated by the panel label.

Example shown below just above the PWB drawing.

Always adjust "Highest to Lowest" voltages.

VS and VA adjustment resistors are shown in the drawing below.

They are located at the top left of the board.

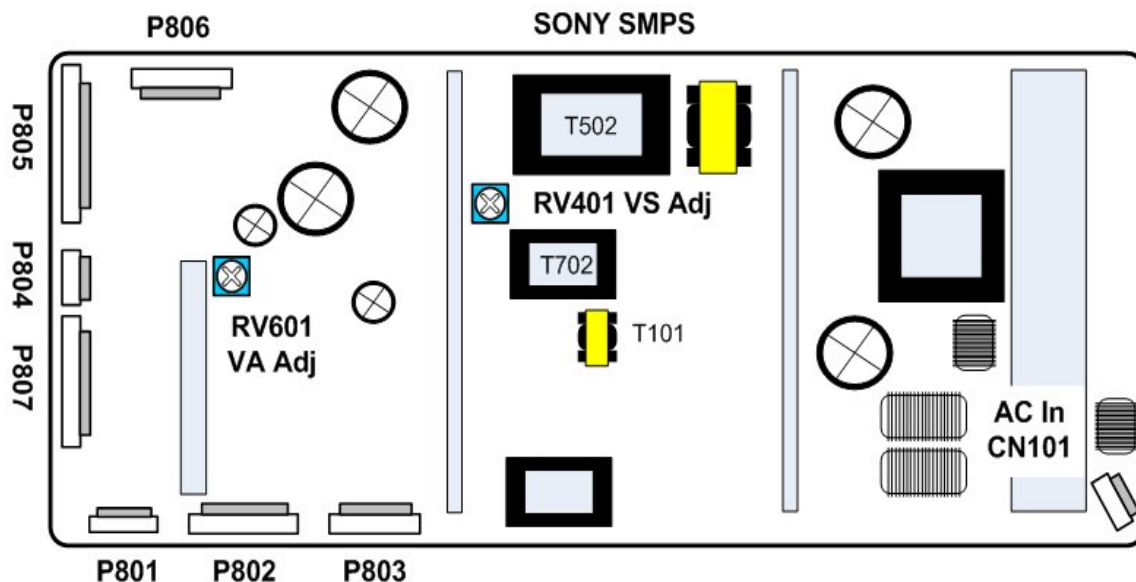
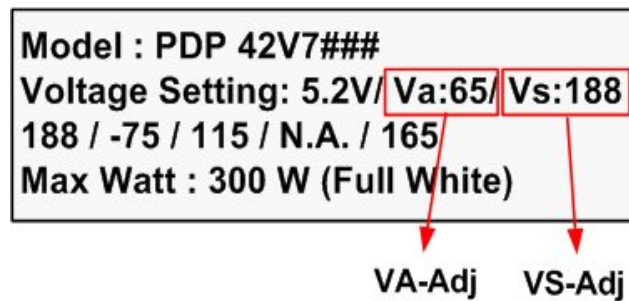
RV401 is the VS adjustment pot.

RV601 is the VA adjustment pot.

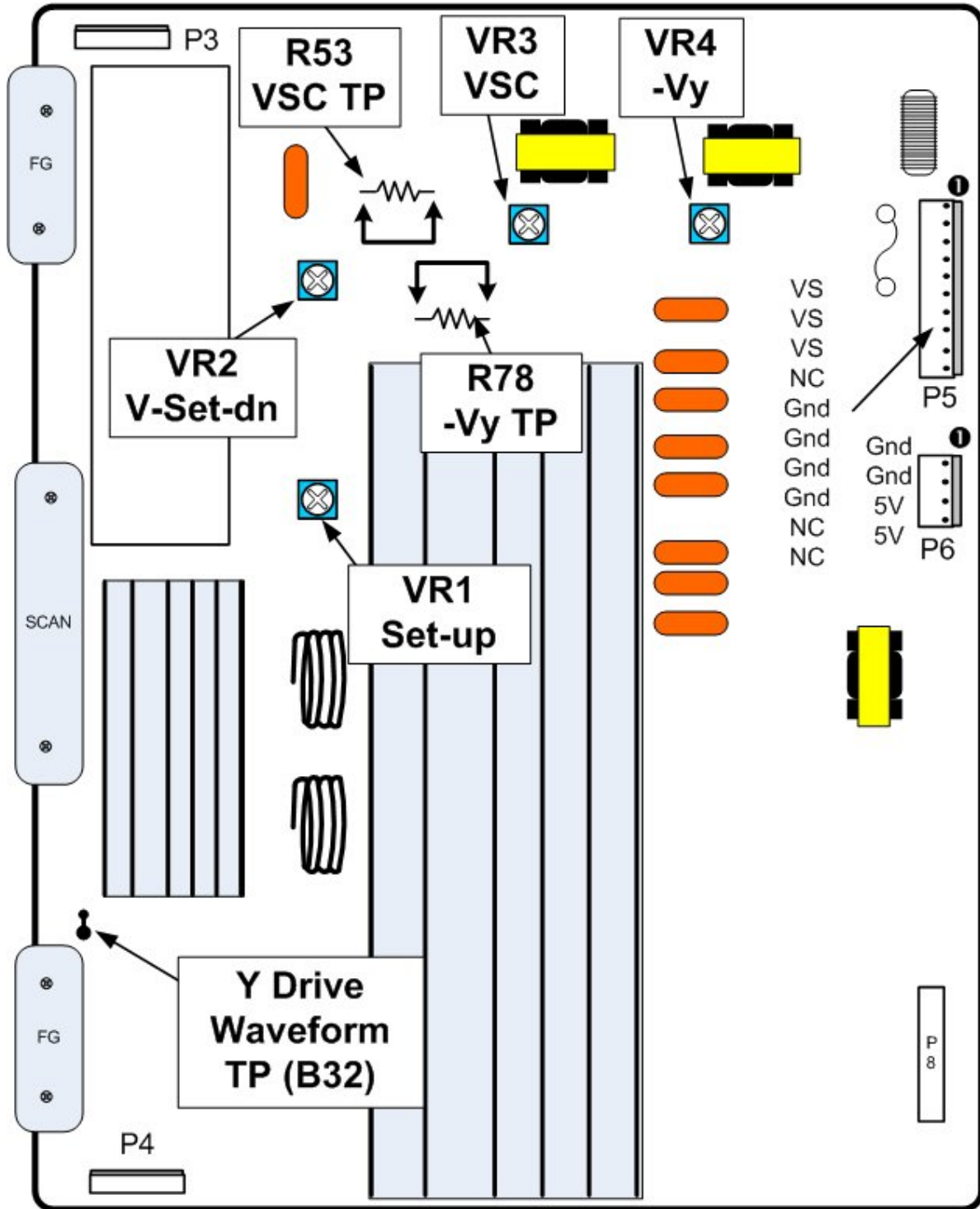
Set should be in "Full White Raster"

1) **VS ADJUST:** Connect DVM to pin 1, 2 or 3 of P805. Adjust **RV401** until the voltage matches the panel's voltage label.

2) **VA ADJUST:** Connect DVM to pin 9 or 10 of P805. Adjust **RV601** until the voltage matches the panel's voltage label.



# 42V7 Y-SUS BOARD ADJUSTMENT POINTS



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# VSC and -Vy Voltage Adjustment Locations

These voltages are Adjustable and should be adjusted to the correct values as indicated by the panel's voltage label.  
Example shown in the top right.

Model : PDP 42V7###		
Voltage Setting: 5.2V/ Va:65/ Vs:188		
188	-75	115 / N.A. / 165
Max Watt : 300 W (Full White)		

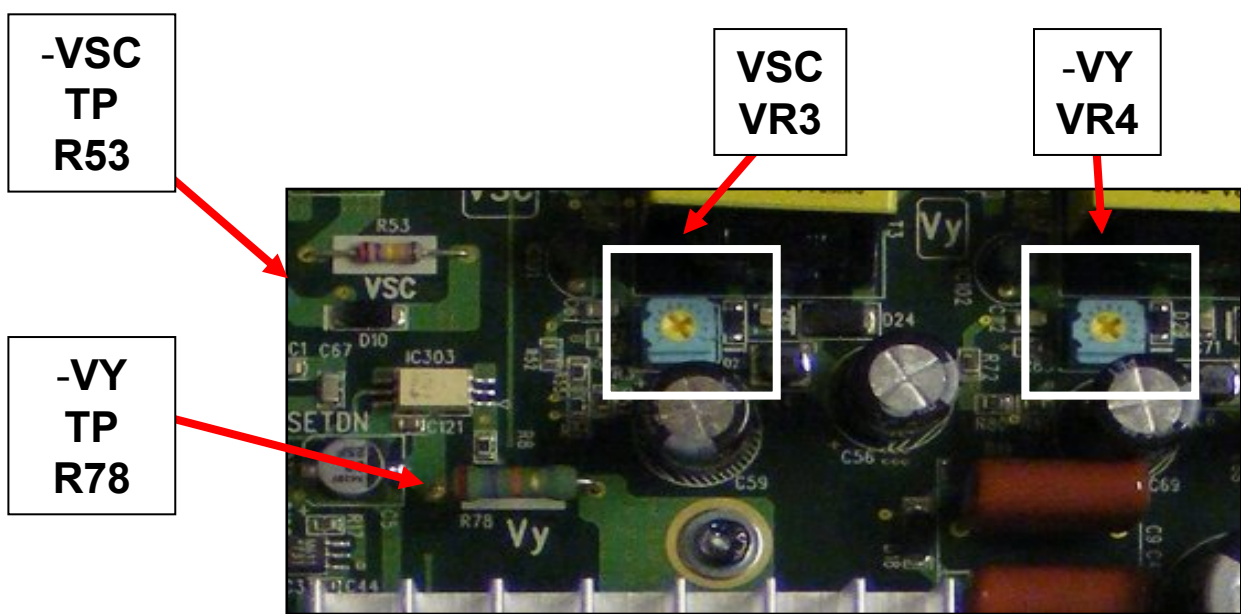
-Vy                      VSC

**VSC (VR3)** adjustment resistor located top center of the board is shown in the picture below.

Adjust the **VSC (VR3)** while reading across R53 until voltage matches the panel's voltage label.

**-Vy (VR4)** adjustment resistor located top center of the board is shown in the picture below.

**Adjust the -Vy (VR4)** while reading across R78 until voltage matches the panel's voltage label.

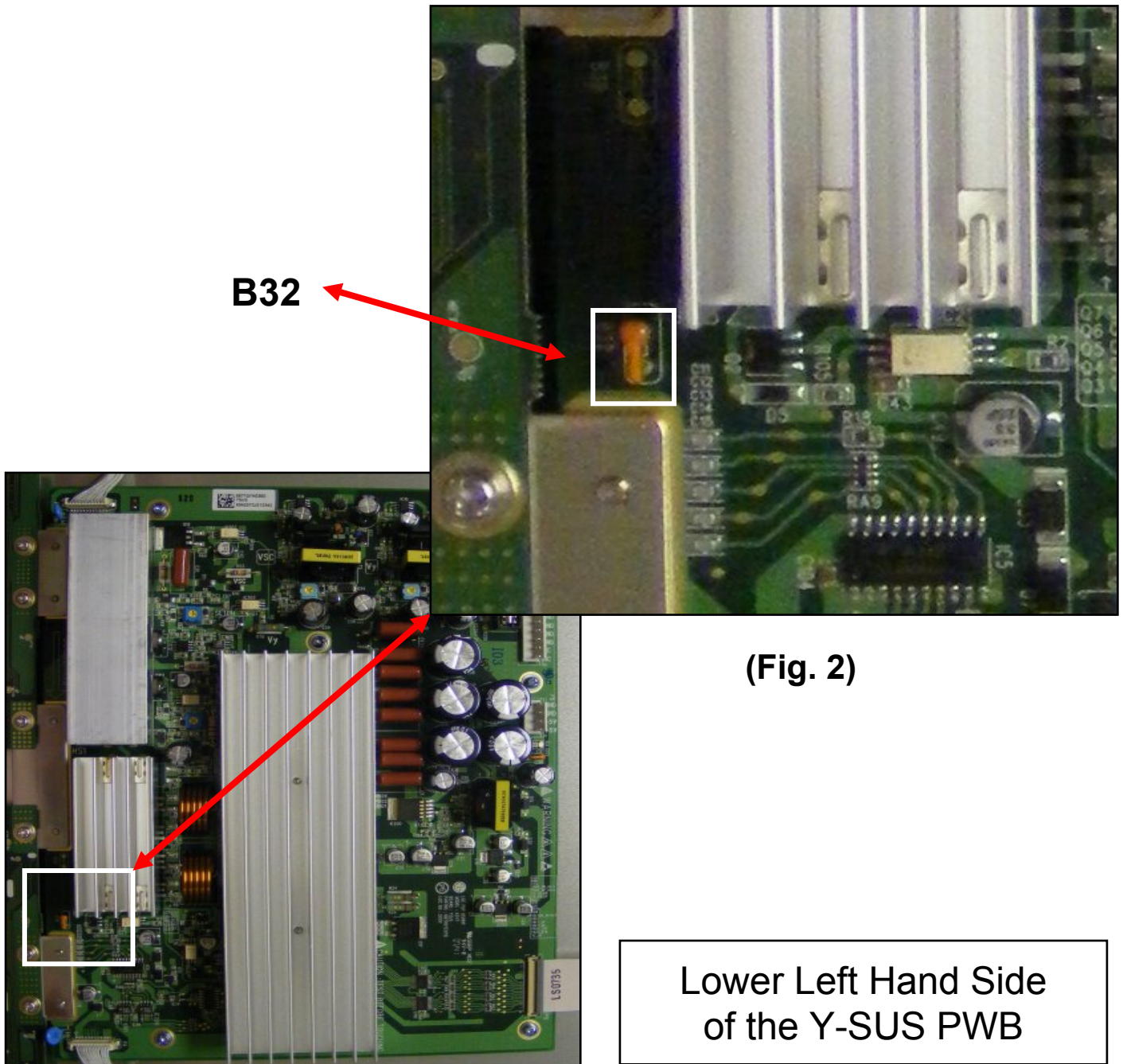


# 42V7 Y-Drive Waveform Test Point

Figure 1 shows the Y-SUS PWB with the area of the Waveform TP outlined in the white box.

Figure 2

Shows a close-up image of the Y-Drive waveform test point **B32**.



(Fig. 1)

(Fig. 2)

Lower Left Hand Side  
of the Y-SUS PWB

# 42V7 Y-DRIVE WAVEFORM ADJUSTMENT

VS, VA, VSC, -Vy should have been completed.

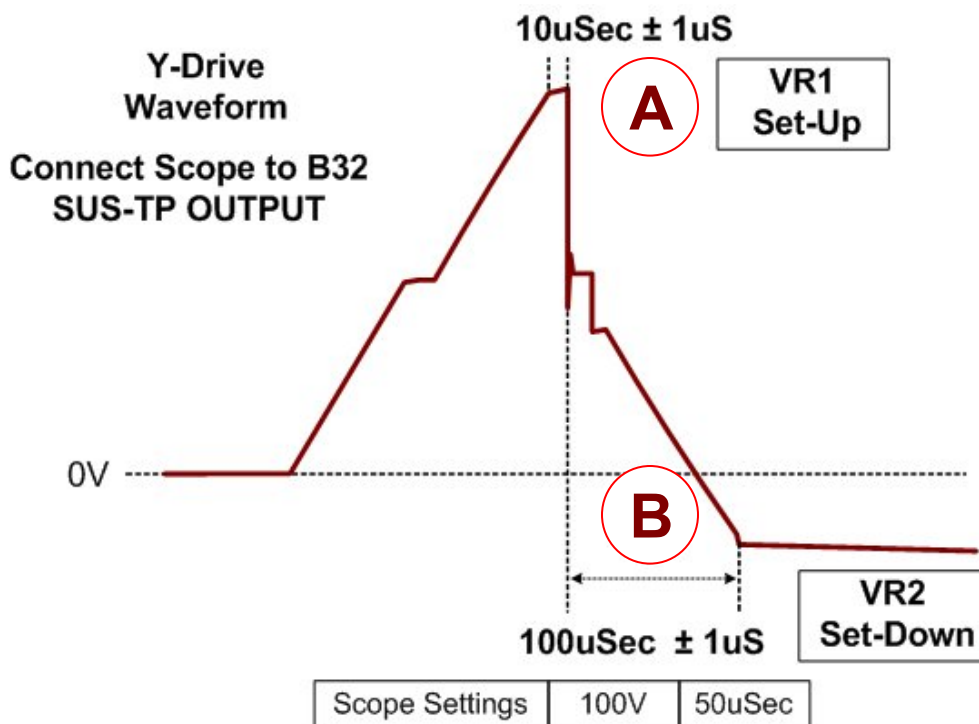
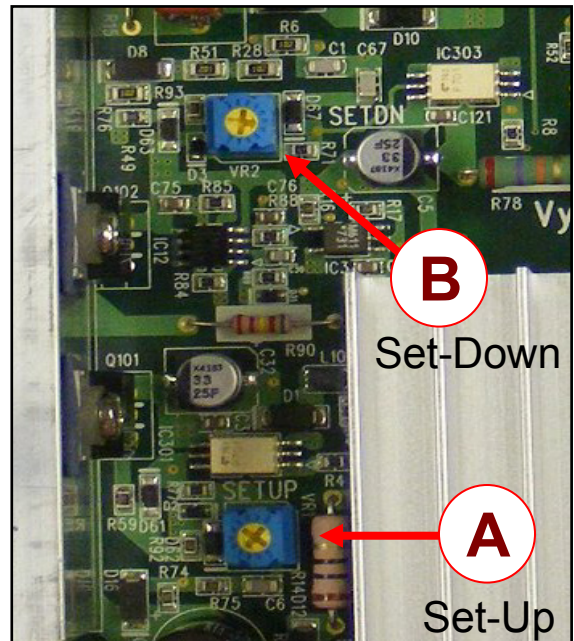
Using a Full White Raster, adjust the Set-up and Set-dn section of the Y-Drive waveform.

See Y-SUS Test Points and Adjustments diagram for locations.

Oscilloscope TP B32 on the "Waveform" TP on the Y-SUS PWB.

**(A) Set-Up:** Adjust **VR1** while observing area **(A)** and set to **10uSec ± 1uSec**.

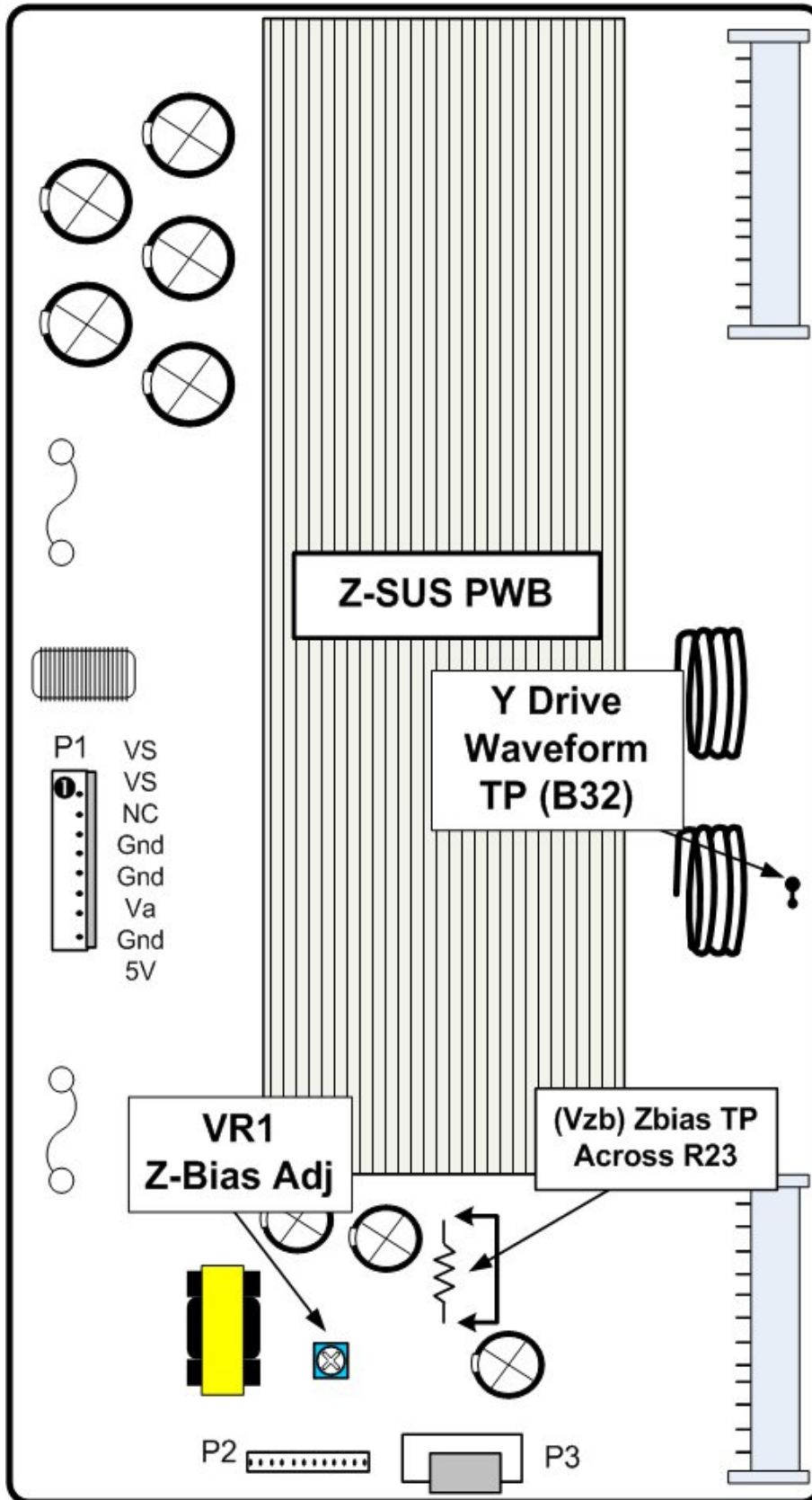
**(B) Set-Down:** Adjust **VR2** while observing area **(B)** and set to **100uSec ± 1uSec**.



After adjustment, please recheck VS, VA, VSC, -Vy and Z-bias.



# 42V7 Z-SUS BOARD ADJUSTMENT POINTS



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# 42V7 Z-SUS PWB ADJUSTMENT POINTS

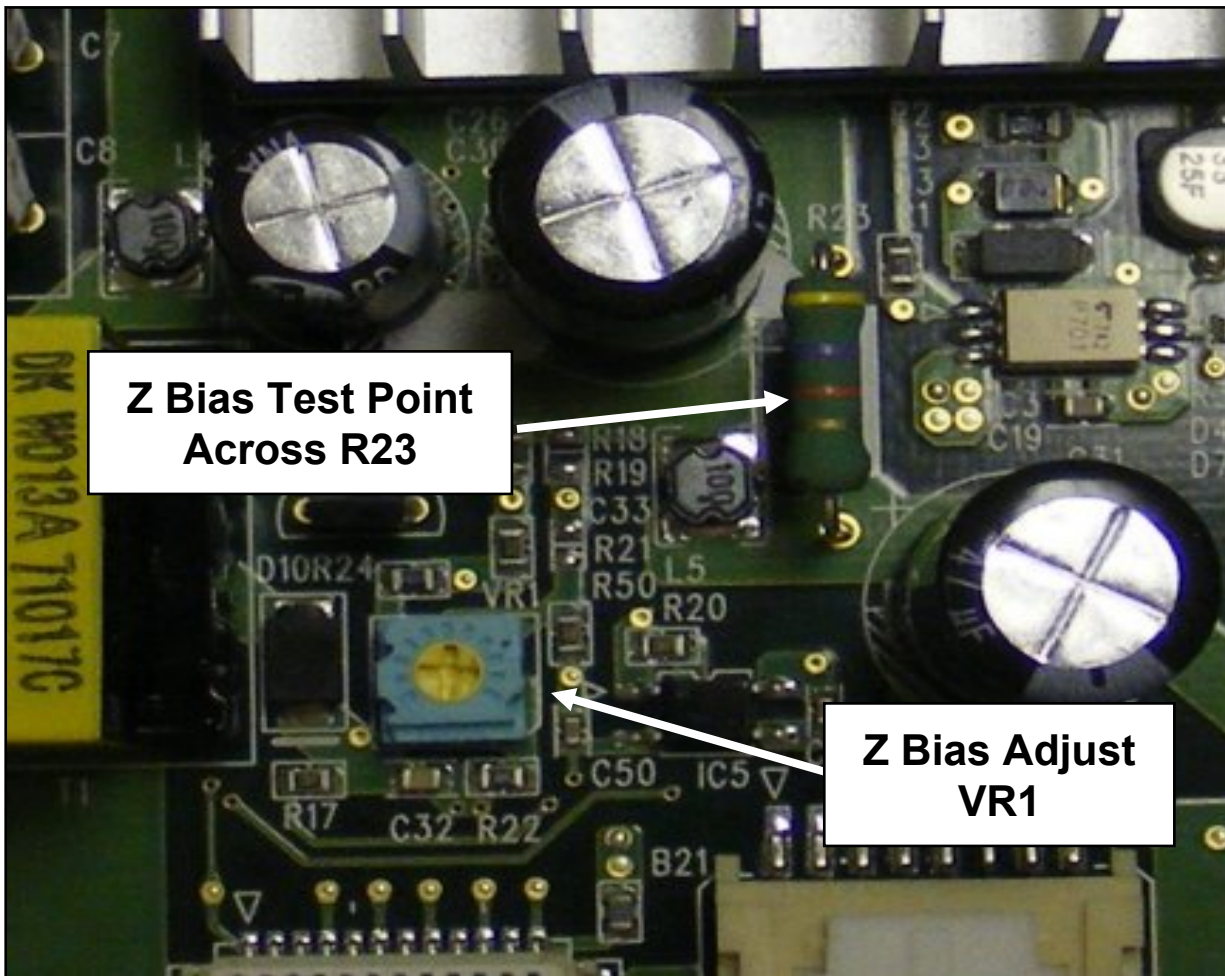
VS, VA, VSC, -Vy should have been completed.

Model : PDP 42V7###  
Voltage Setting: 5.2V/ Va:65/ Vs:188  
188 / -75 / 115 / N.A. / 165  
Max Watt : 300 W (Full White)

Zbias

## Full White Raster

- 1) Z-Bias TP: Connect DVM across R23.
- 2) Adjust Z-Bias (VR1) to match the panel's voltage label.



Z Bias Test Point  
Across R23

Z Bias Adjust  
VR1

Bottom of the Y-SUS Board