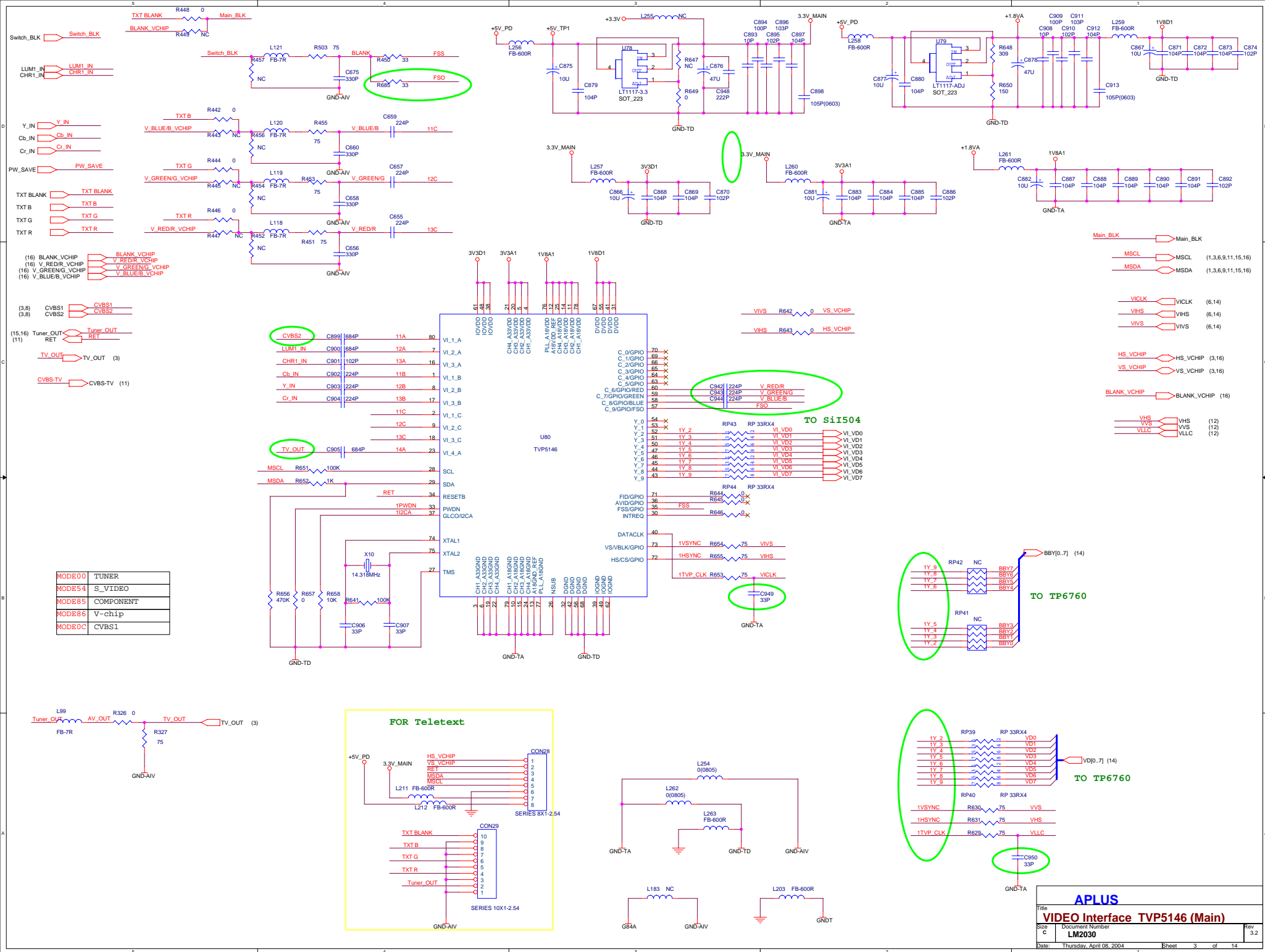


Title			APLUS		
Size			VGA Interface (AD9883)		
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- (16) BLANK_VCHIP
- (16) V_RED1_VCHIP
- (16) V_GREEN1_VCHIP
- (16) V_BLUE1_VCHIP

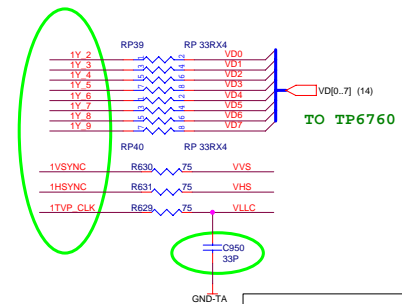
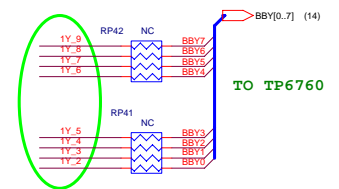
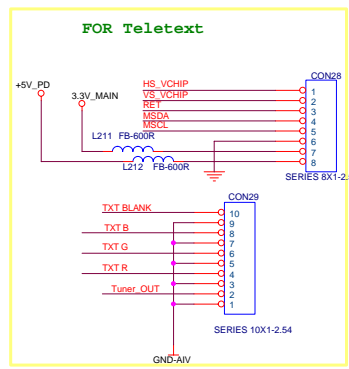
- (3,8) CVBS1
- (3,8) CVBS2

- (15,16) Tuner_OUT
- (11) RET

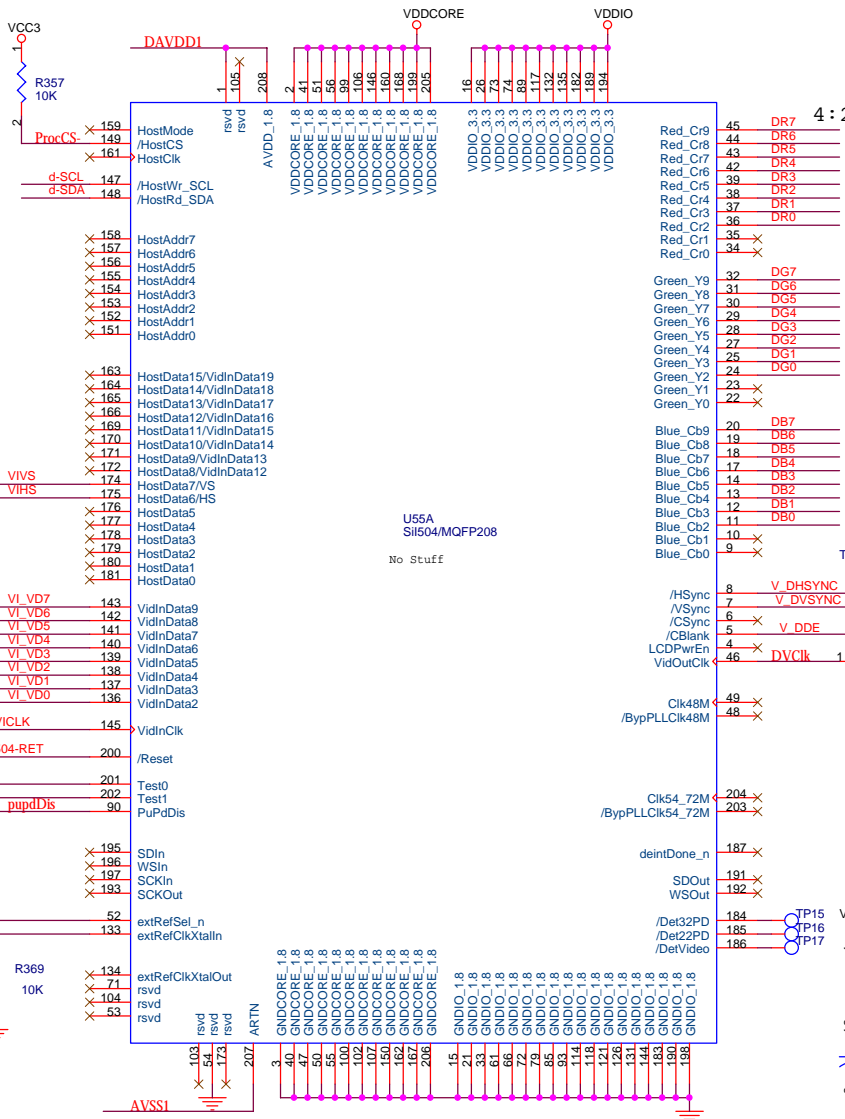
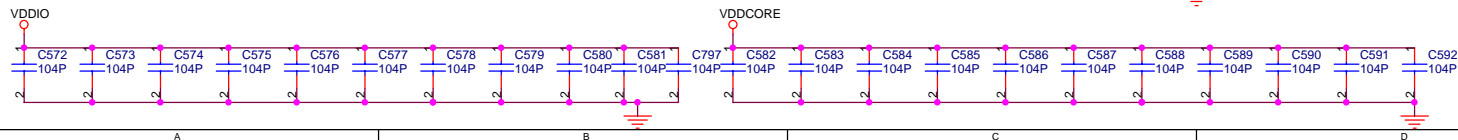
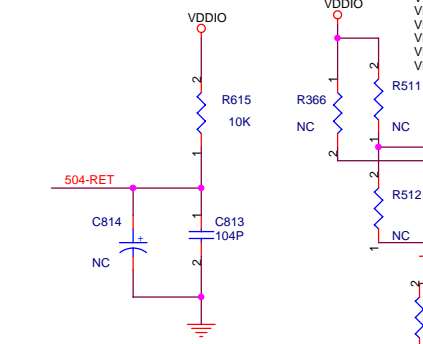
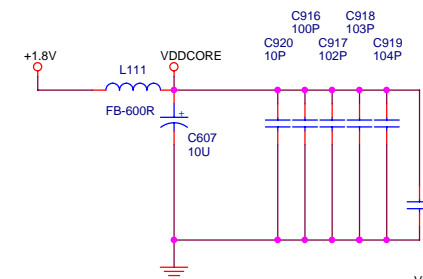
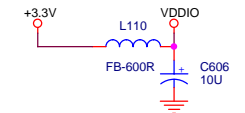
- TV_OUT (3)

- CVBS-TV (11)

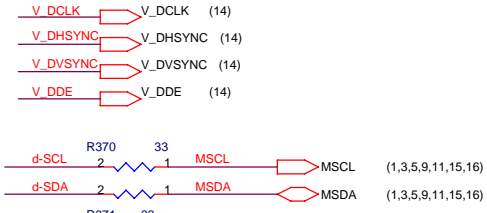
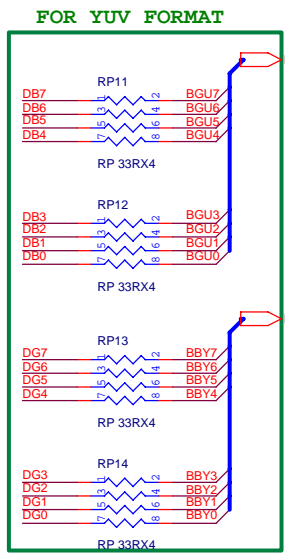
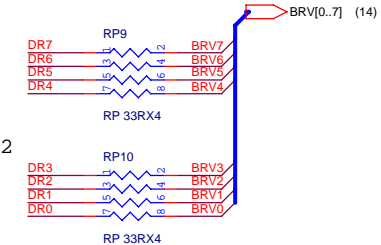
MODE00	TUNER
MODE54	S_VIDEO
MODE85	COMPONENT
MODE86	V-chip
MODE0C	CVBS1



- (11) VIDEO_S
- (11) 504-RET
- (5,14) VICLK
- (5,14) VIHS
- (5,14) VIVS



U55A
Si504/MQFP208
No Stuff

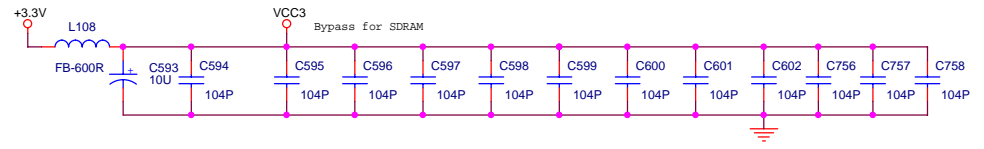
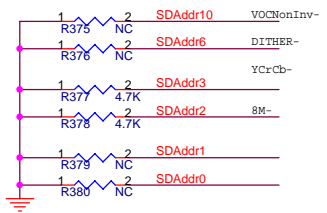
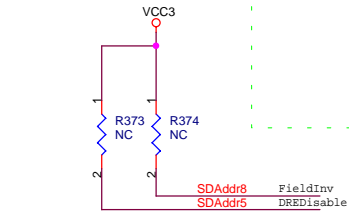
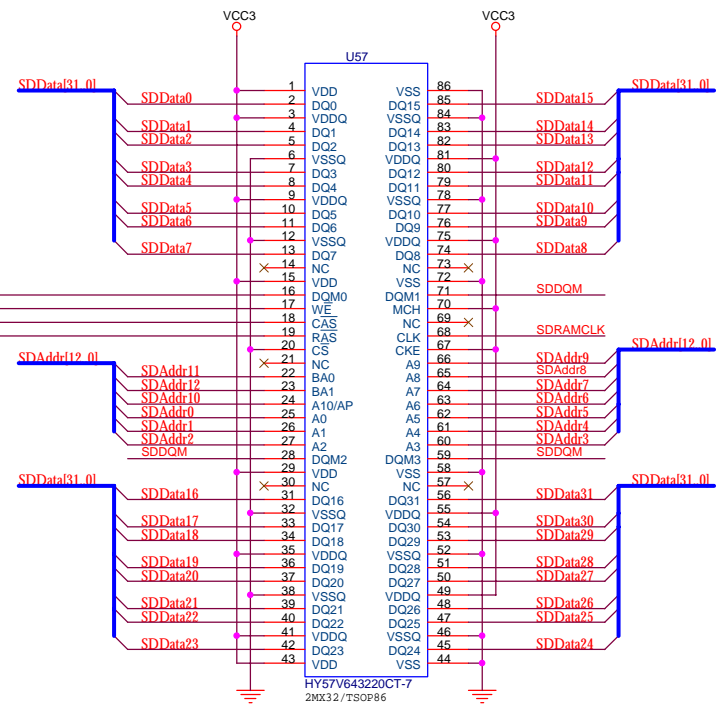
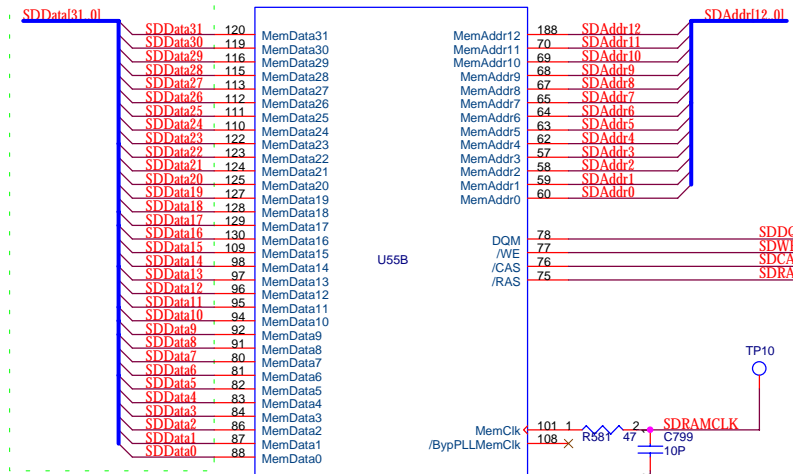


APLUS		
Title		
Si504 (De-interlace)		
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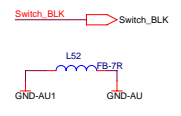
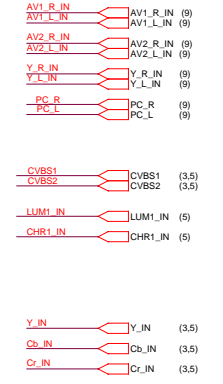
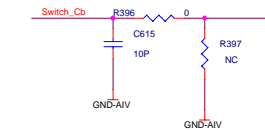
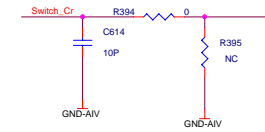
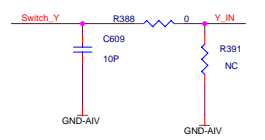
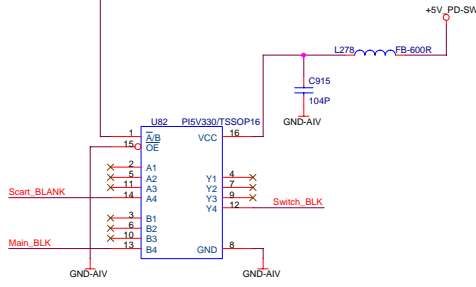
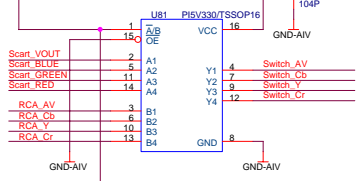
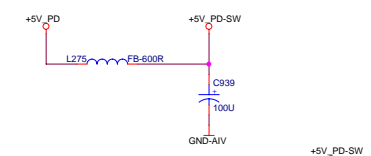
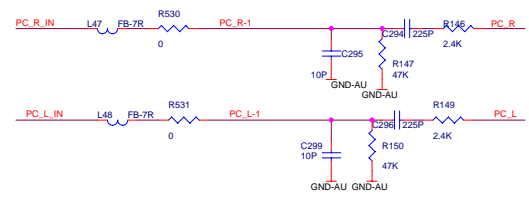
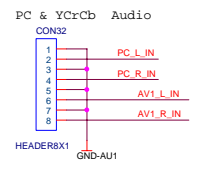
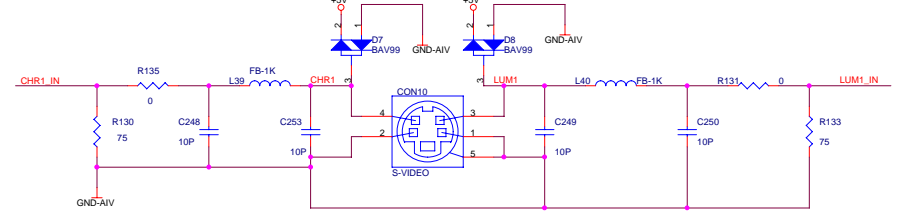
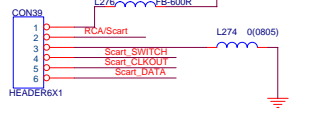
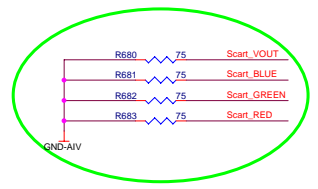
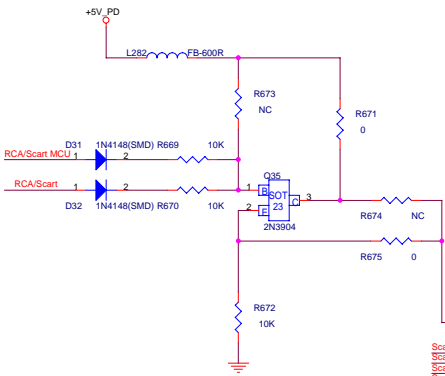
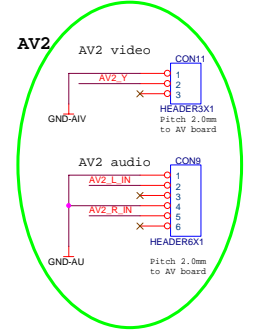
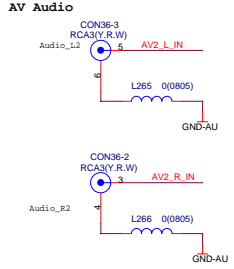
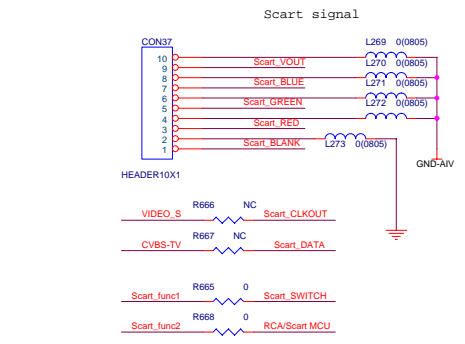
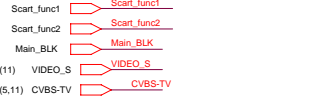
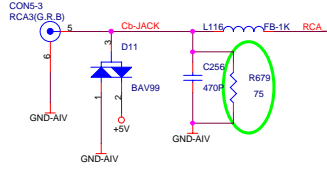
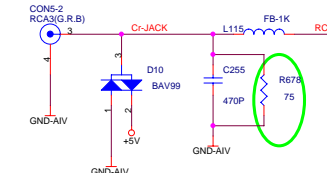
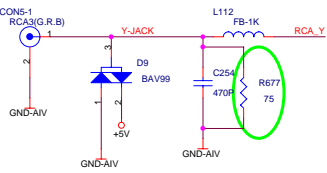
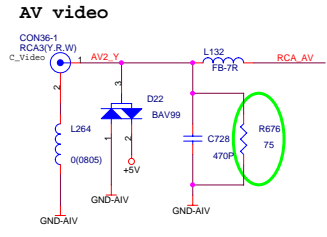
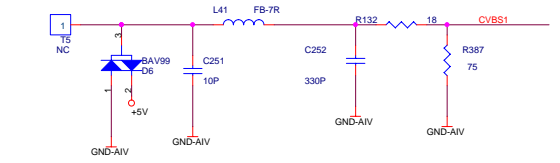
Equal length

Equal length

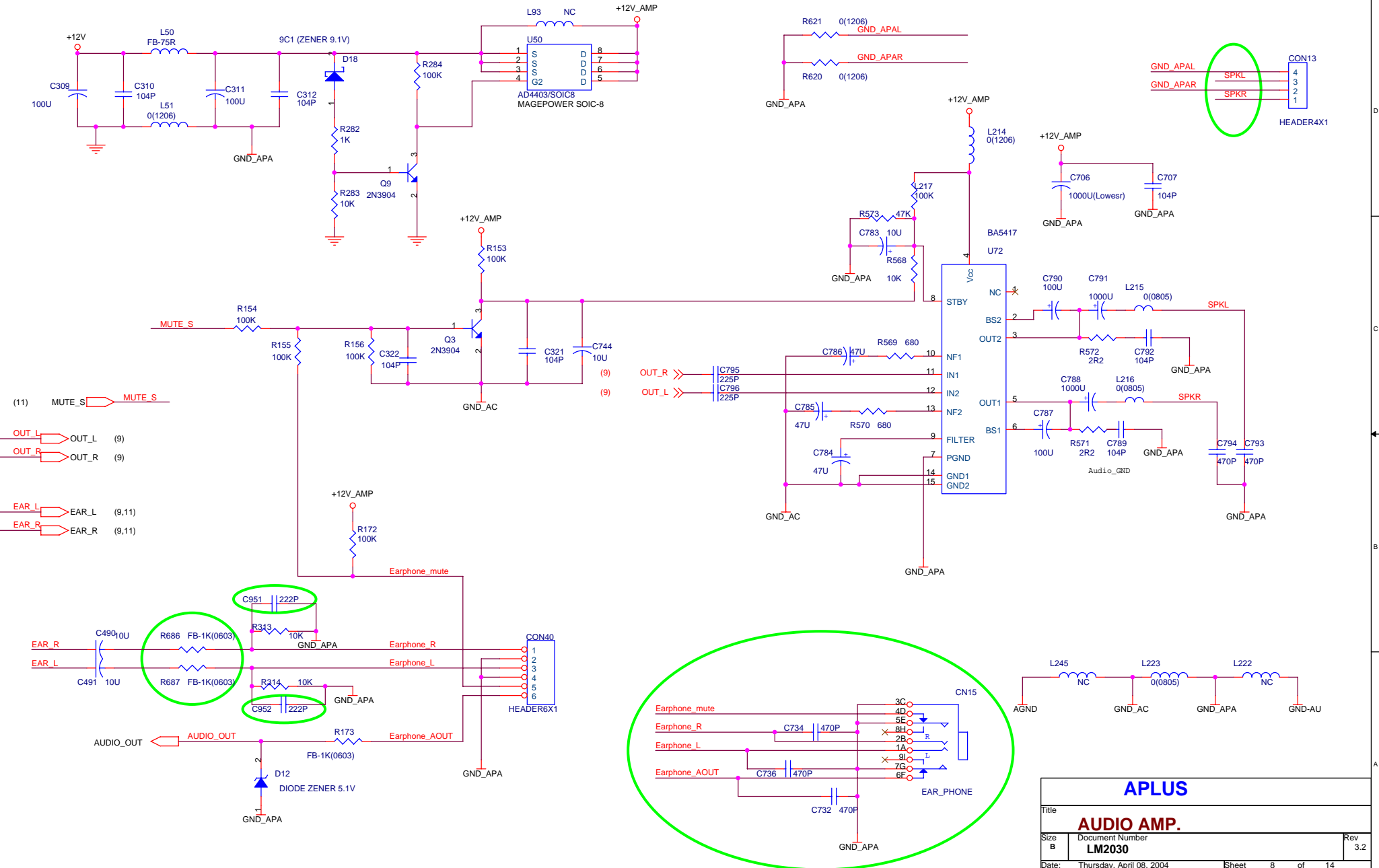
No Stuff



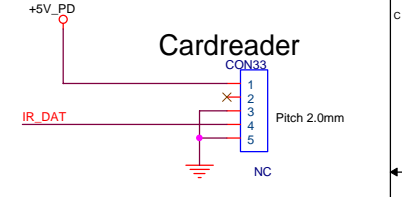
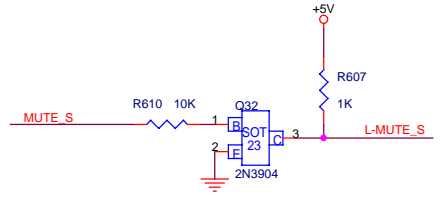
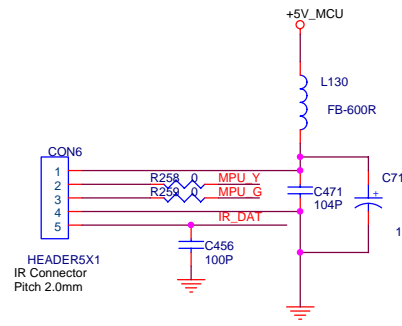
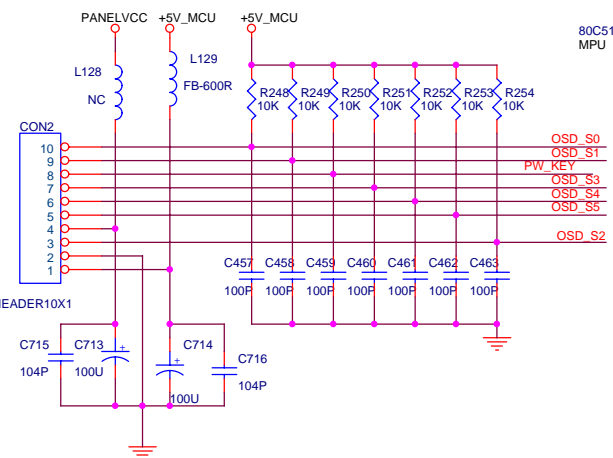
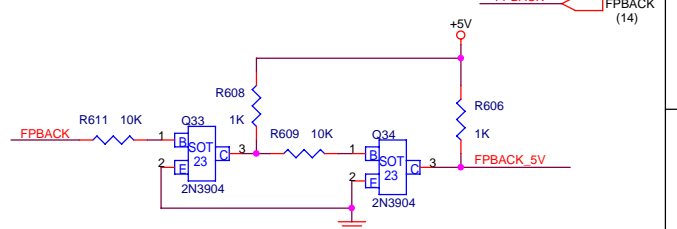
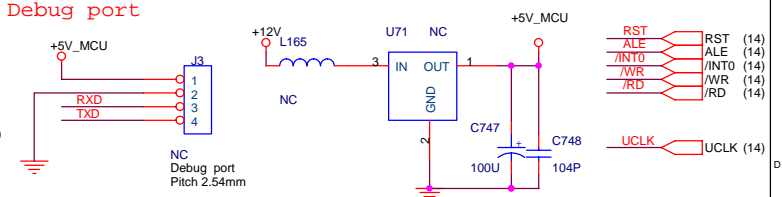
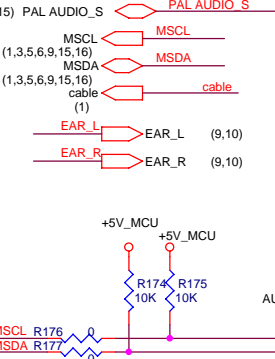
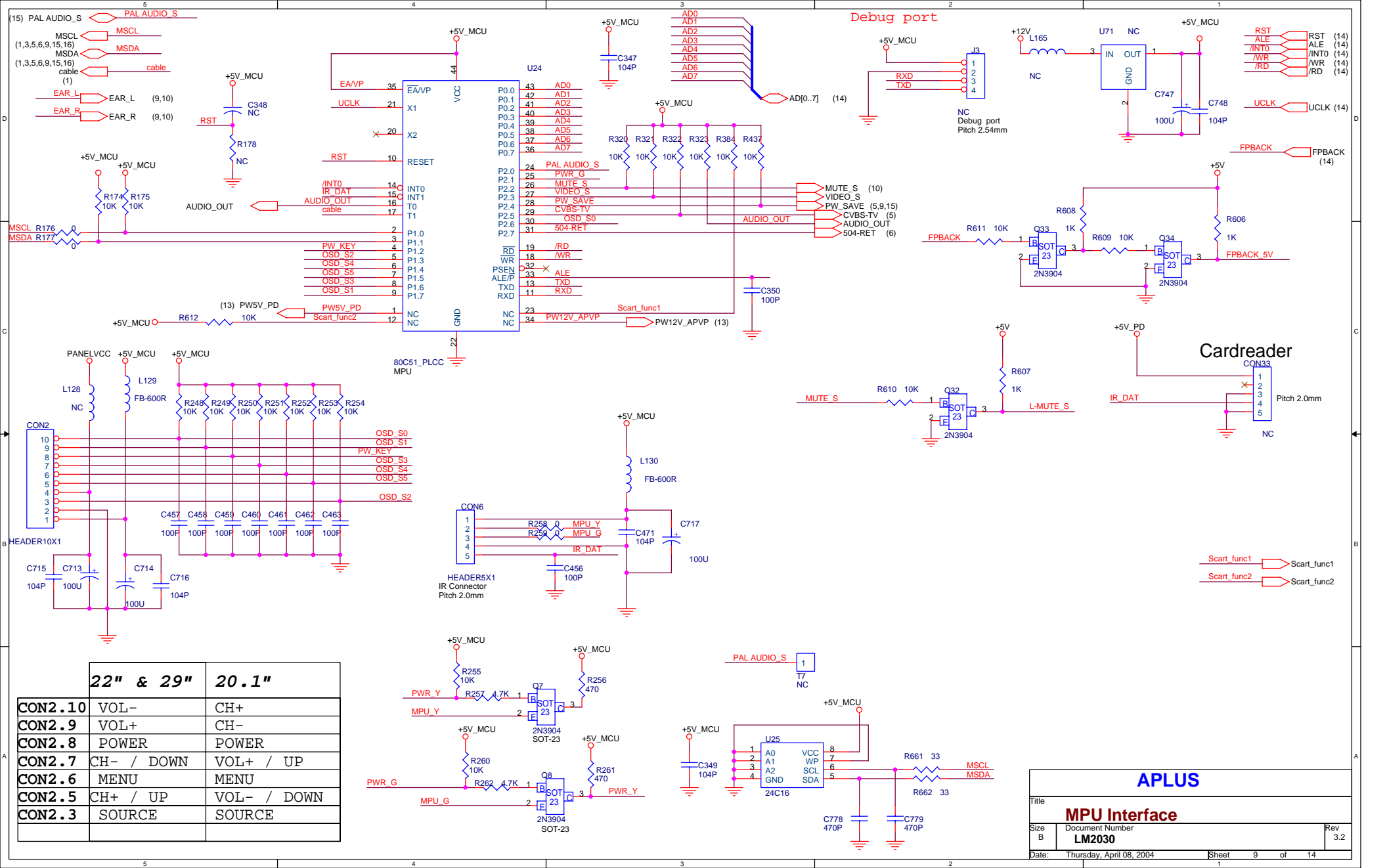
APLUS		
Title		
SDRAM (FOR SiI504)		
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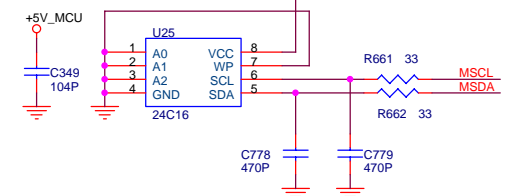
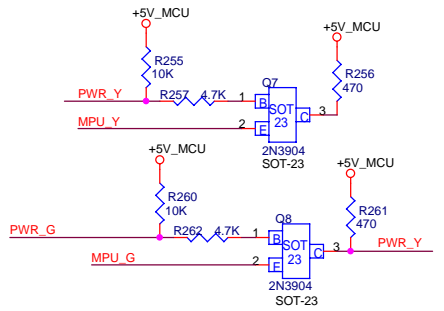
APLUS		
Title	Connector Interface	
Size	Document Number	Rev
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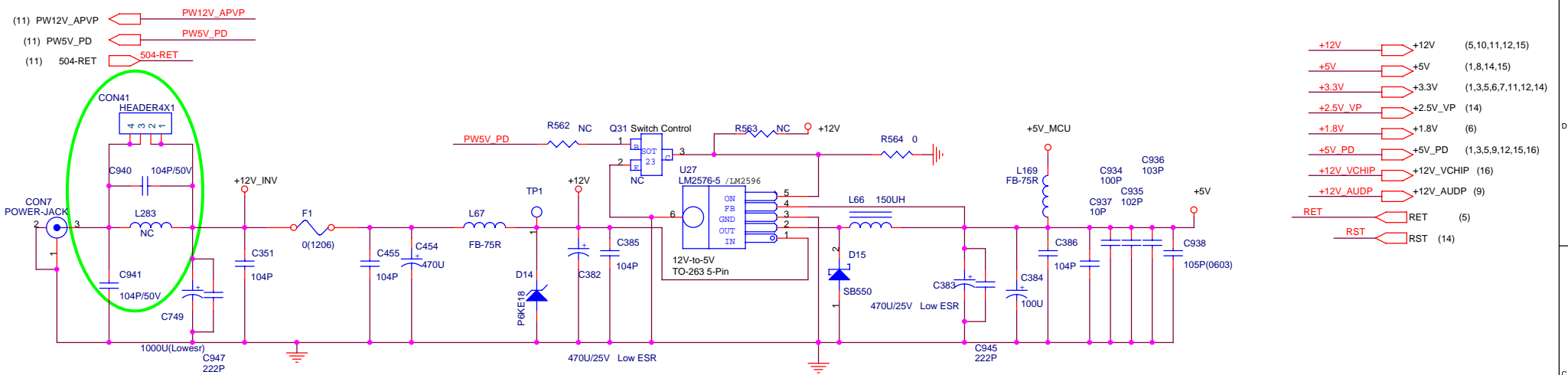
APLUS		
Title		
AUDIO AMP.		
Size	Document Number	Rev
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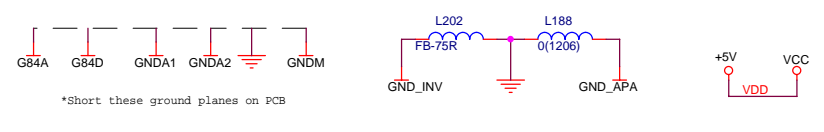
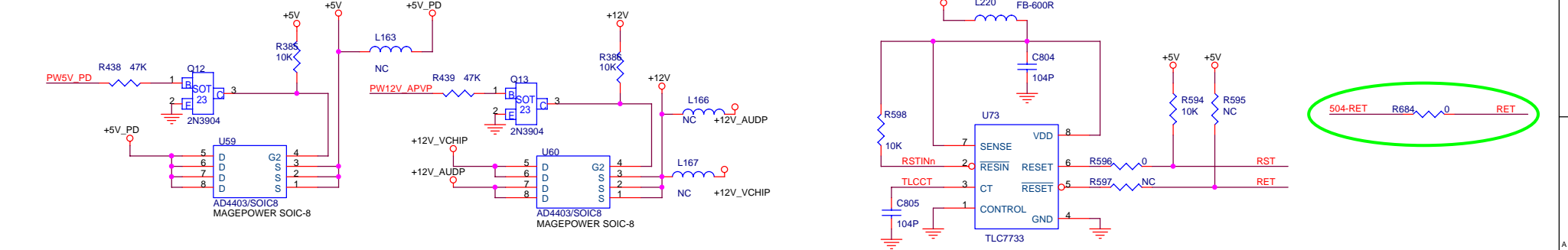
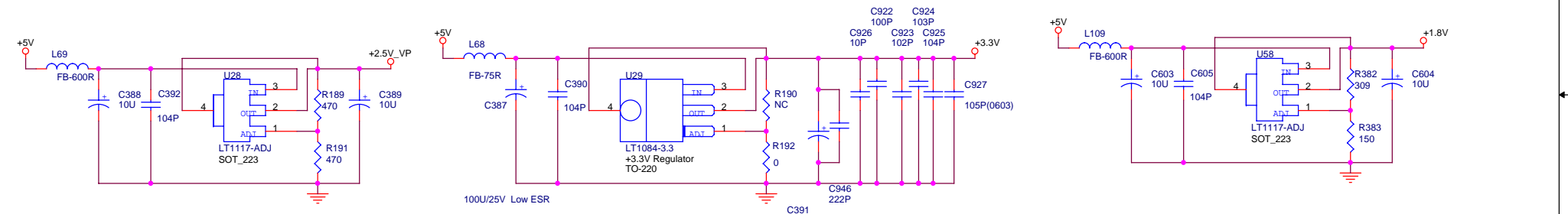
	22" & 29"	20.1"
CON2.10	VOL-	CH+
CON2.9	VOL+	CH-
CON2.8	POWER	POWER
CON2.7	CH- / DOWN	VOL+ / UP
CON2.6	MENU	MENU
CON2.5	CH+ / UP	VOL- / DOWN
CON2.3	SOURCE	SOURCE



APLUS		
Title: MPU Interface		
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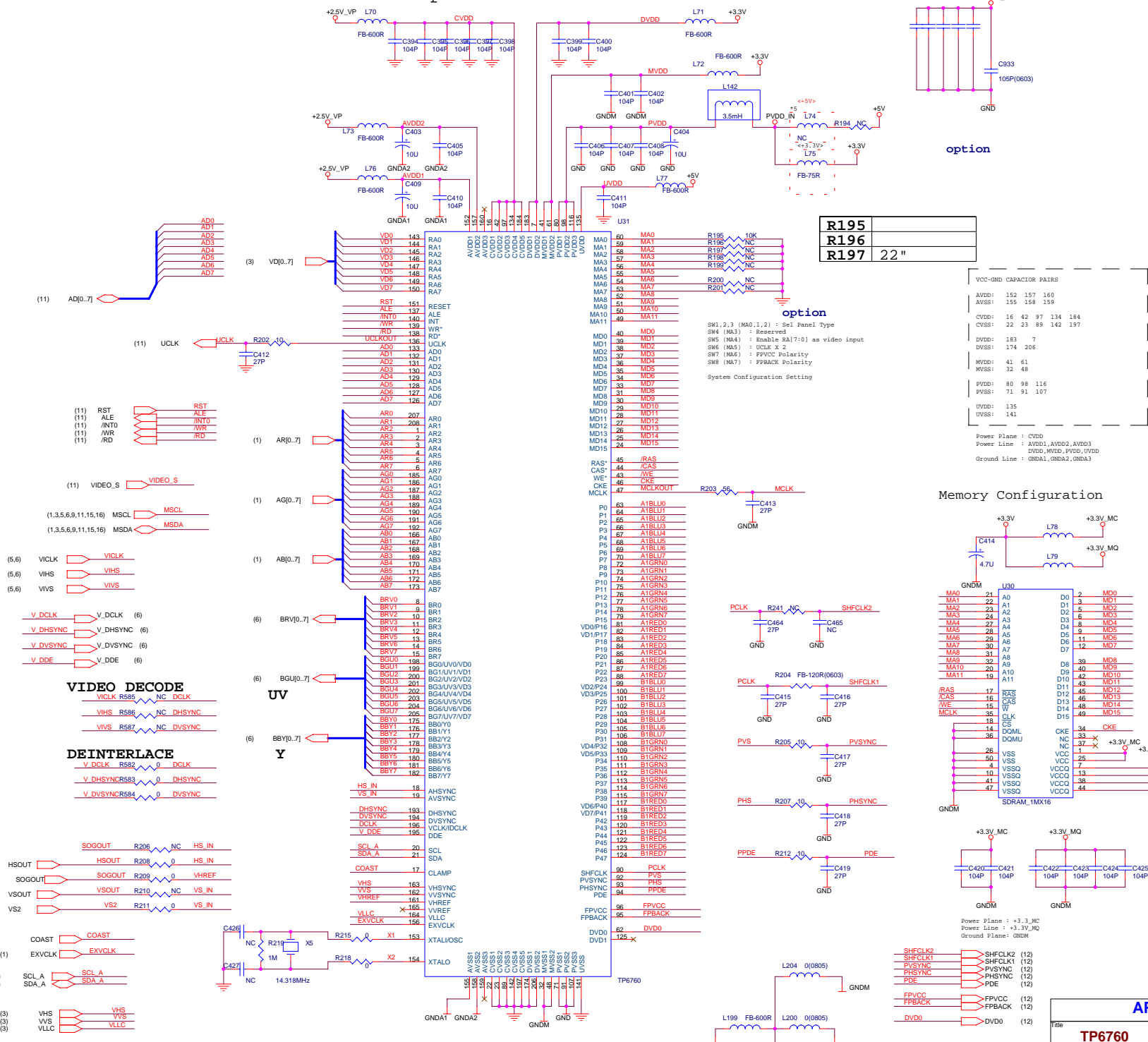
- +12V → +12V (5,10,11,12,15)
- +5V → +5V (1,8,14,15)
- +3.3V → +3.3V (1,3,5,6,7,11,12,14)
- +2.5V_VP → +2.5V_VP (14)
- +1.8V → +1.8V (6)
- +5V_PD → +5V_PD (1,3,5,9,12,15,16)
- +12V_VCHIP → +12V_VCHIP (16)
- +12V_AUDP → +12V_AUDP (9)
- RET → RET (5)
- RST → RST (14)



*Short these ground planes on PCB

APLUS		
POWER Interface		
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Chip Interface



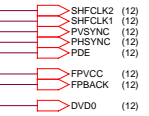
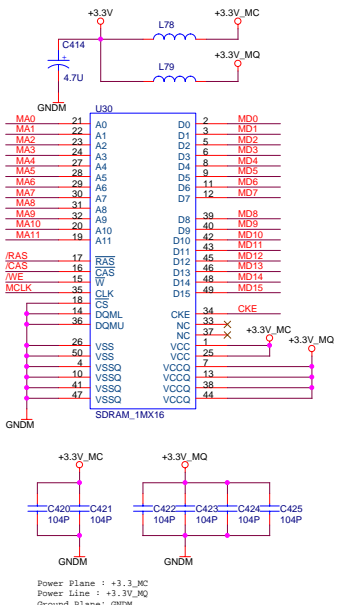
R195	
R196	
R197	22 "

VCC-GND CAPACITOR PAIRS

AVDD:	152	157	160		
AVSS:	155	158	159		
CVDD:	16	42	97	134	184
CVSS:	22	23	89	142	197
DVDD:	183	7			
DVSS:	174	206			
MVDD:	41	61			
MVSS:	32	48			
PVDD:	80	98	116		
PVSS:	71	91	107		
UVDD:	135				
UVSS:	141				

Power Plane : CVDD
Power Line : AVDD, AVDD2, AVDD3, DVDD, MVDD, PVDD, UVDD
Ground Line : GND, GND1, GND2, GND3

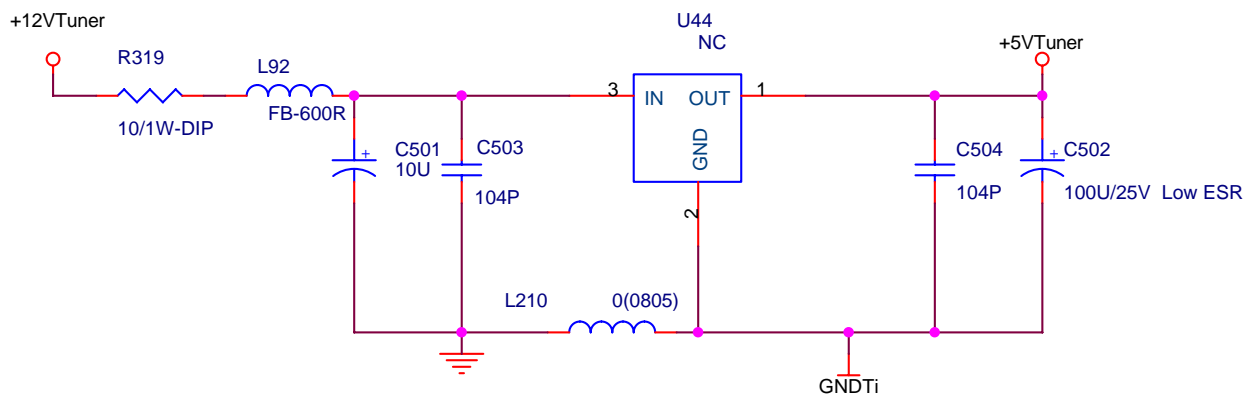
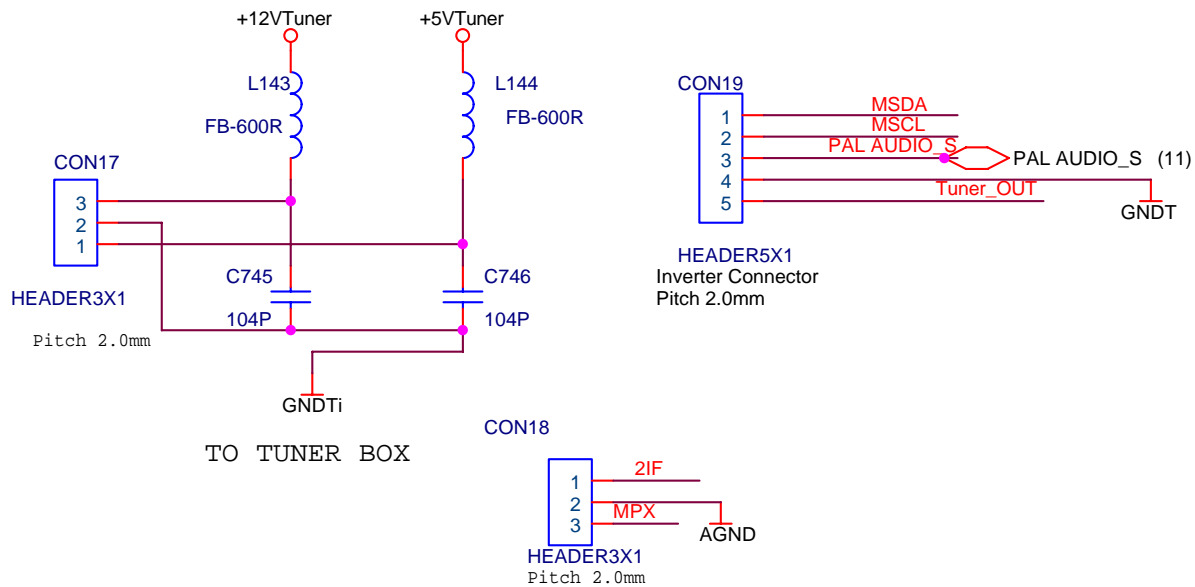
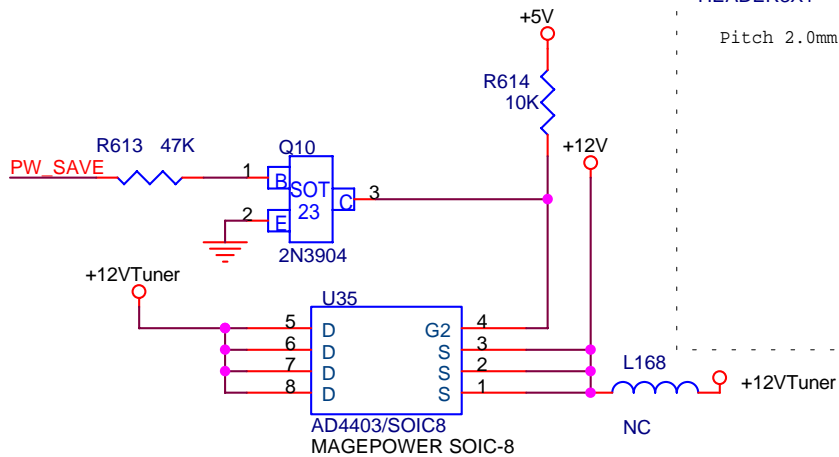
Memory Configuration



APLUS

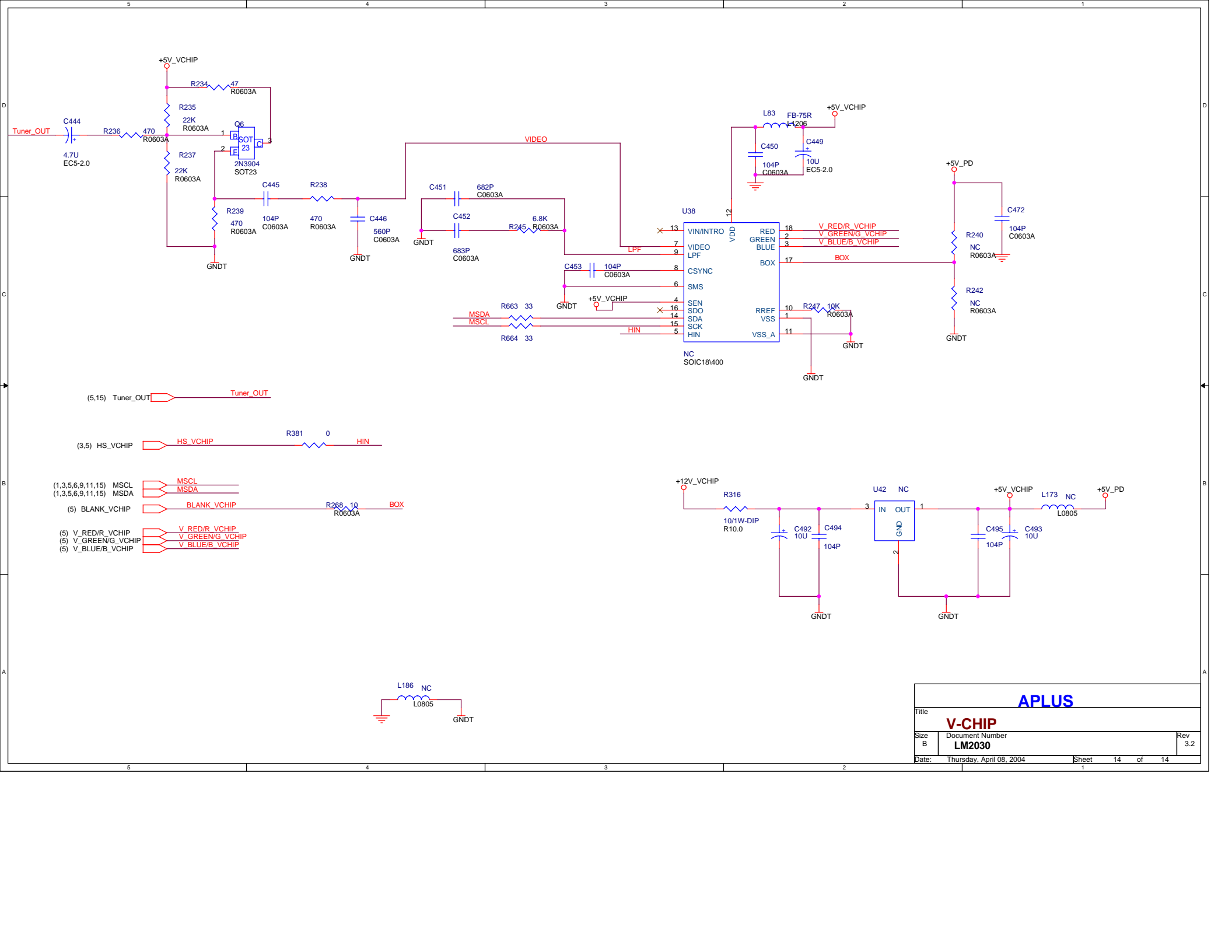
File: **TP6760**
Size: Document Number
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- MSCL MSCL (1,3,5,6,9,11,16)
- MSDA MSDA (1,3,5,6,9,11,16)
- MPX MPX (9)
- PW_SAVE PW_SAVE (5,9,11)
- Tuner_OUT Tuner_OUT (5,16)
- 2IF 2IF (9)



APLUS

Title		
Tuner Interface		
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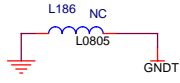
(5,15) Tuner_OUT Tuner_OUT

(3,5) HS_VCHIP HS_VCHIP R381 0 HIN

(1,3,5,6,9,11,15) MSCL MSCL
 (1,3,5,6,9,11,15) MSDA MSDA

(5) BLANK_VCHIP BLANK_VCHIP R268 10 BOX

(5) V_RED/R_VCHIP V_RED/R_VCHIP
 (5) V_GREEN/G_VCHIP V_GREEN/G_VCHIP
 (5) V_BLUE/B_VCHIP V_BLUE/B_VCHIP



APLUS		
Title		
V-CHIP		
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