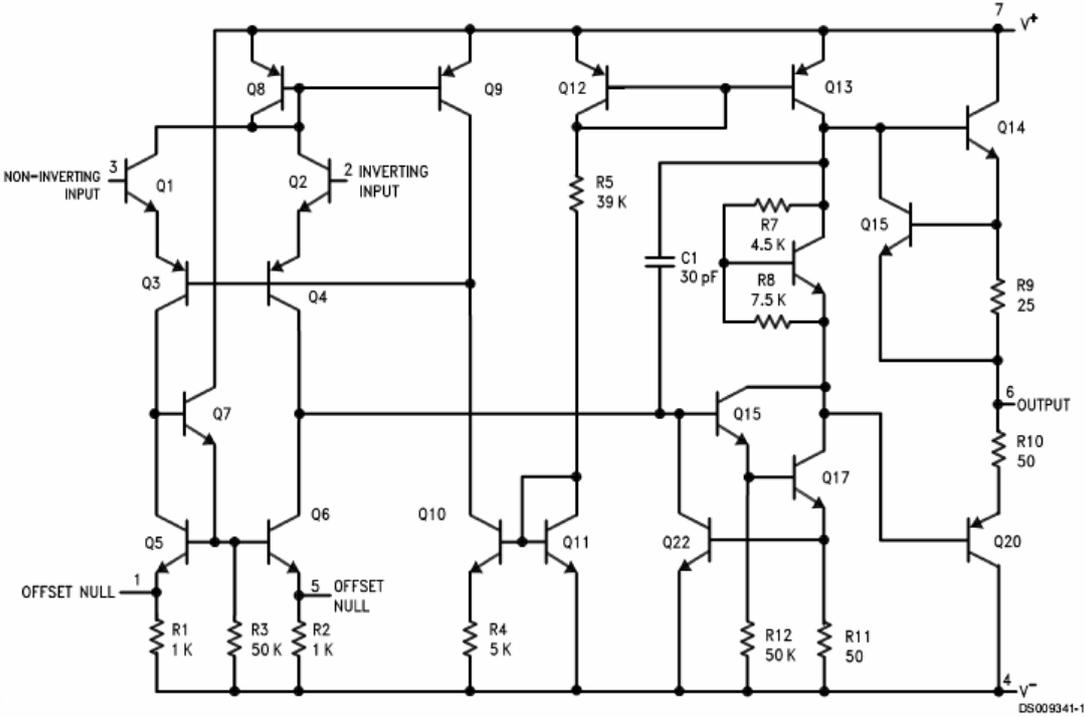


Schematic Diagram



Offset Nulling Circuit

