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; Assembly code generated by mikroVirtualMachine - V. 5.0.0.3
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; ADDRESS      OPCODE      ASM
; -----
$0000 $2B81          GOTO  _main
$0374 $          _usart_data_ready:
;USART_b21.ppas,48 ::
;USART_b21.ppas,50 ::
$0374 $1303          BCF   STATUS, RP1
$0375 $1283          BCF   STATUS, RP0
$0376 $080C          MOVF  PIR1, 0
$0377 $00F1          MOVWF STACK_1
$0378 $3000          MOVLW 0
$0379 $1AF1          BTFSC STACK_1, 5
$037A $3001          MOVLW 1
$037B $00F1          MOVWF STACK_1
$037C $0871          MOVF  STACK_1, 0
$037D $00D1          MOVWF _usart_data_ready_local_result
$037E $          USART_b21_L_18:
;USART_b21.ppas,51 ::
$037E $0851          MOVF  _usart_data_ready_local_result, 0
$037F $00F0          MOVWF STACK_0
$0380 $0008          RETURN
$032D $          _usart_read:
;USART_b21.ppas,54 ::
;USART_b21.ppas,55 ::
$032D $1303          BCF   STATUS, RP1
$032E $1283          BCF   STATUS, RP0
$032F $081A          MOVF  RCREG, 0
$0330 $00D1          MOVWF _usart_read_local_result
;USART_b21.ppas,61 ::
$0331 $0818          MOVF  RCSTA, 0
$0332 $00F1          MOVWF STACK_1
$0333 $3000          MOVLW 0
$0334 $18F1          BTFSC STACK_1, 1
$0335 $3001          MOVLW 1
$0336 $00F1          MOVWF STACK_1
$0337 $0871          MOVF  STACK_1, 0
$0338 $3A01          XORLW 1
$0339 $1D03          BTFSS STATUS, Z
$033A $2B3D          GOTO  USART_b21_L_21
$033B $          USART_b21_L_20:
;USART_b21.ppas,63 ::
$033B $1218          BCF   RCSTA, 4
$033C $          USART_b21_L_24:
;USART_b21.ppas,64 ::
$033C $          USART_b21_L_25:
$033C $1618          BSF   RCSTA, 4
$033D $          USART_b21_L_26:
;USART_b21.ppas,65 ::
$033D $          USART_b21_L_21:
$033D $          USART_b21_L_22:
$033D $          USART_b21_L_19:
;USART_b21.ppas,66 ::
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$033D $0851          MOVF  _usart_read_local_result, 0
$033E $00F0          MOVWF STACK_0
$033F $0008          RETURN
$0266 $      _usart_read_text:
;USART_b21.ppas,78 ::
;USART_b21.ppas,79 ::
$0266 $1303          BCF   STATUS, RP1
$0267 $1283          BCF   STATUS, RP0
$0268 $01CC          CLRF  _usart_read_text_local_i, 1
;USART_b21.ppas,80 ::
$0269 $01CD          CLRF  _usart_read_text_local_j, 1
;USART_b21.ppas,81 ::
$026A $01D0          CLRF  _usart_read_text_local_lasti, 1
;USART_b21.ppas,82 ::
$026B $084B          MOVF  FARG_Usart_Read_Text+1, 0
$026C $0084          MOVWF FSR
$026D $0800          MOVF  INDF, 0
$026E $00CF          MOVWF _usart_read_text_local_data2
;USART_b21.ppas,83 ::
$026F $      USART_b21_L_29:
;USART_b21.ppas,85 ::
$026F $2374          CALL  _usart_data_ready
$0270 $0870          MOVF  STACK_0, 0
$0271 $3A00          XORLW 0
$0272 $1903          BTFSC STATUS, Z
$0273 $2A9F          GOTO  USART_b21_L_34
$0274 $      USART_b21_L_33:
;USART_b21.ppas,87 ::
$0274 $232D          CALL  _usart_read
$0275 $0870          MOVF  STACK_0, 0
$0276 $00CE          MOVWF _usart_read_text_local_data
;USART_b21.ppas,88 ::
$0277 $084C          MOVF  _usart_read_text_local_i, 0
$0278 $074A          ADDWF FARG_Usart_Read_Text, 0
$0279 $00F1          MOVWF STACK_1
$027A $0871          MOVF  STACK_1, 0
$027B $0084          MOVWF FSR
$027C $0871          MOVF  STACK_1, 0
$027D $0084          MOVWF FSR
$027E $0870          MOVF  STACK_0, 0
$027F $0080          MOVWF INDF
;USART_b21.ppas,89 ::
$0280 $084E          MOVF  _usart_read_text_local_data, 0
$0281 $064F          XORWF _usart_read_text_local_data2, 0
$0282 $1D03          BTFSS STATUS, Z
$0283 $2A89          GOTO  USART_b21_L_37
$0284 $      USART_b21_L_36:
;USART_b21.ppas,91 ::
$0284 $084D          MOVF  _usart_read_text_local_j, 0
$0285 $024C          SUBWF _usart_read_text_local_i, 0
$0286 $00D0          MOVWF _usart_read_text_local_lasti
;USART_b21.ppas,92 ::
$0287 $0ACD          INCF  _usart_read_text_local_j, 1
;USART_b21.ppas,93 ::
$0288 $2A8B          GOTO  USART_b21_L_38
;USART_b21.ppas,94 ::
$0289 $      USART_b21_L_37:

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;USART_b21.ppas,96 ::
$0289 $01CD          CLRf  _usart_read_text_local_j, 1
;USART_b21.ppas,97 ::
$028A $01D0          CLRf  _usart_read_text_local_lasti, 1
;USART_b21.ppas,98 ::
$028B $          USART_b21_L_38:
;USART_b21.ppas,100 ::
$028B $084D          MOVf  _usart_read_text_local_j, 0
$028C $074B          ADDWF FARG_Usart_Read_Text+1, 0
$028D $0084          MOVWF FSR
$028E $0800          MOVf  INDF, 0
$028F $00F1          MOVWF STACK_1
$0290 $0871          MOVf  STACK_1, 0
$0291 $00CF          MOVWF _usart_read_text_local_data2
;USART_b21.ppas,102 ::
$0292 $0871          MOVf  STACK_1, 0
$0293 $3A00          XORLW 0
$0294 $1D03          BTFSS STATUS, Z
$0295 $2A97          GOTO  USART_b21_L_40
$0296 $          USART_b21_L_39:
;USART_b21.ppas,103 ::
$0296 $2AA0          GOTO  USART_b21_L_30
$0297 $          USART_b21_L_40:
$0297 $          USART_b21_L_41:
;USART_b21.ppas,105 ::
$0297 $0ACC          INCF  _usart_read_text_local_i, 1
;USART_b21.ppas,106 ::
$0298 $084C          MOVf  _usart_read_text_local_i, 0
$0299 $3C14          SUBLW 20
$029A $1803          BTFSC STATUS, C
$029B $2A9F          GOTO  USART_b21_L_43
$029C $          USART_b21_L_42:
;USART_b21.ppas,108 ::
$029C $3013          MOVLW 19
$029D $00D0          MOVWF _usart_read_text_local_lasti
;USART_b21.ppas,109 ::
$029E $2AA0          GOTO  USART_b21_L_30
;USART_b21.ppas,110 ::
$029F $          USART_b21_L_43:
$029F $          USART_b21_L_44:
;USART_b21.ppas,111 ::
$029F $          USART_b21_L_34:
$029F $          USART_b21_L_35:
;USART_b21.ppas,112 ::
$029F $2A6F          GOTO  USART_b21_L_29
$02A0 $          USART_b21_L_30:
;USART_b21.ppas,113 ::
$02A0 $0850          MOVf  _usart_read_text_local_lasti, 0
$02A1 $074A          ADDWF FARG_Usart_Read_Text, 0
$02A2 $00F0          MOVWF STACK_0
$02A3 $0870          MOVf  STACK_0, 0
$02A4 $0084          MOVWF FSR
$02A5 $0870          MOVf  STACK_0, 0
$02A6 $0084          MOVWF FSR
$02A7 $0180          CLRf  INDF, 1
$02A8 $          USART_b21_L_27:
;USART_b21.ppas,114 ::

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$02A8 $0008          RETURN
$0363 $      _strlen:
;StringsLib.ppas,204 ::
;StringsLib.ppas,205 ::
$0363 $1303          BCF   STATUS, RP1
$0364 $1283          BCF   STATUS, RP0
$0365 $01CB          CLRf  _strlen_local_result, 1
;StringsLib.ppas,206 ::
$0366 $      StringsLib_L_89:
$0366 $084B          MOVF  _strlen_local_result, 0
$0367 $074A          ADDWF FARG_strlen, 0
$0368 $0084          MOVWF FSR
$0369 $0800          MOVF  INDF, 0
$036A $00F1          MOVWF STACK_1
$036B $0871          MOVF  STACK_1, 0
$036C $3A00          XORLW 0
$036D $1903          BTFSC STATUS, Z
$036E $2B71          GOTO  StringsLib_L_90
;StringsLib.ppas,207 ::
$036F $0ACB          INCF  _strlen_local_result, 1
$0370 $2B66          GOTO  StringsLib_L_89
$0371 $      StringsLib_L_90:
$0371 $      StringsLib_L_87:
;StringsLib.ppas,208 ::
$0371 $084B          MOVF  _strlen_local_result, 0
$0372 $00F0          MOVWF STACK_0
$0373 $0008          RETURN
$0340 $      _usart_write:
;USART_b21.ppas,40 ::
;USART_b21.ppas,42 ::
$0340 $      USART_b21_L_14:
$0340 $1303          BCF   STATUS, RP1
$0341 $1683          BSF   STATUS, RP0
$0342 $0818          MOVF  TXSTA, 0
$0343 $00F1          MOVWF STACK_1
$0344 $3000          MOVLW 0
$0345 $18F1          BTFSC STACK_1, 1
$0346 $3001          MOVLW 1
$0347 $00F1          MOVWF STACK_1
$0348 $0871          MOVF  STACK_1, 0
$0349 $3A00          XORLW 0
$034A $1D03          BTFSS STATUS, Z
$034B $2B4E          GOTO  USART_b21_L_15
;USART_b21.ppas,43 ::
$034C $0000          NOP
$034D $2B40          GOTO  USART_b21_L_14
$034E $      USART_b21_L_15:
;USART_b21.ppas,44 ::
$034E $1283          BCF   STATUS, RP0
$034F $084A          MOVF  FARG_USART_Write, 0
$0350 $0099          MOVWF TXREG
$0351 $      USART_b21_L_12:
;USART_b21.ppas,45 ::
$0351 $0008          RETURN
$0004 $      _interrupt:
$0004 $00FF          MOVWF STACK_15
$0005 $0E03          SWAPF STATUS, 0

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$0006 $0183          CLRf  STATUS
$0007 $00C5          MOVWf ?saveSTATUS
$0008 $0804          MOVf  FSR, 0
$0009 $00C4          MOVWf ?saveFSR
$000A $080A          MOVf  PCLATH, 0
$000B $00C6          MOVWf ?savePCLATH
$000C $018A          CLRf  PCLATH
$000D $0871          MOVf  STACK_1, 0
$000E $00DE          MOVWf STSAVED_1
$000F $0870          MOVf  STACK_0, 0
$0010 $00DD          MOVWf STSAVED_0
;proglcd.ppas,43 ::      begin
;proglcd.ppas,44 ::      PIE1:=0;
$0011 $1683          BSF  STATUS, RP0
$0012 $018C          CLRf  PIE1, 1
$0013 $0064          CLRWDT
;proglcd.ppas,46 ::      if Usart_Data_Ready() = 1 then
Usart_Read_Text(data, '>>');
$0014 $2374          CALL  _usart_data_ready
$0015 $0870          MOVf  STACK_0, 0
$0016 $3A01          XORLW 1
$0017 $1D03          BTFSS STATUS, Z
$0018 $2823          GOTO  proglcd_L_5
$0019 $          proglcd_L_4:
$0019 $3020          MOVLW _data
$001A $00CA          MOVWf FARG_Usart_Read_Text
$001B $303E          MOVLW 62
$001C $00C7          MOVWf ?interrupt_Local_Text+0
$001D $303E          MOVLW 62
$001E $00C8          MOVWf ?interrupt_Local_Text+1
$001F $01C9          CLRf  ?interrupt_Local_Text+2
$0020 $3047          MOVLW ?interrupt_Local_Text
$0021 $00CB          MOVWf FARG_Usart_Read_Text+1
$0022 $2266          CALL  _usart_read_text
$0023 $          proglcd_L_5:
$0023 $          proglcd_L_6:
;proglcd.ppas,47 ::      i:=Strlen(data);
$0023 $3020          MOVLW _data
$0024 $00CA          MOVWf FARG_strlen
$0025 $2363          CALL  _strlen
$0026 $0870          MOVf  STACK_0, 0
$0027 $00B4          MOVWf _i
;proglcd.ppas,48 ::      for j:=i+1 to 19 do data[j]:='_';
$0028 $0A70          INCF  STACK_0, 0
$0029 $00B5          MOVWf _j
$002A $          proglcd_L_7:
$002A $0835          MOVf  _j, 0
$002B $3C13          SUBLW 19
$002C $1C03          BTFSS STATUS, C
$002D $283E          GOTO  proglcd_L_10
$002E $3020          MOVLW _data
$002F $00F0          MOVWf STACK_0
$0030 $0835          MOVf  _j, 0
$0031 $07F0          ADDWf STACK_0, 1
$0032 $0870          MOVf  STACK_0, 0
$0033 $0084          MOVWf FSR
$0034 $0870          MOVf  STACK_0, 0

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$0035 $0084          MOVWF FSR
$0036 $305F          MOVLW 95
$0037 $0080          MOVWF INDF
$0038 $0835          MOVF  _j, 0
$0039 $3A13          XORLW 19
$003A $1903          BTFSC STATUS, Z
$003B $283E          GOTO  proglcd_L_10
$003C $              proglcd_L_8:
$003C $0AB5          INCF  _j, 1
$003D $282A          GOTO  proglcd_L_7
$003E $              proglcd_L_10:
;proglcd.ppas,49 ::      Usart_Write(13);
$003E $300D          MOVLW 13
$003F $00CA          MOVWF FARG_USART_Write
$0040 $2340          CALL  _usart_write
;proglcd.ppas,50 ::      trame_recu:=1;
$0041 $3001          MOVLW 1
$0042 $00B6          MOVWF _trame_recu
;proglcd.ppas,51 ::      PIR1:=0;
$0043 $018C          CLRWF PIR1, 1
;proglcd.ppas,52 ::      PIE1:=$20;
$0044 $3020          MOVLW 32
$0045 $1683          BSF  STATUS, RP0
$0046 $008C          MOVWF PIE1
$0047 $              proglcd_L_3:
;proglcd.ppas,53 ::      end;
$0047 $              L_Interrupt_end:
$0047 $1283          BCF  STATUS, RP0
$0048 $085E          MOVF  STSAVED_1, 0
$0049 $00F1          MOVWF STACK_1
$004A $085D          MOVF  STSAVED_0, 0
$004B $00F0          MOVWF STACK_0
$004C $0846          MOVF  ?savePCLATH, 0
$004D $008A          MOVWF PCLATH
$004E $0844          MOVF  ?saveFSR, 0
$004F $0084          MOVWF FSR
$0050 $0E45          SWAPF ?saveSTATUS, 0
$0051 $0083          MOVWF STATUS
$0052 $0EFF          SWAPF STACK_15, 1
$0053 $0E7F          SWAPF STACK_15, 0
$0054 $0009          RETFIE
$0395 $              _delay_1us:
;delays.ppas,11 ::      begin
;delays.ppas,12 ::      Delay_us(1);
$0395 $0000          NOP
$0396 $              delays_L_1:
;delays.ppas,13 ::      end;
$0396 $0008          RETURN
$0352 $              _delay_5500us:
;delays.ppas,41 ::      begin
;delays.ppas,42 ::      Delay_us(5500);
$0352 $3008          MOVLW 8
$0353 $1303          BCF  STATUS, RP1
$0354 $1283          BCF  STATUS, RP0
$0355 $00FB          MOVWF STACK_11
$0356 $30FF          MOVLW 255
$0357 $00FA          MOVWF STACK_10

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$0358 $0BFB          DECFSZ      STACK_11, F
$0359 $2B5B          GOTO      $+2
$035A $2B5E          GOTO      $+4
$035B $0BFA          DECFSZ      STACK_10, F
$035C $2B5B          GOTO      $-1
$035D $2B58          GOTO      $-5
$035E $301E          MOVLW     30
$035F $00FA          MOVWF     STACK_10
$0360 $0BFA          DECFSZ      STACK_10, F
$0361 $2B60          GOTO      $-1
$0362 $      delays_L_7:
;delays.ppas,43 ::          end;
$0362 $0008          RETURN
$038B $      _delay_50us:
;delays.ppas,26 ::          begin
;delays.ppas,27 ::          Delay_us(50);
$038B $3010          MOVLW     16
$038C $1303          BCF      STATUS, RP1
$038D $1283          BCF      STATUS, RP0
$038E $00FA          MOVWF     STACK_10
$038F $0BFA          DECFSZ      STACK_10, F
$0390 $2B8F          GOTO      $-1
$0391 $0000          NOP
$0392 $      delays_L_4:
;delays.ppas,28 ::          end;
$0392 $0008          RETURN
$0174 $      _lcd_cmd:
;lcd.ppas,19  ::
;lcd.ppas,20  ::
$0174 $1303          BCF      STATUS, RP1
$0175 $1283          BCF      STATUS, RP0
$0176 $0837          MOVF     lcd_ctrlport_address, 0
$0177 $0084          MOVWF     FSR
;lcd.ppas,22  ::
$0178 $0938          COMF     lcd___menable, W
$0179 $00F0          MOVWF     STACK_0
$017A $0870          MOVF     STACK_0, 0
$017B $0580          ANDWF     INDF, 1
;lcd.ppas,23  ::
$017C $0939          COMF     lcd___mrs, W
$017D $00F0          MOVWF     STACK_0
$017E $0870          MOVF     STACK_0, 0
$017F $0580          ANDWF     INDF, 1
;lcd.ppas,25  ::
$0180 $083A          MOVF     lcd_dataport_address, 0
$0181 $0084          MOVWF     FSR
;lcd.ppas,26  ::
$0182 $083C          MOVF     lcd___mdb6, 0
$0183 $043B          IORWF     lcd___mdb7, 0
$0184 $00F0          MOVWF     STACK_0
$0185 $083D          MOVF     lcd___mdb5, 0
$0186 $04F0          IORWF     STACK_0, 1
$0187 $083E          MOVF     lcd___mdb4, 0
$0188 $04F0          IORWF     STACK_0, 1
$0189 $09F0          COMF     STACK_0, F
$018A $0870          MOVF     STACK_0, 0
$018B $0580          ANDWF     INDF, 1

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;lcd.ppas,28 ::
$018C $3000          MOVLW 0
$018D $1BDC          BTFSC FARG_Lcd_Cmd, 7
$018E $3001          MOVLW 1
$018F $00F1          MOVWF STACK_1
$0190 $0871          MOVF  STACK_1, 0
$0191 $3A01          XORLW 1
$0192 $1D03          BTFSS STATUS, Z
$0193 $2996          GOTO  lcd_L_2
$0194 $      lcd_L_1:
;lcd.ppas,29 ::
$0194 $083B          MOVF  lcd_____mdb7, 0
$0195 $0480          IORWF INDF, 1
$0196 $      lcd_L_2:
$0196 $      lcd_L_3:
;lcd.ppas,30 ::
$0196 $3000          MOVLW 0
$0197 $1B5C          BTFSC FARG_Lcd_Cmd, 6
$0198 $3001          MOVLW 1
$0199 $00F1          MOVWF STACK_1
$019A $0871          MOVF  STACK_1, 0
$019B $3A01          XORLW 1
$019C $1D03          BTFSS STATUS, Z
$019D $29A0          GOTO  lcd_L_5
$019E $      lcd_L_4:
;lcd.ppas,31 ::
$019E $083C          MOVF  lcd_____mdb6, 0
$019F $0480          IORWF INDF, 1
$01A0 $      lcd_L_5:
$01A0 $      lcd_L_6:
;lcd.ppas,32 ::
$01A0 $3000          MOVLW 0
$01A1 $1ADC          BTFSC FARG_Lcd_Cmd, 5
$01A2 $3001          MOVLW 1
$01A3 $00F1          MOVWF STACK_1
$01A4 $0871          MOVF  STACK_1, 0
$01A5 $3A01          XORLW 1
$01A6 $1D03          BTFSS STATUS, Z
$01A7 $29AA          GOTO  lcd_L_8
$01A8 $      lcd_L_7:
;lcd.ppas,33 ::
$01A8 $083D          MOVF  lcd_____mdb5, 0
$01A9 $0480          IORWF INDF, 1
$01AA $      lcd_L_8:
$01AA $      lcd_L_9:
;lcd.ppas,34 ::
$01AA $3000          MOVLW 0
$01AB $1A5C          BTFSC FARG_Lcd_Cmd, 4
$01AC $3001          MOVLW 1
$01AD $00F1          MOVWF STACK_1
$01AE $0871          MOVF  STACK_1, 0
$01AF $3A01          XORLW 1
$01B0 $1D03          BTFSS STATUS, Z
$01B1 $29B4          GOTO  lcd_L_11
$01B2 $      lcd_L_10:
;lcd.ppas,35 ::
$01B2 $083E          MOVF  lcd_____mdb4, 0

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$01B3 $0480          IORWF INDF, 1
$01B4 $          lcd_L_11:
$01B4 $          lcd_L_12:
;lcd.ppas,37 ::
$01B4 $0837          MOVF lcd_ctrlport_address, 0
$01B5 $0084          MOVWF FSR
;lcd.ppas,38 ::
$01B6 $083F          MOVF lcd_lcdcmd_status, 0
$01B7 $3A01          XORLW 1
$01B8 $1D03          BTFSS STATUS, Z
$01B9 $29BF          GOTO lcd_L_14
$01BA $          lcd_L_13:
;lcd.ppas,39 ::
$01BA $0939          COMF lcd____mrs, W
$01BB $00F0          MOVWF STACK_0
$01BC $0870          MOVF STACK_0, 0
$01BD $0580          ANDWF INDF, 1
$01BE $29C1          GOTO lcd_L_15
;lcd.ppas,40 ::
$01BF $          lcd_L_14:
;lcd.ppas,41 ::
$01BF $0839          MOVF lcd____mrs, 0
$01C0 $0480          IORWF INDF, 1
$01C1 $          lcd_L_15:
;lcd.ppas,43 ::
$01C1 $0838          MOVF lcd____menable, 0
$01C2 $0480          IORWF INDF, 1
;lcd.ppas,44 ::
$01C3 $2395          CALL _delay_1us
;lcd.ppas,45 ::
$01C4 $0938          COMF lcd____menable, W
$01C5 $00F0          MOVWF STACK_0
$01C6 $0870          MOVF STACK_0, 0
$01C7 $0580          ANDWF INDF, 1
;lcd.ppas,47 ::
$01C8 $0939          COMF lcd____mrs, W
$01C9 $00F0          MOVWF STACK_0
$01CA $0870          MOVF STACK_0, 0
$01CB $0580          ANDWF INDF, 1
;lcd.ppas,49 ::
$01CC $083A          MOVF lcd_dataport_address, 0
$01CD $0084          MOVWF FSR
;lcd.ppas,50 ::
$01CE $083C          MOVF lcd____mdb6, 0
$01CF $043B          IORWF lcd____mdb7, 0
$01D0 $00F0          MOVWF STACK_0
$01D1 $083D          MOVF lcd____mdb5, 0
$01D2 $04F0          IORWF STACK_0, 1
$01D3 $083E          MOVF lcd____mdb4, 0
$01D4 $04F0          IORWF STACK_0, 1
$01D5 $09F0          COMF STACK_0, F
$01D6 $0870          MOVF STACK_0, 0
$01D7 $0580          ANDWF INDF, 1
;lcd.ppas,52 ::
$01D8 $3000          MOVLW 0
$01D9 $19DC          BTFSC FARG_Lcd_Cmd, 3
$01DA $3001          MOVLW 1

```

```

$01DB $00F1      MOVWF STACK_1
$01DC $0871      MOVF  STACK_1, 0
$01DD $3A01      XORLW 1
$01DE $1D03      BTFSS STATUS, Z
$01DF $29E2      GOTO  lcd_L_17
$01E0 $          lcd_L_16:
;lcd.ppas,53 ::
$01E0 $083B      MOVF  lcd_____mdb7, 0
$01E1 $0480      IORWF INDF, 1
$01E2 $          lcd_L_17:
$01E2 $          lcd_L_18:
;lcd.ppas,54 ::
$01E2 $3000      MOVLW 0
$01E3 $195C      BTFSC FARG_Lcd_Cmd, 2
$01E4 $3001      MOVLW 1
$01E5 $00F1      MOVWF STACK_1
$01E6 $0871      MOVF  STACK_1, 0
$01E7 $3A01      XORLW 1
$01E8 $1D03      BTFSS STATUS, Z
$01E9 $29EC      GOTO  lcd_L_20
$01EA $          lcd_L_19:
;lcd.ppas,55 ::
$01EA $083C      MOVF  lcd_____mdb6, 0
$01EB $0480      IORWF INDF, 1
$01EC $          lcd_L_20:
$01EC $          lcd_L_21:
;lcd.ppas,56 ::
$01EC $3000      MOVLW 0
$01ED $18DC      BTFSC FARG_Lcd_Cmd, 1
$01EE $3001      MOVLW 1
$01EF $00F1      MOVWF STACK_1
$01F0 $0871      MOVF  STACK_1, 0
$01F1 $3A01      XORLW 1
$01F2 $1D03      BTFSS STATUS, Z
$01F3 $29F6      GOTO  lcd_L_23
$01F4 $          lcd_L_22:
;lcd.ppas,57 ::
$01F4 $083D      MOVF  lcd_____mdb5, 0
$01F5 $0480      IORWF INDF, 1
$01F6 $          lcd_L_23:
$01F6 $          lcd_L_24:
;lcd.ppas,58 ::
$01F6 $3001      MOVLW 1
$01F7 $055C      ANDWF FARG_Lcd_Cmd, 0
$01F8 $00F1      MOVWF STACK_1
$01F9 $0871      MOVF  STACK_1, 0
$01FA $3A01      XORLW 1
$01FB $1D03      BTFSS STATUS, Z
$01FC $29FF      GOTO  lcd_L_26
$01FD $          lcd_L_25:
;lcd.ppas,59 ::
$01FD $083E      MOVF  lcd_____mdb4, 0
$01FE $0480      IORWF INDF, 1
$01FF $          lcd_L_26:
$01FF $          lcd_L_27:
;lcd.ppas,61 ::
$01FF $0837      MOVF  lcd_ctrlport_address, 0

```

```

$0200 $0084          MOVWF FSR
;lcd.ppas,62 ::
$0201 $083F          MOVF  lcd_lcdcmd_status, 0
$0202 $3A01          XORLW 1
$0203 $1D03          BTFSS STATUS, Z
$0204 $2A0A          GOTO  lcd_L_29
$0205 $          lcd_L_28:
;lcd.ppas,63 ::
$0205 $0939          COMF  lcd___mrs, W
$0206 $00F0          MOVWF STACK_0
$0207 $0870          MOVF  STACK_0, 0
$0208 $0580          ANDWF INDF, 1
$0209 $2A0C          GOTO  lcd_L_30
;lcd.ppas,64 ::
$020A $          lcd_L_29:
;lcd.ppas,65 ::
$020A $0839          MOVF  lcd___mrs, 0
$020B $0480          IORWF INDF, 1
$020C $          lcd_L_30:
;lcd.ppas,67 ::
$020C $0838          MOVF  lcd___menable, 0
$020D $0480          IORWF INDF, 1
;lcd.ppas,68 ::
$020E $2395          CALL  _delay_1us
;lcd.ppas,69 ::
$020F $0938          COMF  lcd___menable, W
$0210 $00F0          MOVWF STACK_0
$0211 $0870          MOVF  STACK_0, 0
$0212 $0580          ANDWF INDF, 1
;lcd.ppas,71 ::
$0213 $083F          MOVF  lcd_lcdcmd_status, 0
$0214 $3A01          XORLW 1
$0215 $1D03          BTFSS STATUS, Z
$0216 $2A19          GOTO  lcd_L_32
$0217 $          lcd_L_31:
;lcd.ppas,72 ::
$0217 $2352          CALL  _delay_5500us
$0218 $2A1A          GOTO  lcd_L_33
;lcd.ppas,73 ::
$0219 $          lcd_L_32:
;lcd.ppas,74 ::
$0219 $238B          CALL  _delay_50us
$021A $          lcd_L_33:
$021A $          lcd_L_0:
;lcd.ppas,75 ::
$021A $0008          RETURN
$0055 $          _lcd_config:
;lcd.ppas,115 ::
;lcd.ppas,116 ::
$0055 $2352          CALL  _delay_5500us
;lcd.ppas,117 ::
$0056 $2352          CALL  _delay_5500us
;lcd.ppas,118 ::
$0057 $2352          CALL  _delay_5500us
;lcd.ppas,120 ::
$0058 $0854          MOVF  FARG_Lcd_Config+1, 0
$0059 $00F0          MOVWF STACK_0

```

```

$005A $3001          MOVLW 1
$005B $00F4          MOVWF STACK_4
$005C $0870          MOVF  STACK_0, 0
$005D $      L_Lcd_Config_0:
$005D $1903          BTFSC STATUS, Z
$005E $2863          GOTO  L_Lcd_Config_1
$005F $0DF4          RLF  STACK_4, 1
$0060 $1074          BCF  STACK_4, 0
$0061 $3FFF          ADDLW 255
$0062 $285D          GOTO  L_Lcd_Config_0
$0063 $      L_Lcd_Config_1:
$0063 $0874          MOVF  STACK_4, 0
$0064 $00BB          MOVWF lcd____mdb7
;lcd.ppas,121 ::
$0065 $0855          MOVF  FARG_Lcd_Config+2, 0
$0066 $00F0          MOVWF STACK_0
$0067 $3001          MOVLW 1
$0068 $00F3          MOVWF STACK_3
$0069 $0870          MOVF  STACK_0, 0
$006A $      L_Lcd_Config_2:
$006A $1903          BTFSC STATUS, Z
$006B $2870          GOTO  L_Lcd_Config_3
$006C $0DF3          RLF  STACK_3, 1
$006D $1073          BCF  STACK_3, 0
$006E $3FFF          ADDLW 255
$006F $286A          GOTO  L_Lcd_Config_2
$0070 $      L_Lcd_Config_3:
$0070 $0873          MOVF  STACK_3, 0
$0071 $00BC          MOVWF lcd____mdb6
;lcd.ppas,122 ::
$0072 $0856          MOVF  FARG_Lcd_Config+3, 0
$0073 $00F0          MOVWF STACK_0
$0074 $3001          MOVLW 1
$0075 $00F2          MOVWF STACK_2
$0076 $0870          MOVF  STACK_0, 0
$0077 $      L_Lcd_Config_4:
$0077 $1903          BTFSC STATUS, Z
$0078 $287D          GOTO  L_Lcd_Config_5
$0079 $0DF2          RLF  STACK_2, 1
$007A $1072          BCF  STACK_2, 0
$007B $3FFF          ADDLW 255
$007C $2877          GOTO  L_Lcd_Config_4
$007D $      L_Lcd_Config_5:
$007D $0872          MOVF  STACK_2, 0
$007E $00BD          MOVWF lcd____mdb5
;lcd.ppas,123 ::
$007F $0857          MOVF  FARG_Lcd_Config+4, 0
$0080 $00F0          MOVWF STACK_0
$0081 $3001          MOVLW 1
$0082 $00F1          MOVWF STACK_1
$0083 $0870          MOVF  STACK_0, 0
$0084 $      L_Lcd_Config_6:
$0084 $1903          BTFSC STATUS, Z
$0085 $288A          GOTO  L_Lcd_Config_7
$0086 $0DF1          RLF  STACK_1, 1
$0087 $1071          BCF  STACK_1, 0
$0088 $3FFF          ADDLW 255

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```

$0089 $2884          GOTO  L_Lcd_Config_6
$008A $          L_Lcd_Config_7:
$008A $0871          MOVF  STACK_1, 0
$008B $00BE          MOVWF lcd____mdb4
;lcd.ppas,124 ::
$008C $0859          MOVF  FARG_Lcd_Config+6, 0
$008D $00F0          MOVWF STACK_0
$008E $3001          MOVLW 1
$008F $00B9          MOVWF lcd____mrs
$0090 $0870          MOVF  STACK_0, 0
$0091 $          L_Lcd_Config_8:
$0091 $1903          BTFSC STATUS, Z
$0092 $2897          GOTO  L_Lcd_Config_9
$0093 $0DB9          RLF  lcd____mrs, 1
$0094 $1039          BCF  lcd____mrs, 0
$0095 $3FFF          ADDLW 255
$0096 $2891          GOTO  L_Lcd_Config_8
$0097 $          L_Lcd_Config_9:
;lcd.ppas,125 ::
$0097 $085A          MOVF  FARG_Lcd_Config+7, 0
$0098 $00F0          MOVWF STACK_0
$0099 $3001          MOVLW 1
$009A $00C0          MOVWF lcd____mrw
$009B $0870          MOVF  STACK_0, 0
$009C $          L_Lcd_Config_10:
$009C $1903          BTFSC STATUS, Z
$009D $28A2          GOTO  L_Lcd_Config_11
$009E $0DC0          RLF  lcd____mrw, 1
$009F $1040          BCF  lcd____mrw, 0
$00A0 $3FFF          ADDLW 255
$00A1 $289C          GOTO  L_Lcd_Config_10
$00A2 $          L_Lcd_Config_11:
;lcd.ppas,126 ::
$00A2 $085B          MOVF  FARG_Lcd_Config+8, 0
$00A3 $00F0          MOVWF STACK_0
$00A4 $3001          MOVLW 1
$00A5 $00B8          MOVWF lcd____menable
$00A6 $0870          MOVF  STACK_0, 0
$00A7 $          L_Lcd_Config_12:
$00A7 $1903          BTFSC STATUS, Z
$00A8 $28AD          GOTO  L_Lcd_Config_13
$00A9 $0DB8          RLF  lcd____menable, 1
$00AA $1038          BCF  lcd____menable, 0
$00AB $3FFF          ADDLW 255
$00AC $28A7          GOTO  L_Lcd_Config_12
$00AD $          L_Lcd_Config_13:
;lcd.ppas,128 ::
$00AD $0853          MOVF  FARG_Lcd_Config, 0
$00AE $00BA          MOVWF lcd_dataport_address
;lcd.ppas,129 ::
$00AF $0858          MOVF  FARG_Lcd_Config+5, 0
$00B0 $00B7          MOVWF lcd_ctrlport_address
;lcd.ppas,131 ::
$00B1 $0853          MOVF  FARG_Lcd_Config, 0
$00B2 $0084          MOVWF FSR
;lcd.ppas,133 ::
$00B3 $0873          MOVF  STACK_3, 0

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```

$00B4 $0474      IORWF STACK_4, 0
$00B5 $00F0      MOVWF STACK_0
$00B6 $0872      MOVF  STACK_2, 0
$00B7 $04F0      IORWF STACK_0, 1
$00B8 $0871      MOVF  STACK_1, 0
$00B9 $04F0      IORWF STACK_0, 1
$00BA $09F0      COMF  STACK_0, F
$00BB $0870      MOVF  STACK_0, 0
$00BC $0580      ANDWF INDF, 1
;lcd.ppas,134 ::
$00BD $          lcd_L_56:
$00BD $1784      BSF   FSR, 7
$00BE $          lcd_L_57:
;lcd.ppas,135 ::
$00BE $083C      MOVF  lcd____mdb6, 0
$00BF $043B      IORWF lcd____mdb7, 0
$00C0 $00F0      MOVWF STACK_0
$00C1 $083D      MOVF  lcd____mdb5, 0
$00C2 $04F0      IORWF STACK_0, 1
$00C3 $083E      MOVF  lcd____mdb4, 0
$00C4 $04F0      IORWF STACK_0, 1
$00C5 $09F0      COMF  STACK_0, F
$00C6 $0870      MOVF  STACK_0, 0
$00C7 $0580      ANDWF INDF, 1
;lcd.ppas,136 ::
$00C8 $1384      BCF   FSR, 7
$00C9 $          lcd_L_59:
;lcd.ppas,139 ::
$00C9 $0837      MOVF  lcd_ctrlport_address, 0
$00CA $0084      MOVWF FSR
;lcd.ppas,141 ::
$00CB $0840      MOVF  lcd____mrw, 0
$00CC $0439      IORWF lcd____mrs, 0
$00CD $00F0      MOVWF STACK_0
$00CE $0838      MOVF  lcd____menable, 0
$00CF $04F0      IORWF STACK_0, 1
$00D0 $09F0      COMF  STACK_0, F
$00D1 $0870      MOVF  STACK_0, 0
$00D2 $0580      ANDWF INDF, 1
;lcd.ppas,142 ::
$00D3 $          lcd_L_60:
$00D3 $1784      BSF   FSR, 7
$00D4 $          lcd_L_61:
;lcd.ppas,143 ::
$00D4 $0840      MOVF  lcd____mrw, 0
$00D5 $0439      IORWF lcd____mrs, 0
$00D6 $00F0      MOVWF STACK_0
$00D7 $0838      MOVF  lcd____menable, 0
$00D8 $04F0      IORWF STACK_0, 1
$00D9 $09F0      COMF  STACK_0, F
$00DA $0870      MOVF  STACK_0, 0
$00DB $0580      ANDWF INDF, 1
;lcd.ppas,144 ::
$00DC $1384      BCF   FSR, 7
$00DD $          lcd_L_63:
;lcd.ppas,148 ::
$00DD $083A      MOVF  lcd_dataport_address, 0

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```

$00DE $0084      MOVWF FSR
;lcd.ppas,149 ::
$00DF $083D      MOVF  lcd____mdb5, 0
$00E0 $0400      IORWF INDF, 0
$00E1 $00F0      MOVWF STACK_0
$00E2 $083E      MOVF  lcd____mdb4, 0
$00E3 $0470      IORWF STACK_0, 0
$00E4 $0080      MOVWF INDF
;lcd.ppas,151 ::
$00E5 $0837      MOVF  lcd_ctrlport_address, 0
$00E6 $0084      MOVWF FSR
;lcd.ppas,152 ::
$00E7 $0838      MOVF  lcd____menable, 0
$00E8 $0480      IORWF INDF, 1
;lcd.ppas,153 ::
$00E9 $0938      COMF  lcd____menable, W
$00EA $00F0      MOVWF STACK_0
$00EB $0870      MOVF  STACK_0, 0
$00EC $0580      ANDWF INDF, 1
;lcd.ppas,155 ::
$00ED $2352      CALL  _delay_5500us
;lcd.ppas,160 ::
$00EE $0838      MOVF  lcd____menable, 0
$00EF $0480      IORWF INDF, 1
;lcd.ppas,161 ::
$00F0 $0938      COMF  lcd____menable, W
$00F1 $00F0      MOVWF STACK_0
$00F2 $0870      MOVF  STACK_0, 0
$00F3 $0580      ANDWF INDF, 1
;lcd.ppas,163 ::
$00F4 $2352      CALL  _delay_5500us
;lcd.ppas,168 ::
$00F5 $0838      MOVF  lcd____menable, 0
$00F6 $0480      IORWF INDF, 1
;lcd.ppas,169 ::
$00F7 $0938      COMF  lcd____menable, W
$00F8 $00F0      MOVWF STACK_0
$00F9 $0870      MOVF  STACK_0, 0
$00FA $0580      ANDWF INDF, 1
;lcd.ppas,171 ::
$00FB $2352      CALL  _delay_5500us
;lcd.ppas,174 ::
$00FC $083A      MOVF  lcd_dataport_address, 0
$00FD $0084      MOVWF FSR
;lcd.ppas,175 ::
$00FE $083D      MOVF  lcd____mdb5, 0
$00FF $0480      IORWF INDF, 1
;lcd.ppas,176 ::
$0100 $093E      COMF  lcd____mdb4, W
$0101 $00F0      MOVWF STACK_0
$0102 $0870      MOVF  STACK_0, 0
$0103 $0580      ANDWF INDF, 1
;lcd.ppas,178 ::
$0104 $0837      MOVF  lcd_ctrlport_address, 0
$0105 $0084      MOVWF FSR
;lcd.ppas,179 ::
$0106 $0838      MOVF  lcd____menable, 0

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```

$0107 $0480          IORWF INDF, 1
;lcd.ppas,180 ::
$0108 $0938          COMF lcd___menable, W
$0109 $00F0          MOVWF STACK_0
$010A $0870          MOVF STACK_0, 0
$010B $0580          ANDWF INDF, 1
;lcd.ppas,182 ::
$010C $2352          CALL  _delay_5500us
;lcd.ppas,187 ::
$010D $0838          MOVF lcd___menable, 0
$010E $0480          IORWF INDF, 1
;lcd.ppas,188 ::
$010F $0938          COMF lcd___menable, W
$0110 $00F1          MOVWF STACK_1
$0111 $0871          MOVF STACK_1, 0
$0112 $0580          ANDWF INDF, 1
;lcd.ppas,190 ::
$0113 $083A          MOVF lcd_dataport_address, 0
$0114 $0084          MOVWF FSR
;lcd.ppas,191 ::
$0115 $093D          COMF lcd___mdb5, W
$0116 $00F0          MOVWF STACK_0
$0117 $0870          MOVF STACK_0, 0
$0118 $0580          ANDWF INDF, 1
;lcd.ppas,192 ::
$0119 $083B          MOVF lcd___mdb7, 0
$011A $0480          IORWF INDF, 1
;lcd.ppas,194 ::
$011B $0837          MOVF lcd_ctrlport_address, 0
$011C $0084          MOVWF FSR
;lcd.ppas,195 ::
$011D $0838          MOVF lcd___menable, 0
$011E $0480          IORWF INDF, 1
;lcd.ppas,196 ::
$011F $0871          MOVF STACK_1, 0
$0120 $0580          ANDWF INDF, 1
;lcd.ppas,198 ::
$0121 $2352          CALL  _delay_5500us
;lcd.ppas,201 ::
$0122 $083A          MOVF lcd_dataport_address, 0
$0123 $0084          MOVWF FSR
;lcd.ppas,202 ::
$0124 $093B          COMF lcd___mdb7, W
$0125 $00F0          MOVWF STACK_0
$0126 $0870          MOVF STACK_0, 0
$0127 $0580          ANDWF INDF, 1
;lcd.ppas,203 ::
$0128 $083E          MOVF lcd___mdb4, 0
$0129 $0480          IORWF INDF, 1
;lcd.ppas,204 ::
$012A $0837          MOVF lcd_ctrlport_address, 0
$012B $0084          MOVWF FSR
;lcd.ppas,205 ::
$012C $0838          MOVF lcd___menable, 0
$012D $0480          IORWF INDF, 1
;lcd.ppas,206 ::
$012E $0938          COMF lcd___menable, W

```

```

$012F $00F1      MOVWF STACK_1
$0130 $0871      MOVF  STACK_1, 0
$0131 $0580      ANDWF INDF, 1
;lcd.ppas,207 ::
$0132 $083A      MOVF  lcd_dataport_address, 0
$0133 $0084      MOVWF FSR
;lcd.ppas,208 ::
$0134 $093E      COMF  lcd____mdb4, W
$0135 $00F0      MOVWF STACK_0
$0136 $0870      MOVF  STACK_0, 0
$0137 $0580      ANDWF INDF, 1
;lcd.ppas,209 ::
$0138 $0837      MOVF  lcd_ctrlport_address, 0
$0139 $0084      MOVWF FSR
;lcd.ppas,210 ::
$013A $0838      MOVF  lcd____menable, 0
$013B $0480      IORWF INDF, 1
;lcd.ppas,211 ::
$013C $0871      MOVF  STACK_1, 0
$013D $0580      ANDWF INDF, 1
;lcd.ppas,213 ::
$013E $2352      CALL  _delay_5500us
;lcd.ppas,218 ::
$013F $0838      MOVF  lcd____menable, 0
$0140 $0480      IORWF INDF, 1
;lcd.ppas,219 ::
$0141 $0938      COMF  lcd____menable, W
$0142 $00F0      MOVWF STACK_0
$0143 $0870      MOVF  STACK_0, 0
$0144 $0580      ANDWF INDF, 1
;lcd.ppas,220 ::
$0145 $083A      MOVF  lcd_dataport_address, 0
$0146 $0084      MOVWF FSR
;lcd.ppas,221 ::
$0147 $083E      MOVF  lcd____mdb4, 0
$0148 $0480      IORWF INDF, 1
;lcd.ppas,222 ::
$0149 $0837      MOVF  lcd_ctrlport_address, 0
$014A $0084      MOVWF FSR
;lcd.ppas,223 ::
$014B $0838      MOVF  lcd____menable, 0
$014C $0480      IORWF INDF, 1
;lcd.ppas,224 ::
$014D $0870      MOVF  STACK_0, 0
$014E $0580      ANDWF INDF, 1
;lcd.ppas,226 ::
$014F $2352      CALL  _delay_5500us
;lcd.ppas,229 ::
$0150 $083A      MOVF  lcd_dataport_address, 0
$0151 $0084      MOVWF FSR
;lcd.ppas,230 ::
$0152 $093E      COMF  lcd____mdb4, W
$0153 $00F0      MOVWF STACK_0
$0154 $0870      MOVF  STACK_0, 0
$0155 $0580      ANDWF INDF, 1
;lcd.ppas,231 ::
$0156 $0837      MOVF  lcd_ctrlport_address, 0

```

```

$0157 $0084          MOVWF FSR
;lcd.ppas,232 ::
$0158 $0838          MOVF  lcd____menable, 0
$0159 $0480          IORWF INDF, 1
;lcd.ppas,233 ::
$015A $0938          COMF  lcd____menable, W
$015B $00F1          MOVWF STACK_1
$015C $0871          MOVF  STACK_1, 0
$015D $0580          ANDWF INDF, 1
;lcd.ppas,234 ::
$015E $083A          MOVF  lcd_dataport_address, 0
$015F $0084          MOVWF FSR
;lcd.ppas,235 ::
$0160 $083E          MOVF  lcd____mdb4, 0
$0161 $0400          IORWF INDF, 0
$0162 $00F0          MOVWF STACK_0
$0163 $083D          MOVF  lcd____mdb5, 0
$0164 $04F0          IORWF STACK_0, 1
$0165 $083C          MOVF  lcd____mdb6, 0
$0166 $04F0          IORWF STACK_0, 1
$0167 $083B          MOVF  lcd____mdb7, 0
$0168 $0470          IORWF STACK_0, 0
$0169 $0080          MOVWF INDF
;lcd.ppas,236 ::
$016A $0837          MOVF  lcd_ctrlport_address, 0
$016B $0084          MOVWF FSR
;lcd.ppas,237 ::
$016C $0838          MOVF  lcd____menable, 0
$016D $0480          IORWF INDF, 1
;lcd.ppas,238 ::
$016E $0871          MOVF  STACK_1, 0
$016F $0580          ANDWF INDF, 1
;lcd.ppas,240 ::
$0170 $2352          CALL  _delay_5500us
;lcd.ppas,242 ::
$0171 $3001          MOVLW 1
$0172 $00BF          MOVWF lcd_lcdcmd_status
$0173 $          lcd_L_55:
;lcd.ppas,243 ::
$0173 $0008          RETURN
$0318 $          _usart_init:
;USART_b21.ppas,22 ::
;USART_b21.ppas,24 ::
$0318 $          USART_b21_L_1:
$0318 $1303          BCF   STATUS, RP1
$0319 $1683          BSF   STATUS, RP0
$031A $1698          BSF   TXSTA, 5
$031B $          USART_b21_L_2:
;USART_b21.ppas,25 ::
$031B $3090          MOVLW 144
$031C $1283          BCF   STATUS, RP0
$031D $0098          MOVWF RCSTA
;USART_b21.ppas,31 ::
$031E $          USART_b21_L_3:
$031E $1683          BSF   STATUS, RP0
$031F $1486          BSF   TRISB, 1
$0320 $          USART_b21_L_4:

```

```

;USART_b21.ppas,32 ::
$0320 $1106          BCF    TRISB, 2
$0321 $      USART_b21_L_6:
;USART_b21.ppas,35 ::
$0321 $      USART_b21_L_8:
$0321 $1283          BCF    STATUS, RP0
$0322 $080C          MOVF   PIR1, 0
$0323 $00F1          MOVWF  STACK_1
$0324 $3000          MOVLW 0
$0325 $1AF1          BTFSC  STACK_1, 5
$0326 $3001          MOVLW 1
$0327 $00F1          MOVWF  STACK_1
$0328 $0871          MOVF   STACK_1, 0
$0329 $3A01          XORLW 1
$032A $1903          BTFSC  STATUS, Z
$032B $2B21          GOTO   USART_b21_L_8
;USART_b21.ppas,36 ::
$032C $      USART_b21_L_9:
$032C $      USART_b21_L_0:
;USART_b21.ppas,37 ::
$032C $0008          RETURN
$0393 $      _chien:
;proglcd.ppas,56 ::          begin
;proglcd.ppas,58 ::          CLRWDT; //(remise à zéro)
$0393 $0064          CLRWDT
;proglcd.ppas,59 ::          end;
$0394 $      proglcd_L_11:
;proglcd.ppas,60 ::          end;
$0394 $0008          RETURN
$02E6 $      _lcd_chr:
;lcd.ppas,78 ::
;lcd.ppas,80 ::
$02E6 $      lcd_L_37:
$02E6 $1303          BCF    STATUS, RP1
$02E7 $1283          BCF    STATUS, RP0
$02E8 $0853          MOVF   FARG_Lcd_Chtr, 0
$02E9 $3A01          XORLW 1
$02EA $1D03          BTFSS  STATUS, Z
$02EB $2AEF          GOTO   lcd_L_38
$02EC $      lcd_L_36:
$02EC $3080          MOVLW 128
$02ED $00D3          MOVWF  FARG_Lcd_Chtr
$02EE $2B06          GOTO   lcd_L_35
$02EF $      lcd_L_38:
;lcd.ppas,81 ::
$02EF $      lcd_L_40:
$02EF $0853          MOVF   FARG_Lcd_Chtr, 0
$02F0 $3A02          XORLW 2
$02F1 $1D03          BTFSS  STATUS, Z
$02F2 $2AF6          GOTO   lcd_L_41
$02F3 $      lcd_L_39:
$02F3 $30C0          MOVLW 192
$02F4 $00D3          MOVWF  FARG_Lcd_Chtr
$02F5 $2B06          GOTO   lcd_L_35
$02F6 $      lcd_L_41:
;lcd.ppas,82 ::
$02F6 $      lcd_L_43:

```

```

$02F6 $0853          MOVF  FARG_Lcd_Chr, 0
$02F7 $3A03          XORLW 3
$02F8 $1D03          BTFSS STATUS, Z
$02F9 $2AFD          GOTO  lcd_L_44
$02FA $          lcd_L_42:
$02FA $3094          MOVLW 148
$02FB $00D3          MOVWF FARG_Lcd_Chr
$02FC $2B06          GOTO  lcd_L_35
$02FD $          lcd_L_44:
;lcd.ppas,83 ::
$02FD $          lcd_L_46:
$02FD $0853          MOVF  FARG_Lcd_Chr, 0
$02FE $3A04          XORLW 4
$02FF $1D03          BTFSS STATUS, Z
$0300 $2B04          GOTO  lcd_L_47
$0301 $          lcd_L_45:
$0301 $30D4          MOVLW 212
$0302 $00D3          MOVWF FARG_Lcd_Chr
;lcd.ppas,84 ::
$0303 $2B06          GOTO  lcd_L_35
$0304 $          lcd_L_47:
$0304 $3080          MOVLW 128
$0305 $00D3          MOVWF FARG_Lcd_Chr
$0306 $          lcd_L_35:
;lcd.ppas,86 ::
$0306 $0854          MOVF  FARG_Lcd_Chr+1, 0
$0307 $0753          ADDWF FARG_Lcd_Chr, 0
$0308 $00F0          MOVWF STACK_0
$0309 $03F0          DECF  STACK_0, 1
$030A $0870          MOVF  STACK_0, 0
$030B $00D3          MOVWF FARG_Lcd_Chr
;lcd.ppas,88 ::
$030C $3001          MOVLW 1
$030D $00BF          MOVWF lcd_lcdcmd_status
;lcd.ppas,89 ::
$030E $0870          MOVF  STACK_0, 0
$030F $00DC          MOVWF FARG_Lcd_Cmd
$0310 $2174          CALL  _lcd_cmd
;lcd.ppas,91 ::
$0311 $01BF          CLRF  lcd_lcdcmd_status, 1
;lcd.ppas,92 ::
$0312 $0855          MOVF  FARG_Lcd_Chr+2, 0
$0313 $00DC          MOVWF FARG_Lcd_Cmd
$0314 $2174          CALL  _lcd_cmd
;lcd.ppas,93 ::
$0315 $3001          MOVLW 1
$0316 $00BF          MOVWF lcd_lcdcmd_status
$0317 $          lcd_L_34:
;lcd.ppas,94 ::
$0317 $0008          RETURN
$02A9 $          _init:
;proglcd.ppas,26 ::          begin
;proglcd.ppas,27 ::          option_reg.ps1:=0;
$02A9 $1303          BCF  STATUS, RP1
$02AA $1683          BSF  STATUS, RP0
$02AB $1081          BCF  OPTION_REG, 1
$02AC $          proglcd_L_2:

```

```

;proglcd.ppas,28 ::          trame_recu:=0;
$02AC $1283                BCF   STATUS, RP0
$02AD $01B6                CLRF  _trame_recu, 1
;proglcd.ppas,29 ::          trame_correcte:=0;
$02AE $01C1                CLRF  _trame_correcte, 1
;proglcd.ppas,30 ::          PIE1:=$20;
$02AF $3020                MOVLW 32
$02B0 $1683                BSF   STATUS, RP0
$02B1 $008C                MOVWF PIE1
;proglcd.ppas,31 ::          PIR1:=0;
$02B2 $1283                BCF   STATUS, RP0
$02B3 $018C                CLRF  PIR1, 1
;proglcd.ppas,32 ::          intcon:=$C0;
$02B4 $30C0                MOVLW 192
$02B5 $008B                MOVWF INTCON
;proglcd.ppas,33 ::          cmcon:=$07;
$02B6 $3007                MOVLW 7
$02B7 $009F                MOVWF CMCON
;proglcd.ppas,34 ::          usart_init(50000);
$02B8 $3004                MOVLW 4
$02B9 $1683                BSF   STATUS, RP0
$02BA $0099                MOVWF SPBRG
$02BB $1518                BSF   TXSTA, BRGH
$02BC $2318                CALL  _usart_init
;proglcd.ppas,35 ::          lcd_config(portb,6,5,7,4,porta,2,1,0);
$02BD $3006                MOVLW PORTB
$02BE $00D3                MOVWF FARG_Lcd_Config
$02BF $3006                MOVLW 6
$02C0 $00D4                MOVWF FARG_Lcd_Config+1
$02C1 $3005                MOVLW 5
$02C2 $00D5                MOVWF FARG_Lcd_Config+2
$02C3 $3007                MOVLW 7
$02C4 $00D6                MOVWF FARG_Lcd_Config+3
$02C5 $3004                MOVLW 4
$02C6 $00D7                MOVWF FARG_Lcd_Config+4
$02C7 $3005                MOVLW PORTA
$02C8 $00D8                MOVWF FARG_Lcd_Config+5
$02C9 $3002                MOVLW 2
$02CA $00D9                MOVWF FARG_Lcd_Config+6
$02CB $3001                MOVLW 1
$02CC $00DA                MOVWF FARG_Lcd_Config+7
$02CD $01DB                CLRF  FARG_Lcd_Config+8, 1
$02CE $2055                CALL  _lcd_config
;proglcd.ppas,36 ::          lcd_cmd(lcd_cursor_off);
$02CF $300C                MOVLW 12
$02D0 $00DC                MOVWF FARG_Lcd_Cmd
$02D1 $2174                CALL  _lcd_cmd
;proglcd.ppas,37 ::          lcd_cmd(lcd_clear);
$02D2 $3001                MOVLW 1
$02D3 $00DC                MOVWF FARG_Lcd_Cmd
$02D4 $2174                CALL  _lcd_cmd
;proglcd.ppas,38 ::          delay_ms(100);
$02D5 $3082                MOVLW 130
$02D6 $00FB                MOVWF STACK_11
$02D7 $30FF                MOVLW 255
$02D8 $00FA                MOVWF STACK_10
$02D9 $0BFB                DECFSZ      STACK_11, F

```

```

$02DA $2ADC          GOTO  $+2
$02DB $2ADF          GOTO  $+4
$02DC $0BFA          DECFSZ      STACK_10, F
$02DD $2ADC          GOTO  $-1
$02DE $2AD9          GOTO  $-5
$02DF $3087          MOVLW 135
$02E0 $00FA          MOVWF  STACK_10
$02E1 $0BFA          DECFSZ      STACK_10, F
$02E2 $2AE1          GOTO  $-1
$02E3 $0000          NOP
$02E4 $0064          CLRWDT
;proglcd.ppas,39 ::      asm CLRWDT; end;
$02E5 $      proglcd_L_0:
;proglcd.ppas,40 ::      end;
$02E5 $0008          RETURN
$021B $      _execution:
;proglcd.ppas,63 ::      begin
;proglcd.ppas,64 ::      trame_recu:=0;
$021B $1303          BCF    STATUS, RP1
$021C $1283          BCF    STATUS, RP0
$021D $01B6          CLRF   _trame_recu, 1
;proglcd.ppas,65 ::      chien;
$021E $2393          CALL   _chien
;proglcd.ppas,66 ::      if data[0]= 'O' then lcd_cmd(lcd_cursor_off);
$021F $0820          MOVF   _data+0, 0
$0220 $3A4F          XORLW 79
$0221 $1D03          BTFSS STATUS, Z
$0222 $2A26          GOTO   proglcd_L_14
$0223 $      proglcd_L_13:
$0223 $300C          MOVLW 12
$0224 $00DC          MOVWF  FARG_Lcd_Cmd
$0225 $2174          CALL   _lcd_cmd
$0226 $      proglcd_L_14:
$0226 $      proglcd_L_15:
;proglcd.ppas,67 ::      if data[0]= 'C' then
lcd_cmd(LCD_BLINK_CURSOR_ON);
$0226 $0820          MOVF   _data+0, 0
$0227 $3A43          XORLW 67
$0228 $1D03          BTFSS STATUS, Z
$0229 $2A2D          GOTO   proglcd_L_17
$022A $      proglcd_L_16:
$022A $300F          MOVLW 15
$022B $00DC          MOVWF  FARG_Lcd_Cmd
$022C $2174          CALL   _lcd_cmd
$022D $      proglcd_L_17:
$022D $      proglcd_L_18:
;proglcd.ppas,68 ::      if data[0]= 'N' then lcd_cmd(lcd_clear);
$022D $0820          MOVF   _data+0, 0
$022E $3A4E          XORLW 78
$022F $1D03          BTFSS STATUS, Z
$0230 $2A34          GOTO   proglcd_L_20
$0231 $      proglcd_L_19:
$0231 $3001          MOVLW 1
$0232 $00DC          MOVWF  FARG_Lcd_Cmd
$0233 $2174          CALL   _lcd_cmd
$0234 $      proglcd_L_20:
$0234 $      proglcd_L_21:

```

```

;proglcd.ppas,69 ::      x:= data[1]-48;
$0234 $3030             MOVLW 48
$0235 $0221             SUBWF _data+1, 0
$0236 $00C2             MOVWF _x
;proglcd.ppas,70 ::      y:= data[2]-48;
$0237 $3030             MOVLW 48
$0238 $0222             SUBWF _data+2, 0
$0239 $00C3             MOVWF _y
;proglcd.ppas,71 ::      chien;
$023A $2393             CALL _chien
;proglcd.ppas,72 ::      for j:=3 to i+1 do
$023B $3003             MOVLW 3
$023C $00B5             MOVWF _j
$023D $      proglcd_L_22:
$023D $0A34             INCF _i, 0
$023E $00D2             MOVWF FLOC_execution+0
$023F $0835             MOVF _j, 0
$0240 $0252             SUBWF FLOC_execution+0, 0
$0241 $1C03             BTFSS STATUS, C
$0242 $2A65             GOTO proglcd_L_25
;proglcd.ppas,74 ::      if (data[j+1] <> '_') then lcd_chr(x,y+j-
3,data[j]);
$0243 $3020             MOVLW _data
$0244 $00F1             MOVWF STACK_1
$0245 $0A35             INCF _j, 0
$0246 $00F0             MOVWF STACK_0
$0247 $0870             MOVF STACK_0, 0
$0248 $0771             ADDWF STACK_1, 0
$0249 $0084             MOVWF FSR
$024A $0800             MOVF INDF, 0
$024B $00F1             MOVWF STACK_1
$024C $0871             MOVF STACK_1, 0
$024D $3A5F             XORLW 95
$024E $1903             BTFSC STATUS, Z
$024F $2A5F             GOTO proglcd_L_27
$0250 $      proglcd_L_26:
$0250 $0842             MOVF _x, 0
$0251 $00D3             MOVWF FARG_Lcd_Chr
$0252 $0835             MOVF _j, 0
$0253 $0743             ADDWF _y, 0
$0254 $00D4             MOVWF FARG_Lcd_Chr+1
$0255 $3003             MOVLW 3
$0256 $02D4             SUBWF FARG_Lcd_Chr+1, 1
$0257 $3020             MOVLW _data
$0258 $00F0             MOVWF STACK_0
$0259 $0835             MOVF _j, 0
$025A $0770             ADDWF STACK_0, 0
$025B $0084             MOVWF FSR
$025C $0800             MOVF INDF, 0
$025D $00D5             MOVWF FARG_Lcd_Chr+2
$025E $22E6             CALL _lcd_chr
$025F $      proglcd_L_27:
$025F $      proglcd_L_28:
;proglcd.ppas,75 ::      end;
$025F $0835             MOVF _j, 0
$0260 $0652             XORWF FLOC_execution+0, 0
$0261 $1903             BTFSC STATUS, Z

```

```

$0262 $2A65          GOTO  proglcd_L_25
$0263 $          proglcd_L_23:
$0263 $0AB5         INCF  _j, 1
$0264 $2A3D         GOTO  proglcd_L_22
$0265 $          proglcd_L_25:
$0265 $          proglcd_L_12:
;proglcd.ppas,78 ::          end;
$0265 $0008         RETURN
$0381 $          _main:
;proglcd.ppas,79 ::          begin
;proglcd.ppas,80 ::          chien;
$0381 $2393         CALL  _chien
;proglcd.ppas,81 ::          init;
$0382 $22A9         CALL  _init
;proglcd.ppas,83 ::          while true do
$0383 $          proglcd_L_31:
;proglcd.ppas,85 ::          chien;
$0383 $2393         CALL  _chien
;proglcd.ppas,86 ::          if trame_recu = 1 then execution;
$0384 $0836         MOVF  _trame_recu, 0
$0385 $3A01         XORLW 1
$0386 $1D03         BTFSS STATUS, Z
$0387 $2B89         GOTO  proglcd_L_36
$0388 $          proglcd_L_35:
$0388 $221B         CALL  _execution
$0389 $          proglcd_L_36:
$0389 $          proglcd_L_37:
;proglcd.ppas,87 ::          end;
$0389 $2B83         GOTO  proglcd_L_31
;proglcd.ppas,88 ::          end.
$038A $2B8A         GOTO  $

```