

SKM50GB12V

Target Data

Features

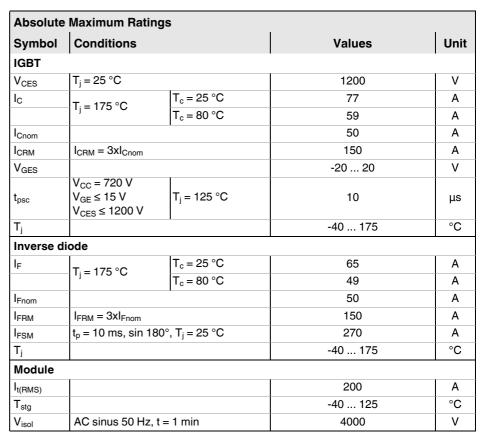
- V-IGBT = 6. Generation Trench V-IGBT (Fuji)
- CAL4 = Soft switching 4. Generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Copper Bonding)
- Increased power cycling capability
- · With integrated gate resistor
- UL recognized, file no. E63532
- Lowest switching losses at High di/dt

Typical Applications*

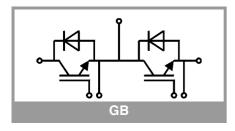
- AC inverter drives
- UPS
- · Electronic welders

Remarks

- Case temperature limited to T_c = 125°C
- Recommended T_{op} = -40 ... +150°C
- Product reliability results valid for T_j = 150°C



Characte	eristics					
Symbol	Conditions	min.	typ.	max.	Unit	
IGBT						
V _{CE(sat)}	$I_{\rm C} = 50 {\rm A}$	T _j = 25 °C		1.84	2.29	V
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.18	2.63	V
V _{CE0}	chiplevel	T _j = 25 °C		0.94	1.04	V
		T _j = 150 °C		0.88	0.98	V
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		18	25	mΩ
		T _j = 150 °C		26	33	mΩ
$V_{GE(th)}$	$V_{GE}=V_{CE}$, $I_{C}=1.7$ mA		5.5	6	6.5	V
I _{CES}	V _{GE} = 0 V V _{CE} = 1200 V	T _j = 25 °C			0.3	mA
		T _j = 150 °C		-		mA
C _{ies}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		3.0		nF
Coes		f = 1 MHz		0.30		nF
C _{res}		f = 1 MHz		0.30		nF
Q_G	V _{GE} = - 8 V+ 15 V			550		nC
R _{Gint}	T _j = 25 °C			4.0		Ω
t _{d(on)}	$V_{CC} = 600 \text{ V}$ $I_{C} = 50 \text{ A}$ $V_{GE} = +15/-15 \text{ V}$ $R_{G \text{ on}} = 13 \Omega$ $R_{G \text{ off}} = 13 \Omega$	T _j = 150 °C		276		ns
t _r		T _j = 150 °C		35		ns
E _{on}		T _j = 150 °C		4.9		mJ
t _{d(off)}		T _j = 150 °C		403		ns
t _f		T _j = 150 °C		62		ns
E _{off}		T _j = 150 °C		4.5		mJ
R _{th(j-c)}	per IGBT			0.53	K/W	





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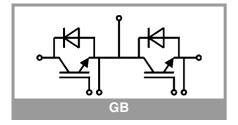
Typical Applications*

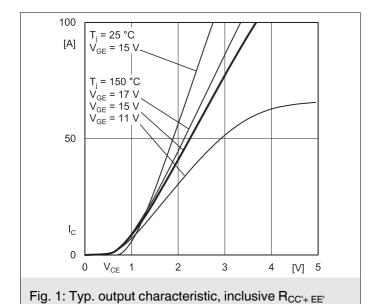
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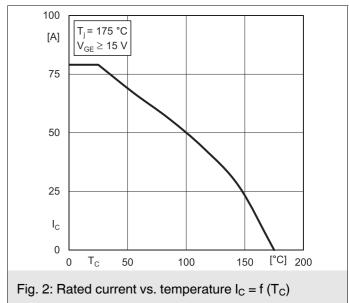
Remarks

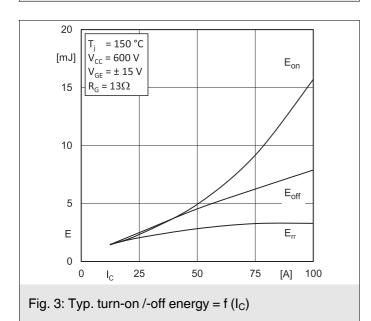
- Case temperature limited to T_c = 125°C max.
- Recommended $T_{op} = -40 \dots +150$ °C
- Product reliability results valid for $T_j = 150$ °C

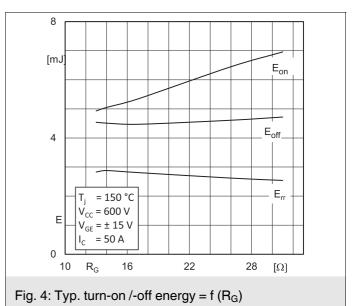
Characteristics											
Symbol	Conditions		min.	typ.	max.	Unit					
Inverse diode											
$V_F = V_{EC}$	I _F = 50 A V _{GE} = 0 V chiplevel	T _j = 25 °C		2.22	2.54	V					
		T _j = 150 °C		2.18	2.50	V					
V_{F0}	chiplevel	T _j = 25 °C		1.30	1.50	V					
		T _j = 150 °C		0.90	1.10	V					
r _F	chiplevel	T _j = 25 °C		18	21	mΩ					
		T _j = 150 °C		26	28	mΩ					
I _{RRM}	$I_F = 50 \text{ A}$	T _j = 150 °C		35		Α					
Q _{rr}	$di/dt_{off} = 1380 A/\mu s$ $V_{GE} = \pm 15 V$	T _j = 150 °C		8.7		μC					
E _{rr}	V _{CC} = 600 V	T _j = 150 °C		2.8		mJ					
R _{th(j-c)}	per diode				0.84	K/W					
Module											
L _{CE}				30		nΗ					
R _{CC'+EE'}	measured per switch	T _C = 25 °C		0.65		mΩ					
		T _C = 125 °C		1.09		mΩ					
R _{th(c-s)}	per module			0.04	0.05	K/W					
Ms	to heat sink M6		3		5	Nm					
Mt		to terminals M5	2.5		5	Nm					
						Nm					
W					160	g					

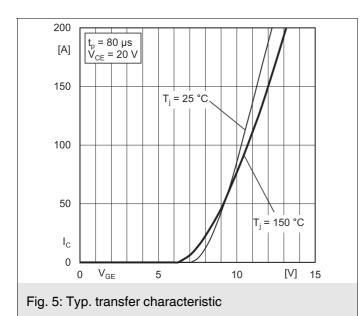




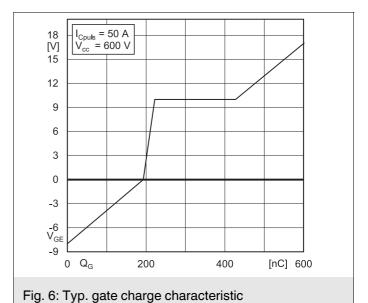




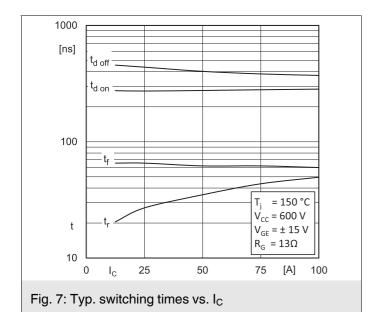




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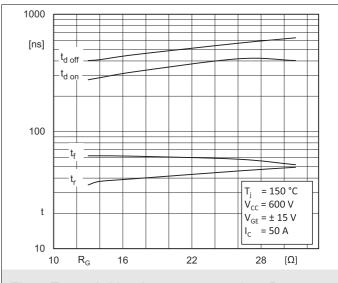


Fig. 8: Typ. switching times vs. gate resistor R_G

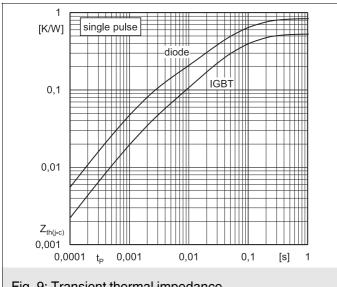


Fig. 9: Transient thermal impedance

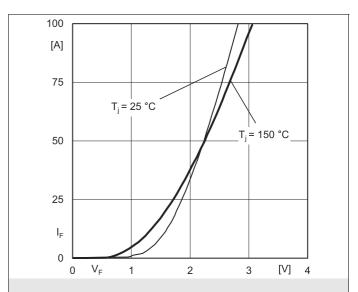
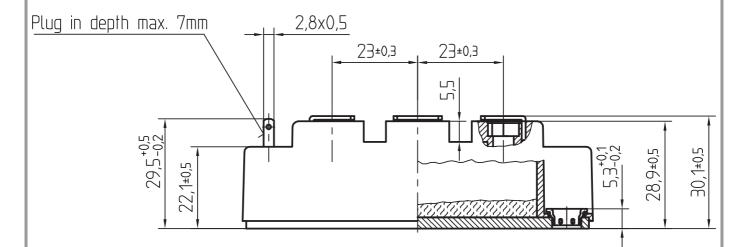
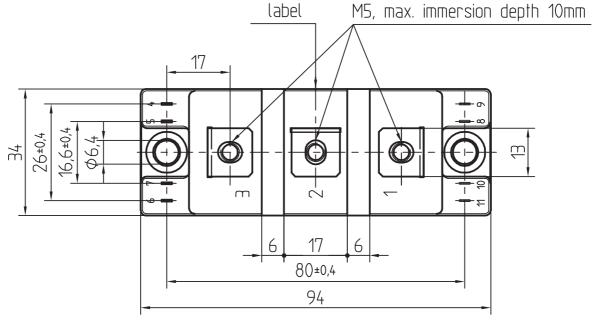


Fig. 10: Typ. CAL diode forward charact., incl. R_{CC'+ EE'}

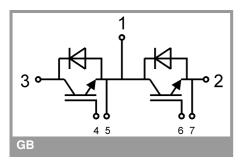






General tolerance +/- 0,5 mm

SEMITRANS 2



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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