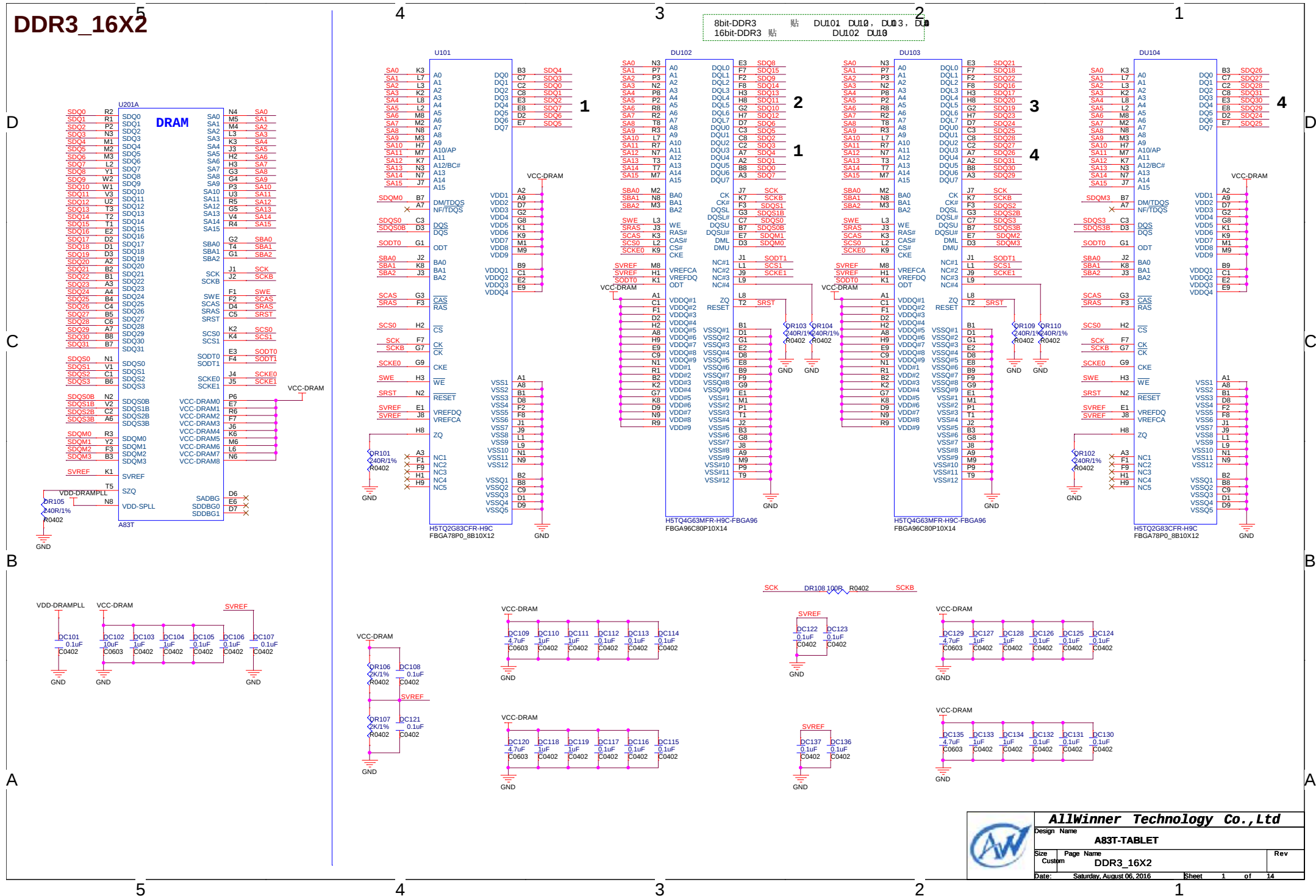
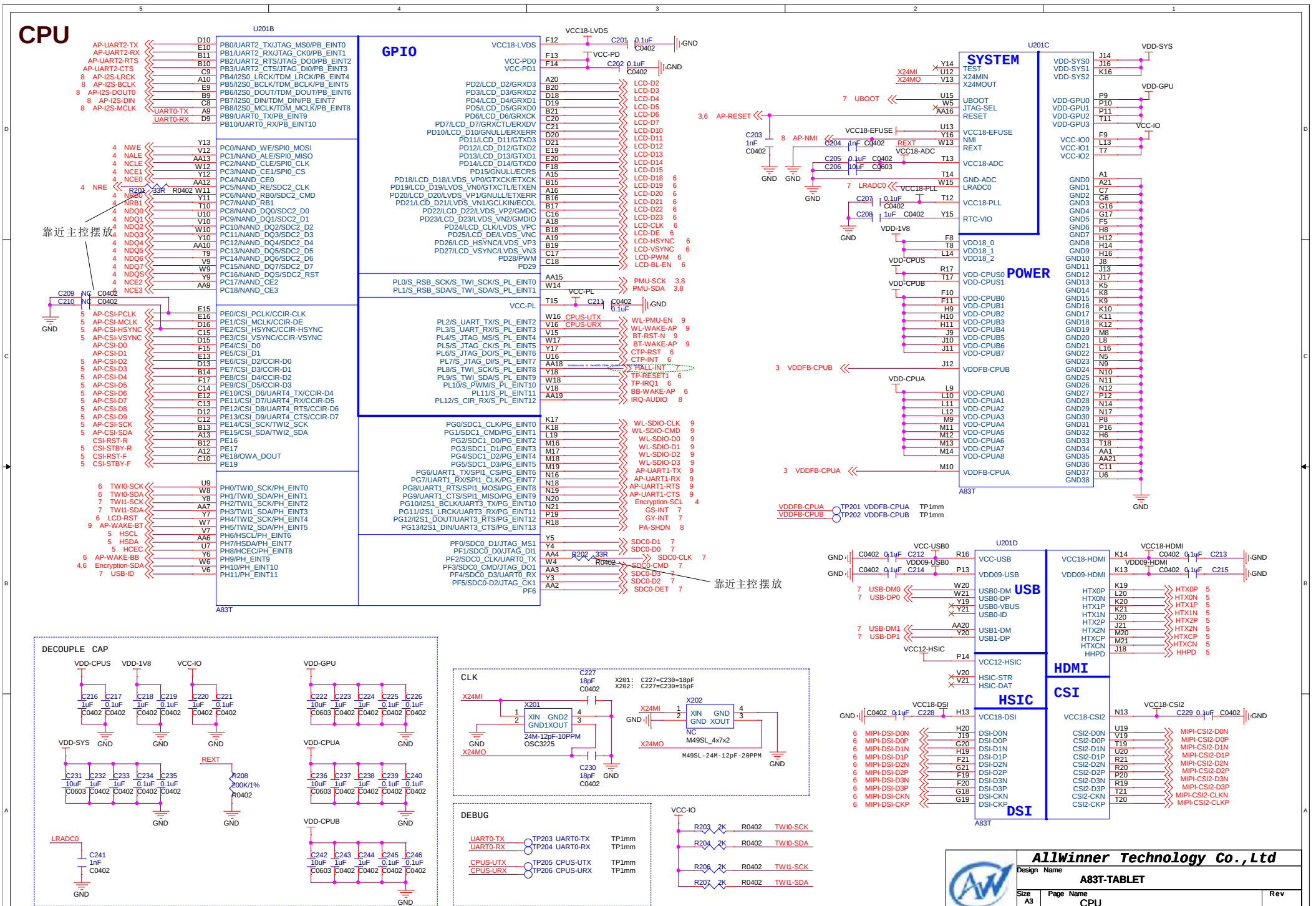


DDR3_16X2

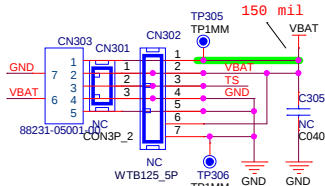


CPU

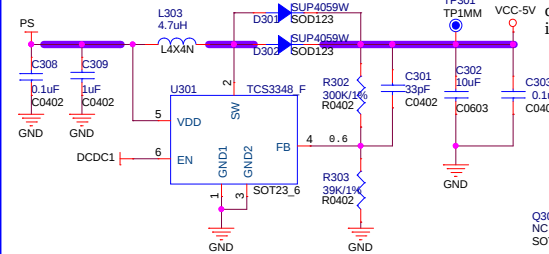


BAT

IF use the battery temperature sensor: A=NC
otherwise: A=10k

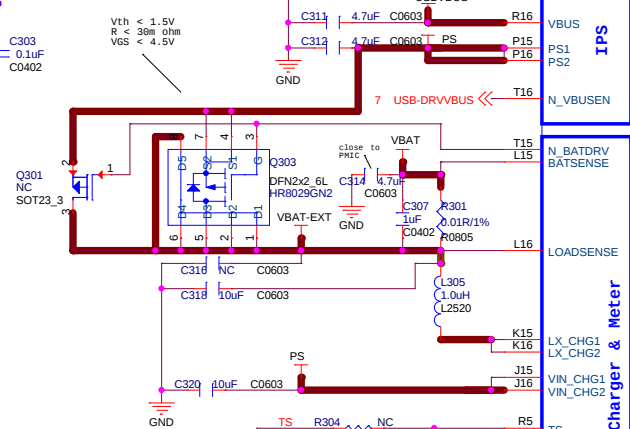


PS-5V

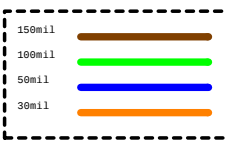
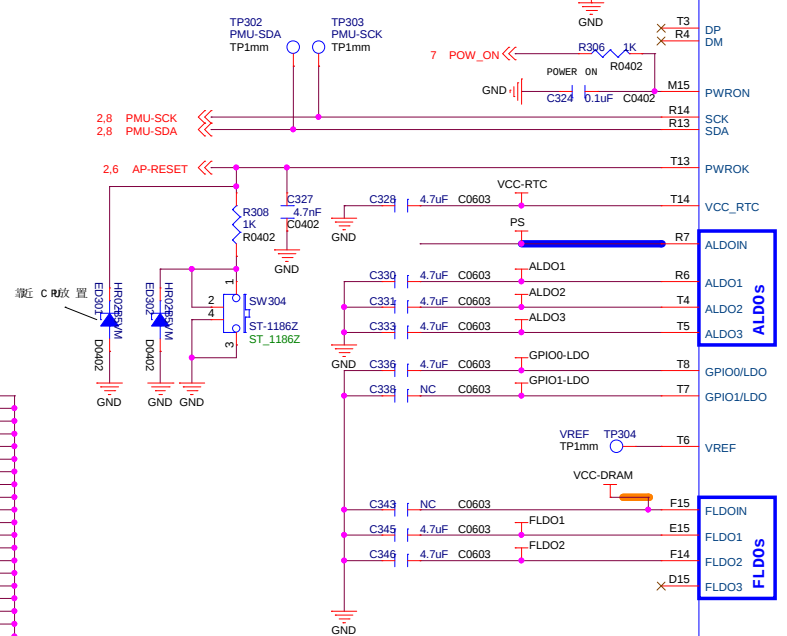
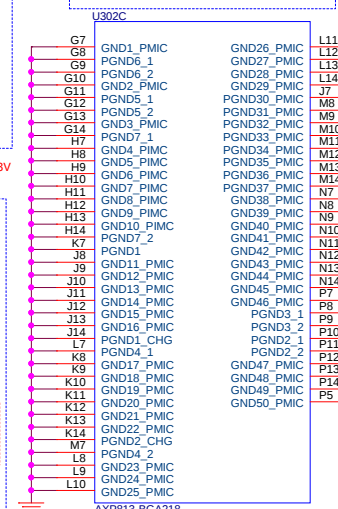
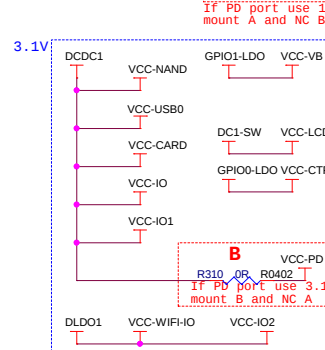
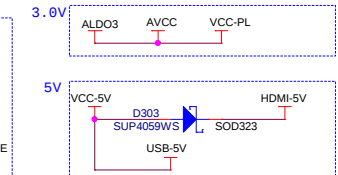
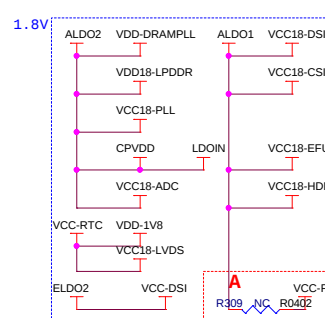
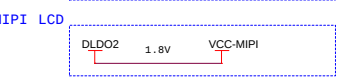
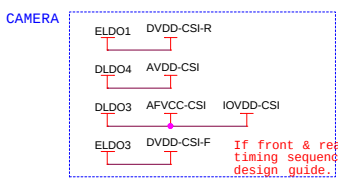
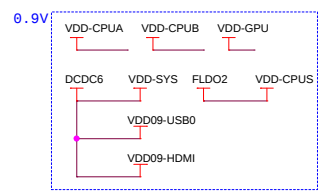
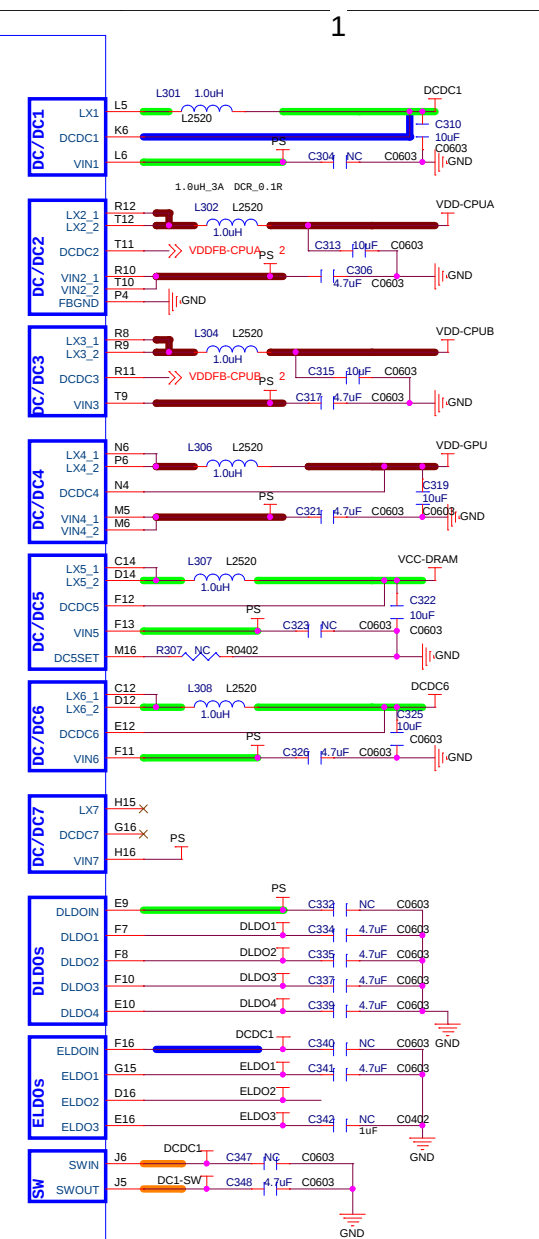


PMIC

connect the USBVBUS to DCIN
if use usb power only

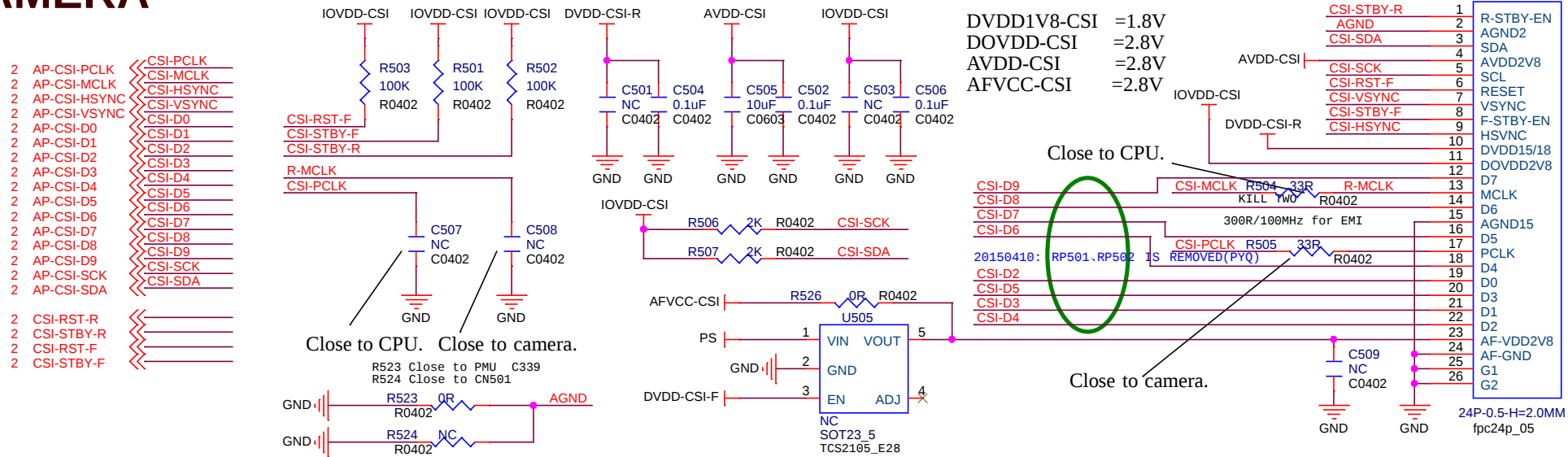


Charger & Meter

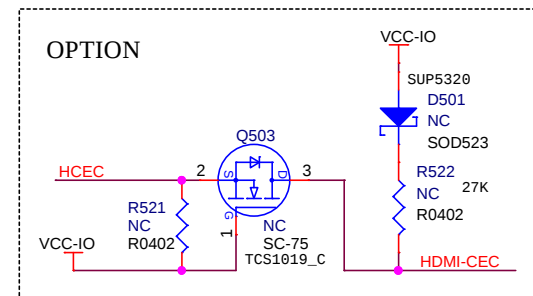
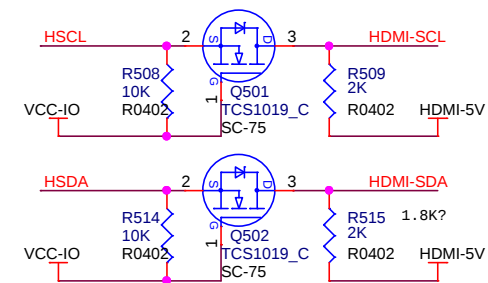
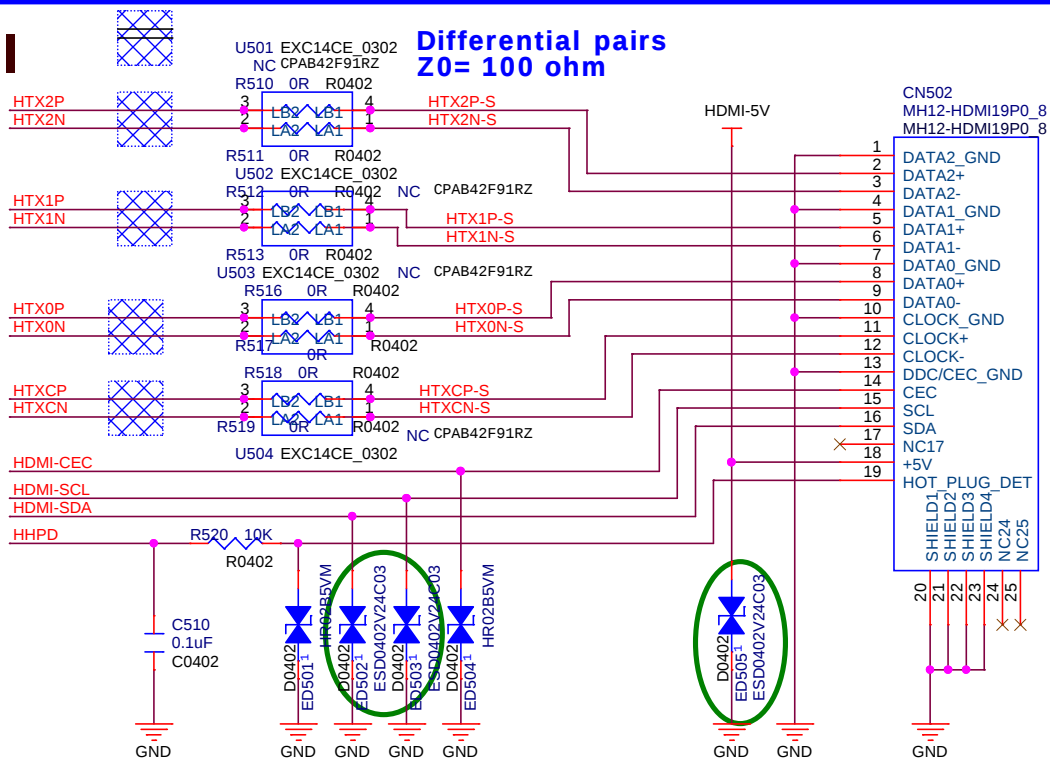


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CAMERA



HDMI



AllWinner Technology Co.,Ltd

Design Name

A83T-TABLET

Size	
Custom	

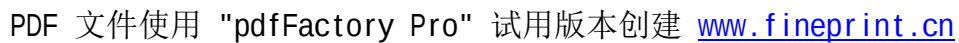
Name

CAMERA-HDMI

Rev

Date: Saturday, August 06, 2016

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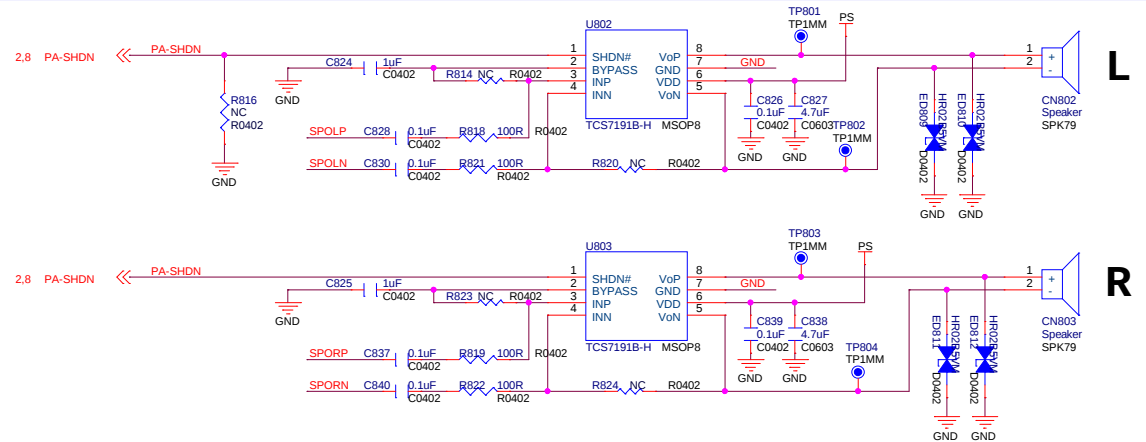


[illegible]

3 HP_JACK

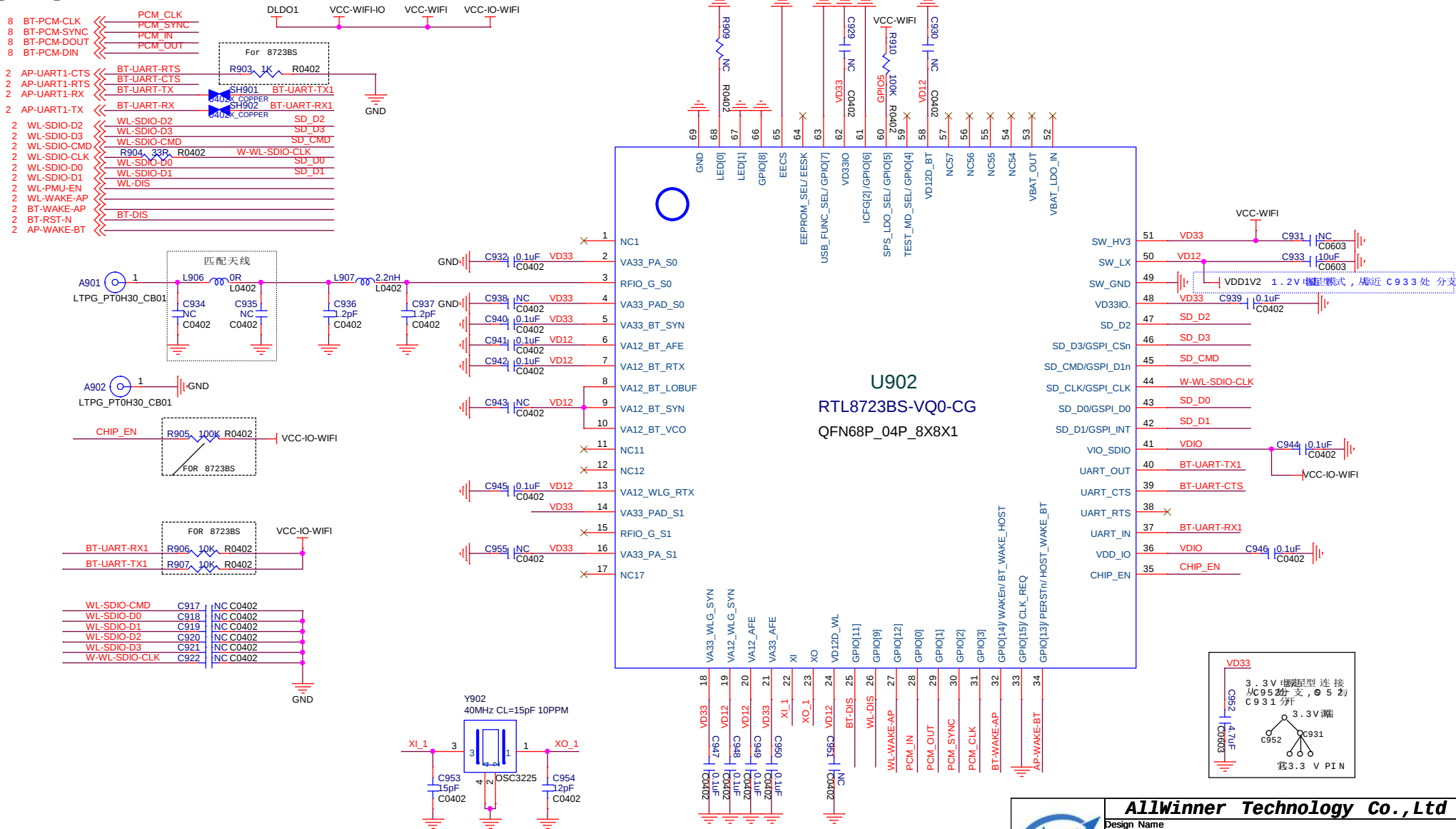
Differential Pair

HP_JACK



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	CODEC		
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SDIO WIFI+BT



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Custom	WIFI+BT		
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MODEM

5

4

3

2

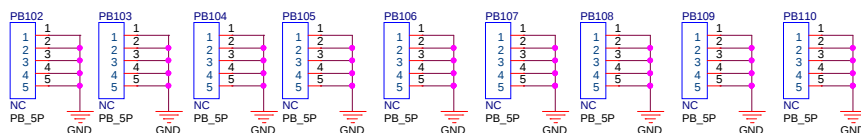
1

8 BB-PCM-CLK << BB-PCM-CLK
8 BB-PCM-SYNC << BB-PCM-SYNC
8 BB-PCM-DOUT << BB-PCM-DOUT
8 BB-PCM-DIN << BB-PCM-DIN

2 AP-UART2-CTS << BB-UART-RTS
2 AP-UART2-RTS << BB-UART-CTS
2 AP-UART2-RX << BB-UART-TX
2 AP-UART2-TX << BB-UART-RX

2,6 BB-WAKE-AP << BB-WAKE-AP
2,6 AP-WAKE-BB << AP-WAKE-BB

8 PHOUTP << MIC-P
8 PHOUTN << MIC-N
8 PHINP << EAR-P
8 PHINN << EAR-N



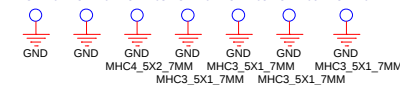
MARK POINT

MARK101 MARK102 MARK103 MARK104 MARK105 MARK106

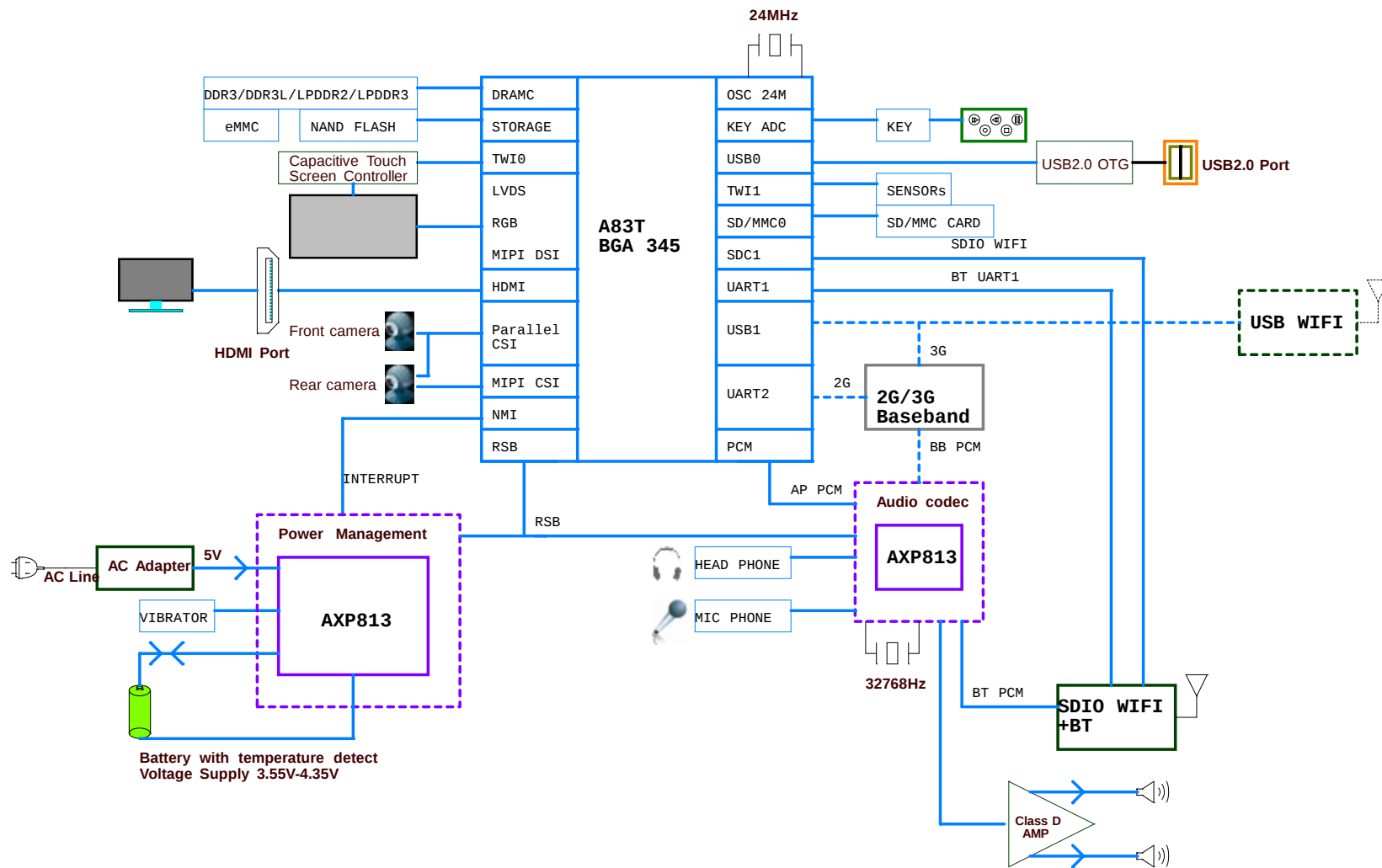


HOLE

HOLE101 HOLE102 HOLE103 HOLE104 HOLE105 HOLE106 HOLE107



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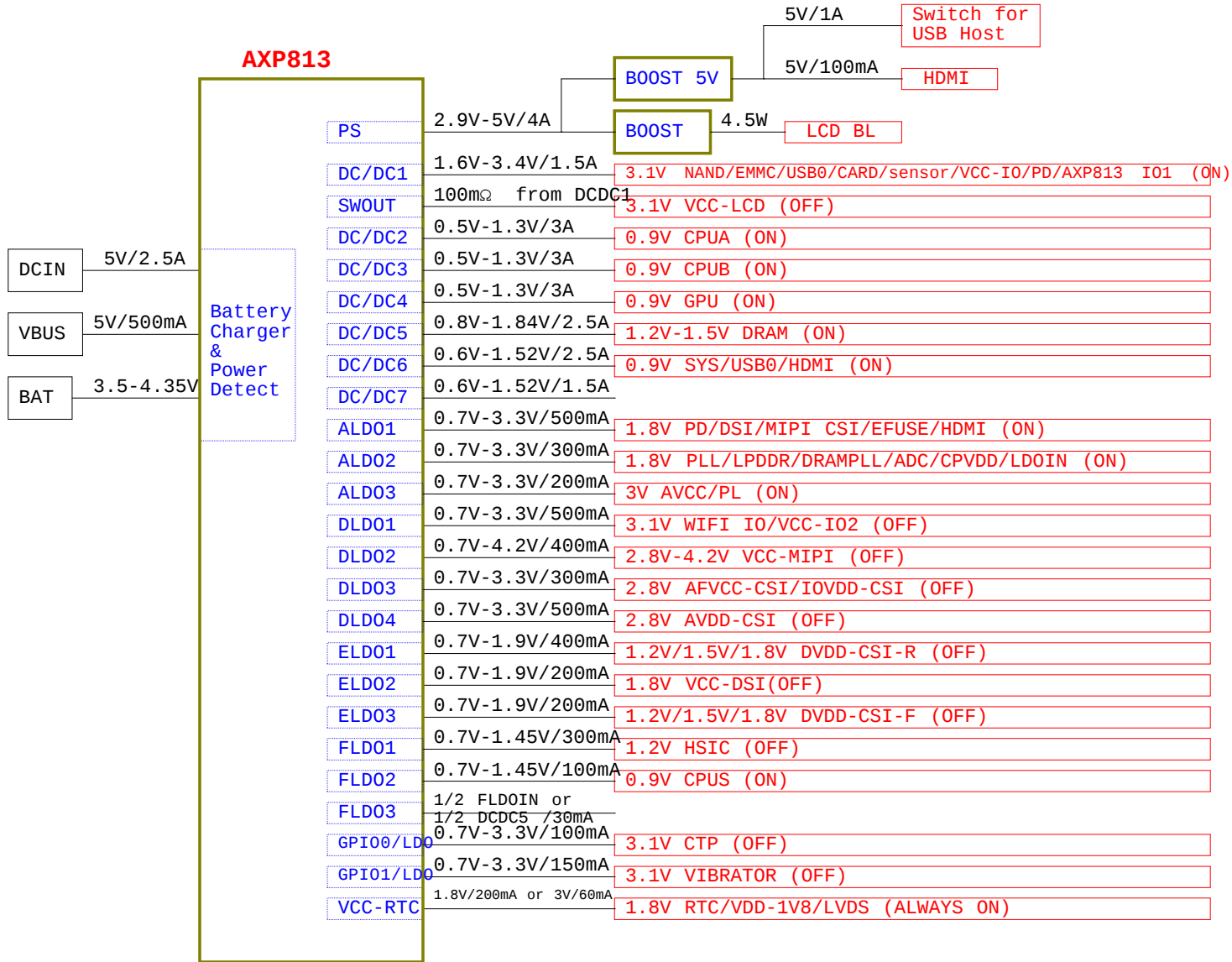
POWER TREE

D

C

B

A



D

C

B

A

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GPIO ASSIGNMENT

PIN	Define	CFG	Function
PB0	UART2-TX	2	MODEM
PB1	UART2-RX	2	
PB2	UART2-RTS	2	
PB3	UART2-CTS	2	CODEC
PB4	I2S-LRCK	3	
PB5	I2S-BCLK	3	
PB6	I2S-DOUT	3	DEBUG
PB7	I2S-DIN	3	
PB8	I2S-MCLK	3	
PB9	UART0-TX	2	
PB10	UART0-RX	2	

PIN	Define	CFG	Function
PC0	NWE	2	NAND /eMMC
PC1	NALE	2	
PC2	NCLK	2	
PC3	NCE1	2	
PC4	NCE0	2	
PC5	NRE/CLK	2/3	
PC6	NRB0/CMD	2/3	
PC7	NRB1	2	
PC8	NDQ0/D0	2/3	
PC9	NDQ1/D1	2/3	
PC10	NDQ2/D2	2/3	
PC11	NDQ3/D3	2/3	
PC12	NDQ4/D4	2/3	
PC13	NDQ5/D5	2/3	
PC14	NDQ6/D6	2/3	
PC15	NDQ7/D7	2/3	
PC16	NDQS/RST	2/3	
PC17	NCE2	2	
PC18	NCE3	2	

PIN	Define	CFG	Function
PD2	D2	2	LCD /LVDS
PD3	D3	2	
PD4	D4	2	
PD5	D5	2	
PD6	D6	2	
PD7	D7	2	
PD10	D10	2	
PD11	D11	2	
PD12	D12	2	
PD13	D13	2	
PD14	D14	2	
PD15	D15	2	
PD18	D18/VP0	2/3	
PD19	D19/VN0	2/3	
PD20	D20/VP1	2/3	
PD21	D21/VN1	2/3	
PD22	D22/VP2	2/3	
PD23	D23/VN2	2/3	
PD24	CLK/VPC	2/3	
PD25	DE/VNC	2/3	
PD26	HSYNC/VP2	2/3	
PD27	VSYSN/VN3	2/3	
PD28	PWM	2	
PD29	LCD-BL-EN1		

PIN	Define	CFG	Function
PE0	PCLK	2	CSI
PE1	MCLK	2	
PE2	HSYNC	2	
PE3	VSYSN	2	
PE4	D0	2	
PE5	D1	2	
PE6	D2	2	
PE7	D3	2	
PE8	D4	2	
PE9	D5	2	
PE10	D6	2	
PE11	D7	2	
PE12	D8	2	
PE13	D9	2	
PE14	CSI-SCK	2	
PE15	CSI-SDA	2	
PE16	RST-R	1	
PE17	STBY-R	1	
PE18	RST-F	1	
PE19	STBY-F	1	

PIN	Define	CFG	Function
PF0	D1	2	CARD0
PF1	D0	2	
PF2	CLK	2	
PF3	CMD	2	
PF4	D3	2	
PF5	D2	2	
PF6	SDC0-DET	0	

PIN	Define	CFG	Function
PG0	WL-SDIO-CLK	2	WIFI+BT
PG1	WL-SDIO-CMD	2	
PG2	WL-SDIO-D0	2	
PG3	WL-SDIO-D1	2	
PG4	WL-SDIO-D2	2	
PG5	WL-SDIO-D3	2	
PG6	BT-UART-RX	2	
PG7	BT-UART-TX	2	
PG8	BT-UART-CTS	2	
PG9	BT-UART-RTS	2	
PG10			
PG11	GS-INT	6	SENSOR
PG12	LS-INT	6	
PG13	PA-SHDN	1	CODEC

PIN	Define	CFG	Function
PH0	TWI0-SCK	2	CTP
PH1	TWI0-SDA	2	
PH2	TWI1-SCK	2	SENSOR
PH3	TWI1-SDA	2	
PH4	LCD-RST	1	LCD
PH5	AP-WAKE-BT1		BT
PH6	HSCL	2	HDMI
PH7	HSDA	2	
PH8	HCEC	2	
PH9	AP-WAKE-BB1		MODEM
PH10			
PH11	USB-ID	1	USB

PIN	Define	CFG	Function
PL0	RSB-SCK	2	PMIC
PL1	RSB-SDA	2	
PL2	WL-PMU-EN	1	WIFI+BT
PL3	WL-WAKE-AP	0	
PL4	BT-RST-N	1	
PL5	BT-WAKE-AP	0	CTP
PL6	CTP-RST	1	
PL7	CTP-INT	6	
PL8	BB-PWREN	1	MODEM
PL9	BB-PWR-BAT	1	
PL10	BB-RST	1	
PL11	BB-WAKE-AP	0	
PL12	IRQ-AUDIO	6	CODEC

AXP813

PIN	Define	Function
GPI00		
GPI01		

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P12: CODEC
P13: WIFI+BT
P14: MODEM 2G
- OPTION: DDR3 8X4
OPTION: LPDDR2_32X1
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OPTION: MODEM 3G

Revision	Description	Date
V1.0	Releas version	2014-10-13

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